1. Draw and explain the internal architecture of 8085A microprocessor. (10 marks)
2. What is the limitation of Von-Neumann architecture? How to overcome this problem? Explain with necessary diagram. (5 marks)
3. Explain with examples about the various types of addressing modes of 8085A microprocessor.
4. What do you mean by subroutine? Write an assembly language programming to subtract the contents of memory locations 4150H from 4151H and place the result in the memory locations 4152Hand 4153H (8-bit subtraction). (5 marks)
5. What do you mean by system bus? Draw the computer architecture showing various types of bus.
6. What is Stored Program Concept? Write short notes about memory hierarchy with necessary diagram. (5 marks)

Multiple choice question. (one mark each)

1. 8085 has 6 sign flags.

A. True

B. False

2. To multiply a number by 8 in 8085 we have to use RAL instruction

A. once

B. twice

C. thrice

D. four times

3. The size of cache memory in most microcomputers is about

A. a few bytes

B. a few kilobytes

C. a few megabytes

D. a few gigabytes

4. Temporary register in 8085 is a 16-bit register.

A. True

B. False

5. A 256 x 4 EPROM has

A. 8 address pins and 4 data pins

B. 8 address pins and 8 data pins

C. 4 address pins and 8 data pins

D. 4 address pins and 4 data pins