
RomWBW Architecture

N8VEM Project
RomWBW Version 2.6

*** DRAFT ***

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Overview

RomWBW provides a complete firmware package for all of the Z80-based systems that are available in the N8VEM Community (see [http:// http://n8vem-sbc.pbworks.com](http://n8vem-sbc.pbworks.com)). Each of these systems provides for a fairly large ROM memory (typically, 512KB or more). RomWBW allows you to configure and build appropriate contents for such a ROM.

Typically, a computer will contain a small ROM that contains the BIOS (Basic Input/Output System) functions as well as code to start the system by booting an operating system from a disk. Since the N8VEM Projects provide a large ROM space, RomWBW provides a much more comprehensive software package. In fact, it is entirely possible to run a fully functioning N8VEM System with nothing but the ROM.

RomWBW firmware includes:

- System startup code (bootstrap)
- A basic system/debug monitor
- HBIOS (Hardware BIOS) providing support for the vast majority of N8VEM I/O components
- A complete operating system (either CP/M 2.2 or ZSDOS 1.1)
- A built-in CP/M filesystem containing the basic applications and utilities for the operating system and hardware being used

It is appropriate to note that much of the code and components that make up a complete RomWBW package are derived from pre-existing work. Most notably, the imbedded operating system is simply a ROM-based copy of generic CP/M or ZSDOS. Much of the hardware support code was originally produced by other members of the N8VEM community.

The remainder of this document will focus on the HBIOS portion of the ROM. HBIOS contains the vast majority of the custom-developed code for the N8VEM hardware platforms. It provides a formal, structured interface that allows the operating system to be hosted with relative ease.

Background

The Z80 CPU architecture has a limited, 64K address range. In general, this address space must accommodate a running application, disk operating system, and hardware support code.

All N8VEM Z80 CPU platforms provide a physical address space that is much larger than the CPU address space (typically 512K or 1MB). This additional memory can be made available to the CPU using a technique called bank switching. To achieve this, the physical memory is divided up into chunks (banks), typically 32K each. A designated area of the CPU's 64K address space is then reserved to "map" any of the physical memory chunks. You can think of this as a window that can be adjusted to view portions of the physical memory in 32K blocks. In the case of N8VEM platforms, the lower 32K of the CPU address

space is used for this purpose (the window). The upper 32K of CPU address space is assigned a fixed 32K area of physical memory that never changes. The lower 32K can be “mapped” on the fly to any of the 32K banks of physical memory at a time. The only constraint is that the CPU cannot be executing code in the lower 32K of CPU address space at the time that a bank switch is performed.

By cleverly utilizing the pages of physical RAM for specific purposes and swapping in the correct page when needed, it is possible to utilize substantially more than 64K of RAM. Because the N8VEM project has now produced a very large variety of hardware, it has become extremely important to implement a bank switched solution to accommodate the maximum range of hardware devices and desired functionality.

General Design Strategy

The design goal is to locate as much of the hardware dependent code as possible out of normal 64KB CP/M address space and into a bank switched area of memory. A very small code shim (proxy) is located in the top 256 bytes of CPU memory. This proxy is responsible for redirecting all hardware BIOS (HBIOS) calls by swapping the “driver code” bank of physical RAM into the lower 32K and completing the request. The operating system is unaware this has occurred. As control is returned to the operating system, the lower 32KB of memory is switched back to normal (bank 0).

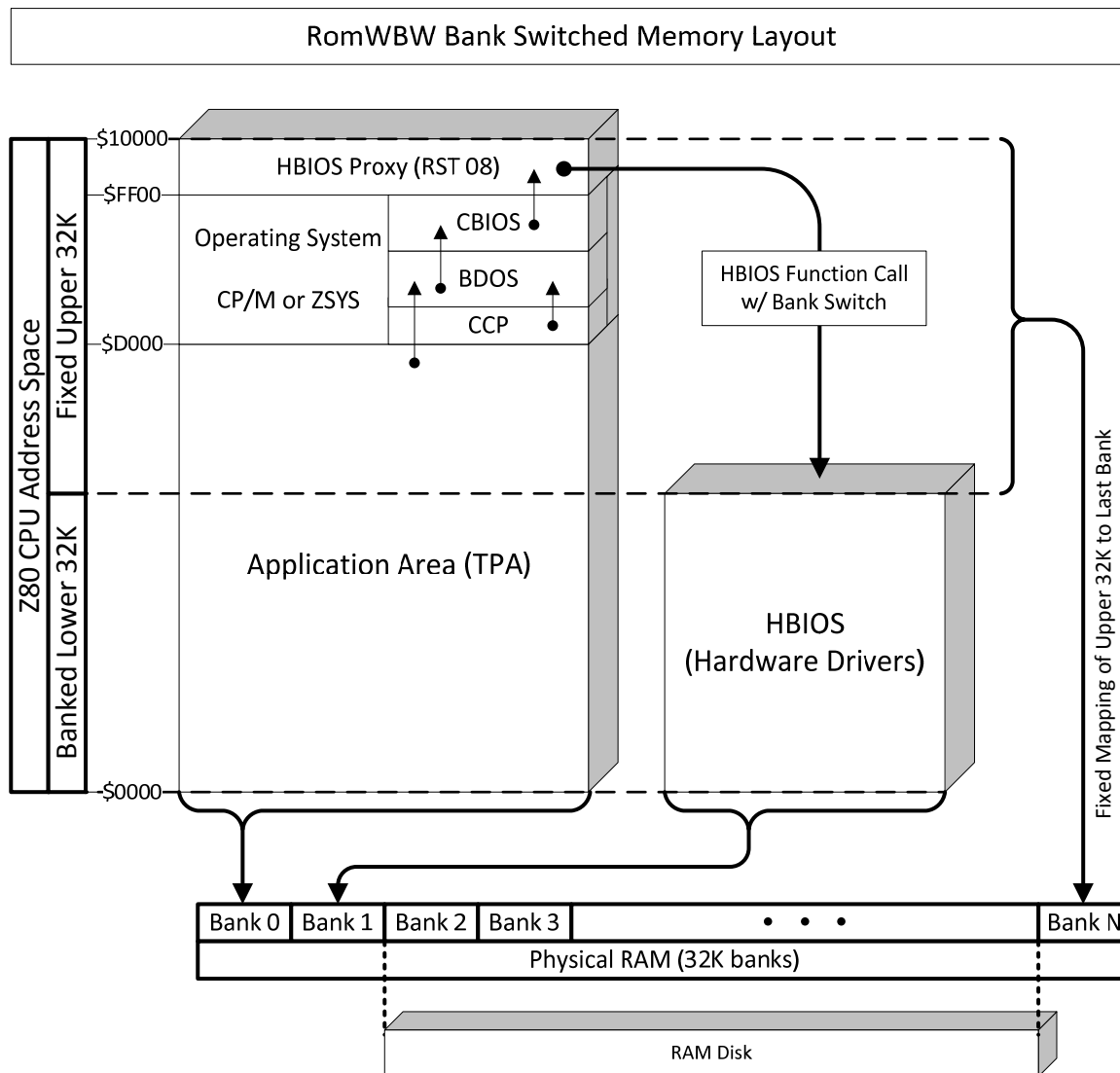
HBIOS is completely agnostic with respect to the operating system (it does not know or care what operating system is using it). The operating system makes simple calls to HBIOS to access any desired hardware functions. Since the HBIOS proxy occupies only 256 bytes at the top of memory, the vast majority of the CPU memory is available to the operating system and the running application. As far as the operating system is concerned, all of the hardware driver code has been magically implemented inside of a tiny 256 byte area at the top of the CPU address space.

Unlike some other Z80 bank switching schemes, there is no attempt to build bank switching into the operating system itself. This is intentional so as to ensure that any operating system can easily be adapted without requiring invasive modifications to the operating system itself. This also keeps the complexity of memory management completely away from the operating system and applications.

There are some operating systems that have built-in support for bank switching (e.g., CP/M 3). These operating systems are allowed to make use of the bank switched memory and are compatible with HBIOS. However, it is necessary that the customization of these operating systems take into account the banks of memory used by HBIOS and not attempt to use those specific banks.

Note that all code and data are located in RAM memory during normal execution. While it is possible to use ROM memory to run code, it would require that more upper memory be reserved for data storage. It is simpler and more memory efficient to keep everything in RAM. At startup (boot) all required code is copied to RAM for subsequent execution.

Runtime Memory Layout



System Boot Process

A multi-phase boot strategy is employed. This is necessary because at cold start, the CPU is executing code from ROM in lower memory which is the same area that is bank switched.

Boot Phase 1 copies the phase 2 code to upper memory and jumps to it to continue the boot process. This is required because the CPU starts at address \$0000 in low memory. However, low memory is used as the area for switching ROM/RAM banks in and out. Therefore, it is necessary to relocate execution to high memory in order to initialize the RAM memory banks.

Boot Phase 2 manages the setup of the RAM page banks for HBIOS operation, performs hardware initialization, and then executes the boot loader.

Boot Phase 3 is the loading of the selecting operating system (or debug monitor) by the Boot Loader. The Boot Loader is responsible for prompting the user to select a target operating system to load, loading it into RAM, then transferring control to it. The Boot Loader is capable of loading a target operating system from a variety of locations including disk drives and ROM.

Note that the entire boot process is entirely operating system agnostic. It is unaware of the operating system being loaded. The Boot Loader prompts the user for the **location** of the binary image to load, but does not know anything about what is being loaded (the image is usually an operating system, but could be any executable code image). Once the Boot Loader has loaded the image at the selected location, it will transfer control to it. Assuming the typical situation where the image was an operating system, the loaded operating system will then perform it's own initialization and begin normal operation.

There are actually two ways to perform a system boot. The first, and most commonly used, method is a "ROM Boot". This refers to booting the system directly from the startup code contained on the physical ROM chip. A ROM Boot is always performed upon power up or when a hardware reset is performed.

Once the system is running (operating system loaded), it is possible to reboot the system from a system image contained on the file system. This is referred to as an "Application Boot". This mechanism allows a temporary copy of the system to be uploaded and stored on the file system of an already running system and then used to boot the system. This boot technique is useful to: 1) test a new build of a system image before programming it to the ROM; or 2) easily switch between system images on the fly.

A more detailed explanation of these two boot processes is presented below. You can refer to the section of this document called Core Functions

In order to support more advanced functionality (primarily memory management), there are a set of HBIOS functions which are accessed via direct vectors into a jump table at the start of the HBIOS proxy.

Core Function Summary

HBIOS Initialize (INIT)	\$FC00
HBIOS Invoke (INVOKE)	\$FC03
HBIOS Set Bank (SETBNK)	\$FC06
HBIOS Get Bank (GETBNK)	\$FC09
HBIOS Memory Copy (COPY)	\$FC0C
HBIOS Extended Memory Copy (XCOPY)	\$FC0F
HBIOS Far Get Byte (FRGETB)	\$FC12
HBIOS Far Get Word (FRGETW)	\$FC15
HBIOS Far Put Byte (FRPUTB)	\$FC18
HBIOS Far Put Word (FRPUTW)	\$FC1B

Core Function Reference

HBIOS Initialize (INIT) - \$FC00

<u>Input</u>	<u>Output</u>
Perform full HBIOS initialization including full hardware reset. All registers used.	

HBIOS Invoke (INVOKE) - \$FC03

<u>Input</u>	<u>Output</u>
B=HB IOS Function HB IOS Function Parm s	According to HBIOS function Uses all primary registers
Wait for a single character to be available at the specified device and return the character in E. Function will wait indefinitely for a character to be available.	

HBIOS Set Bank (SETBNK) - \$FC06

<u>Input</u>	<u>Output</u>
A=Bank to activate	None Register A used
Select bank identified by register A. The selected bank is mapped into the lower 32K of memory. Bank may be ROM or RAM depending on bank id. The bank identified becomes the current bank. The caller is responsible for stack management.	

HBIOS Get Bank (GETBNK) - \$FC09

<u>Input</u>	<u>Output</u>
None	A=Current Bank Id Uses no registers
Returns the Bank Id of the currently selected memory bank.	

HBIOS Memory Copy (COPY) - \$FC0C

<u>Input</u>	<u>Output</u>
HL=Source Address DE=Destination Address BC=Number of bytes to copy	None Uses all primary registers
Copies a block of memory from the address in HL to the address in DE. The number of bytes to copy is indicated by BC. Will copy between banks if XCOPY is called to set a source and destination bank prior to invocation. Source and destination banks are reset to standard user bank after invocation.	

HBIOS Extended Memory Copy (XCOPY) - \$FC0F

<u>Input</u> B=Source Bank Id C=Destination Bank Id	<u>Output</u> None Uses all primary registers
Sets the source and destination Bank Id's for the next call to COPY. The source and destination Bank Id's are reset to the standard user bank after the next call to COPY.	

HBIOS Far Get Byte (FRGETB) - \$FC12

<u>Input</u> HL=Address of Byte C=Bank Id of Byte	<u>Output</u> A=Byte value at C:(HL) Uses AF
Retrieve the byte value from Bank Id in C at Address in HL.	

HBIOS Far Get Word (FRGETW) - \$FC15

<u>Input</u> HL=Address of Word C=Bank Id of Word	<u>Output</u> DE=Word value at C:(HL) Uses AF, DE
Retrieve the word value from Bank Id in C at Address in HL.	

HBIOS Far Put Byte (FRPUTB) - \$FC18

<u>Input</u> HL=Address of Word C=Bank Id of Word A=Byte Value	<u>Output</u> None Uses AF
Stores the byte value in A to Bank Id in C at address in HL.	

HBIOS Far Put Word (FRPUTW) - \$FC1B

<u>Input</u> HL=Address of Word C=Bank Id of Word DE=Word Value	<u>Output</u> None Uses AF
Stores the word value in DE to Bank Id in C at address in HL.	

Memory Layout Detail to help understand the processes.

ROM Boot

At power on (or hardware reset), ROM page 0 is automatically mapped to lower memory by hardware level system initialization. Page Zero (first 256 bytes of the CPU address space) is reserved to contain

dispatching instructions for interrupt instructions. Address \$0000 performs a jump to the start of the phase 1 code so that this first page can be reserved.

The phase 1 code now copies the phase 2 code from lower memory to upper memory and jumps to it. The phase 2 code now initializes the HBIOS by copying the ROM resident HBIOS from ROM page 1 to RAM page 1. It subsequently calls the HBIOS initialization routine. Finally, it starts the Boot Loader which prompts the user for the location of the target system image to execute.

Once the boot loader transfers control to the target system image, all of the Phase 1, Phase 2, and Boot Loader code is abandoned and the space it occupied is normally overwritten by the operating system.

The ROM Boot process is implemented in the source file “bootrom.asm”.

Application Boot

When a new system image is built, one of the output files produced is an actual CP/M application (an executable .COM program file). Once you have a running CP/M (or compatible) system, you can upload/copy this application file to the filesystem. By executing this file, you will initiate an Application Boot using the system image contained in the application file itself.

Upon execution, the Application Boot program is loaded into memory by the previously running operating system starting at \$0100. Note that program image contains a copy of the HBIOS to be installed and run. Once the Application Boot program is loaded by the previous operating system, control is passed to it and it performs a system initialization similar to the ROM Boot, but using the image loaded in RAM.

Specifically, the code at \$0100 (in low memory) copies phase 2 boot code to upper memory and transfers control to it. The phase 2 boot code copies the HBIOS image from application RAM to RAM page 1, then calls the HBIOS initialization routine. At this point, the prior HBIOS code has been discarded and overwritten. Finally, the Boot Loader is invoked just like a ROM Boot.

This process is implemented in the source file “bootapp.asm”

Notes

1. Size of ROM disk and RAM disk will be decreased as needed to accommodate RAM and ROM memory bank usage for the banked BIOS.
2. There is no support for interrupt driven drivers at this time. Such support should be possible in a variety of ways, but none are yet implemented.
3. There are still some places in the CBIOS where it is manipulating memory banks directly. This is inappropriate and will eventually be corrected.

Driver Model

The framework code for bank switching also allows hardware drivers to be implemented mostly without concern for memory management. Drivers are coded to simply implement the HBIOS functions appropriate for the type of hardware being supported. When the driver code gets control, it has already been mapped to the CPU address space and simply performs the requested function based on parameters passed in registers. Upon return, the bank switching framework takes care of restoring the original memory layout expected by the operating system and application.

However, the one constraint of hardware drivers is that any data buffers that are to be returned to the operating system or applications must be allocated in high memory. Buffers inside of the driver's memory bank will be swapped out of the CPU address space when control is returned to the operating system.

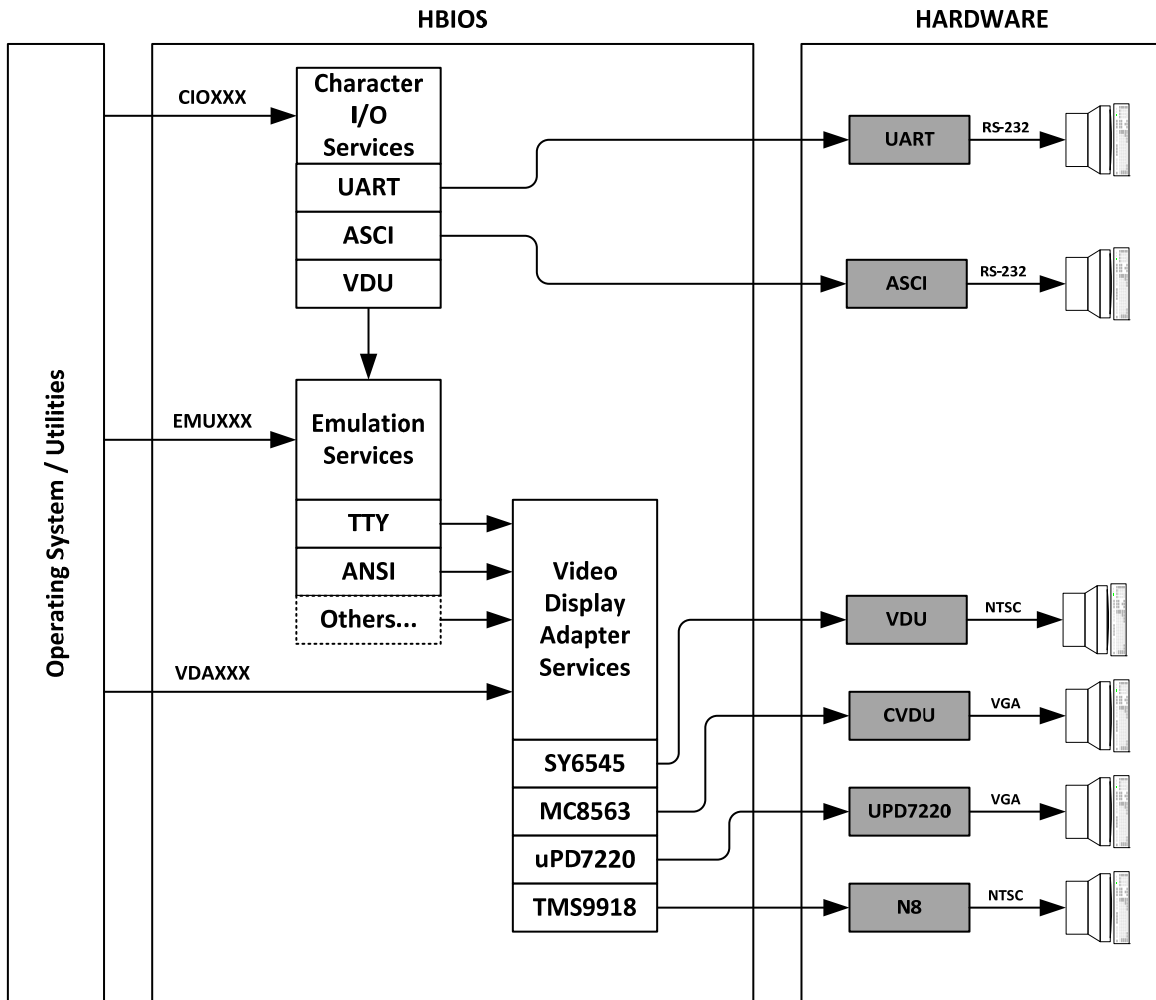
If the driver code must make calls to other code, drivers, or utilities in the driver bank, it must make those calls directly (it must not use RST 08). This is to avoid a nested bank switch which is not supported at this time.

Character / Emulation / Video Services

In addition to a generic set of routines to handle typical character input/output, HBIOS also includes functionality for managing built-in video display adapters. To start with there is a basic set of character input/output functions, the CIOXXX functions, which allow for simple character data streams. These functions fully encompass routing byte stream data to/from serial ports. Note that there is a special character pseudo-device called "CRT". When characters are read/written to/from the CRT character device, the data is actually passed to a built-in terminal emulator which, in turn, utilizes a set of VDA (Video Display Adapter) functions (such as cursor positioning, scrolling, etc.).

The following diagram depicts the relationship between these components of HBIOS video processing:

Character / Emulation / Video Services



Normally, the operating system will simply utilize the CIOXXX functions to send and receive character data. The Character I/O Services will route I/O requests to the specified physical device which is most frequently a serial port (such as UART or ASCI). As shown above, if the CRT device is targeted by a CIOXXX function, it will actually be routed to the Emulation Services which implement TTY, ANSI, etc. escape sequences. The Emulation Services subsequently rely on the Video Display Adapter Services as an additional layer of abstraction. This allows the emulation code to be completely unaware of the actual physical device (device independent). Video Display Adapter (VDA) Services contains drivers as needed to handle the available physical video adapters.

Note that the Emulation and VDA Services API functions are available to be called directly. Doing so must be done carefully so as to not corrupt the “state” of the emulation logic.

Before invoking CIOXXX functions targeting the CRT device, it is necessary that the underlying layers (Emulation and VDA) be properly initialized. The Emulation Services must be initialized to specify the

desired emulation and specific physical VDA device to target. Likewise, the VDA Services may need to be initialized to put the specific video hardware into the proper mode, etc.

HBIOS Reference

Invocation

HBIOS functions are invoked by placing the required parameters in CPU registers and executing an RST 08 instruction. Note that HBIOS does not preserve register values that are unused. However, it must not modify the Z80 alternate registers or IX/IY (these registers can be used within HBIOS as long as they are saved and restored internally).

Normally, applications will not call HBIOS functions directly. It is intended that the operating system makes all HBIOS function calls. Applications that are considered system utilities may use HBIOS, but must be careful not to modify the operating environment in any way that the operating system does not expect.

In general, the desired function is placed in the B register. Additional registers are used as defined by the specific function. Register A should be used to return function result information. A=0 should indicate success, other values are function specific.

Some functions utilize pointers to memory buffers. Such memory buffers are required to be located in the upper 32K for CPU RAM address space. This requirement significantly simplifies the HBIOS proxy and improves performance by avoiding “double copies” of buffers.

Function Overview

Character Input/Output (CIO)	Character Input – CIOIN Character Output – CIOIN Character Input Status – CIOIST Character Output Status – CIOOST Character I/O Configuration – CIOCFG
Disk Input/Output (DIO)	Disk Read – DIORD Disk Write – DIOWR Disk Status – DIOST Disk Media – DIOMED Disk Identify – DIOID Disk Get Buffer Address – DIOGETBUF Disk Set Buffer Address – DIOSETBUF
Real Time Clock (RTC)	RTC Get Time – RTCGETTIM RTC Set Time – RTCSETTIM RTC Get NVRAM Byte – RTCGETBYT RTC Set NVRAM Byte – RTCSETBYT RTC Get NVRAM Block – RTCGETBLK RTC Set NVRAM Block – RTCSETBLK
Emulation (EMU)	Emulation Input – EMUIN Emulation Output – EMUIN Emulation Input Status – EMUIST Emulation Output Status – EMUOST Emulation Initialization – EMUINI Emulation Query – EMUQRY
Video Display Adapter (VDA)	VDA Initialize – VDAINI VDA Query – VDAQRY VDA Reset – VDARES VDA Set Cursor Style – VDASCS VDA Set Cursor Position – VDASCP VDA Set Character Attribute – VDA SAT VDA Set Character Color – VDASCO VDA Write Character – VDAWRC VDA Fill – VDAFIL VDA Copy -- VDACPY VDA Scroll – VDASCR VDA Keyboard Status – VDAKST VDA Keyboard Flush – VDAKFL VDA Keyboard Read – VDAKRD
System (SYS)	System Set Bank – SYSSETBNK System Get Bank – SYSGETBNK System Copy – SYSCPY System Extended Copy – SYSXCPY System Get Configuration – SYSGETCFG System Set Configuration – SYSSETCFG System Get Version – SYSGETVER

Character Input/Output (CIO)

Character input/output functions require that a character device/unit be specified in the C register. The upper nibble (upper 4 bits) specify the device (such as UART). The lower nibble specifies the unit of the device (0=first port, 1=second port, etc.)

The CRT device is a virtual device code that will route characters in/out via the currently active VDA device (see EMUINI function).

The currently supported devices/units are:

Device		Unit
0	UART	Unit = Port
1	ASCI	Unit = Port
2	PropIO VGA	N/A
3	ParPortProp VGA	N/A
F	CRT	N/A

Character Input – CIOIN (\$00)

Input	Output
B=\$00 (function) C=Device/Unit	A=Status (0=OK, 1=Error) E=Character input
Wait for a single character to be available at the specified device and return the character in E. Function will wait indefinitely for a character to be available.	

Character Output – CIOOUT (\$01)

Input	Output
B=\$01 (function) C=Device/Unit E=Character to output	A=Status (0=OK, 1=Error)
Wait for device/unit to be ready to send a character, then send the character specified in E.	

Character Input Status – CIOIST (\$02)

Input	Output
B=\$02 (function) C=Device/Unit	A=Status: # characters in input buffer
Return the number of characters available to read in the input buffer of the device/unit specified. If the device has no input buffer, it is acceptable to return simply 0 or 1 where 0 means there is no character available to read and 1 means there is a character available to read.	

Character Output Status – CIOOST (\$03)

<u>Input</u>	<u>Output</u>
B=\$03 (function) C=Device/Unit	A=Status: output buffer space available
<p>Return the space available in the output buffer expressed as a character count. If a 16 byte output buffer contained 6 characters waiting to be sent, this function would return 10, the number of positions available in the output buffer. If the port has no output buffer, it is acceptable to return simply 0 or 1 where 0 means the port is busy and 1 means the port is ready to output a character.</p>	

Character Config – CIOCFG (\$04)

<u>Input</u>	<u>Output</u>
B=\$04 (function) C=Speed E=Framing/Parity	A=Status: 0=Success, otherwise failure
<p>Not yet implemented.</p> <p>Sets the speed and framing of the character stream. Register C specifies the speed. Register E specifies the framing and parity characteristics.</p>	

Disk Input/Output (DIO)

Disk input/output functions require that a disk device/unit be specified in the C register. The upper nibble (upper 4 bits) specify the device (such as IDE). The lower nibble specifies the unit of the device (0=master, 1=slave, etc.)

The currently supported devices/units are:

Device		Unit
0	Memory Disk	Unit 0 = ROM, Unit 1 = RAM
1	Floppy Disk	Unit 0 = Primary, Unit 1 = Secondary
2	RAM Floppy	Unit 0 = First Board, Unit 1 = Second Board
3	IDE Disk	Unit 0 = Master, Unit 1 = Slave
4	ATAPI Disk (not implemented)	Unit 0 = Master, Unit 1 = Slave
5	PPIDE Disk	Unit 0 = Master, Unit 1 = Slave
6	SD Card	N/A
7	PropIO SD Card	N/A
8	ParPortProp SD Card	N/A
9	SIMH HDSK Disk	Unit 0-7 = SIMH emulated hard disk 0-7

The currently defined media types are:

Media ID	Value	Format
MID_NONE	0	No media installed
MID_MDROM	1	ROM Drive
MID_MDRAM	2	RAM Drive
MID_RF	3	Hard Disk (LBA)
MID_HD	4	Hard Disk (LBA)
MID_FD720	5	3.5" 720K Floppy
MID_FD144	6	3.5" 1.44M Floppy
MID_FD360	7	5.25" 360K Floppy
MID_FD120	8	5.25" 1.2M Floppy
MID_FD111	9	8" 1.11M Floppy

Disk Read – DIORD (\$10)

<u>Input</u>	<u>Output</u>
B=\$10 (function) C=Device/Unit HL=Track D=Head E=Sector	A=Status (0=OK, 1=Error)
<p>Read a single 512 byte sector into the buffer previously specified buffer area (see DIOSBA).</p> <p>For a hard disk device, only LBA addressing is supported. In this case, HL will contain the high 16 bits of the LBA block number and DE will contain the low 16 bits of the LBA block number.</p>	

Disk Write – DIOWR (\$11)

<u>Input</u>	<u>Output</u>
B=\$11 (function) C=Device/Unit HL=Track D=Head E=Sector	A=Status (0=OK, 1=Error)
<p>Write a single 512 byte sector from the buffer previously specified buffer area (see DIOSBA).</p> <p>For a hard disk device, only LBA addressing is supported. In this case, HL will contain the high 16 bits of the LBA block number and DE will contain the low 16 bits of the LBA block number.</p>	

Disk Status – DIOST (\$12)

<u>Input</u>	<u>Output</u>
B=\$12 (function) C=Device/Unit	A=Status (0=OK, 1=Error)
<p>Return the current status of the specified device.</p>	

Disk Media – DIOMED (\$13)

<u>Input</u>	<u>Output</u>
B=\$13 (function) C=Device/Unit	A=Media ID
<p>Return a media identifier that describes the media format of the current media in the device. If the device supports multiple media types, the media will be examined to determine the specific media format currently installed.</p>	

Disk Identify – DIOID (\$14)

Not implemented

Disk Get Buffer Address – DIOGETBUF (\$18)

<u>Input</u> B=\$18 (function) HL=Buffer Address	<u>Output</u> A=Status (0=OK, 1=Error)
Get the current buffer address used for disk read/write calls.	

Disk Set Buffer Address – DIOSETBUF (\$19)

<u>Input</u> B=\$19 (function) HL=Buffer Address	<u>Output</u> A=Status (0=OK, 1=Error)
Set the buffer address to be used for subsequent disk read/write calls. Contents of any prior buffer location are not retained. The new buffer area is not initialized. The buffer must be located in high memory (top 32K).	

Real Time Clock (RTC)

The Real Time Clock functions provide read/write access to the clock and related Non-Volatile RAM.

The time functions (RTCGTM and RTCSTM) require a 6 byte date/time buffer of the following format. Each byte is BCD encoded.

Offset	Contents
0	Year (00-99)
1	Month (01-12)
2	Date (01-31)
3	Hours (00-24)
4	Minutes (00-59)
5	Seconds (00-59)

RTC Get Time – RTCGETTIM(\$20)

<u>Input</u> B=\$20 (function) HL=Time Buffer Address	<u>Output</u> A=Status: 0=Success, otherwise failure
Read the current value of the clock and store the date/time in the buffer pointed to by HL.	

RTC Set Time – RTCSETTIM(\$21)

<u>Input</u> B=\$21 (function)	<u>Output</u> A=Status: 0=Success, otherwise failure
Set the current value of the clock based on the date/time in the buffer pointed to by HL.	

RTC Get NVRAM Byte – RTCGETBYT(\$22)

<u>Input</u> B=\$22 (function) C=Index	<u>Output</u> A=Status: 0=Success, otherwise failure E=Value
Read a single byte value from the Non-Volatile RAM at the index specified by C. The value is returned in register E.	

RTC Set NVRAM Byte – RTCSETBYT(\$23)

<u>Input</u> B=\$23 (function) C=Index	<u>Output</u> A=Status: 0=Success, otherwise failure E=Value
Write a single byte value into the Non-Volatile RAM at the index specified by C. The value to be written is specified in E.	

RTC Get NVRAM Block – RTCGETBLK(\$24)

<u>Input</u> B=\$24 (function) HL=Buffer	<u>Output</u> A=Status: 0=Success, otherwise failure
Read the entire contents of the Non-Volatile RAM into the buffer pointed to by HL. HL must point to a location in the top 32K of CPU address space.	

RTC Set NVRAM Block – RTCSETBLK(\$25)

<u>Input</u> B=\$25 (function) HL=Buffer	<u>Output</u> A=Status: 0=Success, otherwise failure
Write the entire contents of the Non-Volatile RAM from the buffer pointed to by HL. HL must point to a location in the top 32K of CPU address space.	

Emulation (EMU)

The Emulation functions allow setting up the desired emulation (terminal type) as well as the target physical device for emulation. It is not possible to maintain multiple independent emulation states for different physical devices – emulation must be reinitialized to target a new physical device.

Emulation Input – EMUIN (\$30)

<u>Input</u>	<u>Output</u>
B=\$30 (function) E=Character input	A=Status (0=OK, 1=Error)
Wait for a single character to be available at the emulation target device and return the character in E. Function will wait indefinitely for a character to be available.	

Emulation Output – EMUOUT (\$31)

<u>Input</u>	<u>Output</u>
B=\$31 (function) E=Character to output	A=Status (0=OK, 1=Error)
Wait for emulation target device/unit to be ready to send a character, then send the character specified in E.	

Emulation Input Status – EMUIST (\$32)

<u>Input</u>	<u>Output</u>
B=\$32 (function)	A=Status: # characters in input buffer
Return the number of characters available to read in the input buffer of the emulation target device/unit specified. If the device has no input buffer, it is acceptable to return simply 0 or 1 where 0 means there is no character available to read and 1 means there is a character available to read.	

Emulation Output Status – EMUOST (\$33)

<u>Input</u>	<u>Output</u>
B=\$33 (function)	A=Status: output buffer space available
Return the space available in the output buffer expressed as a character count. If a 16 byte output buffer contained 6 characters waiting to be sent, this function would return 10, the number of positions available in the output buffer. If the emulation target device has no output buffer, it is acceptable to return simply 0 or 1 where 0 means the port is busy and 1 means the port is ready to output a character.	

Emulation Initialization –EMUINI (\$38)

<u>Input</u> B=\$38 (function) C=VDA Device/Unit E=Terminal Type	<u>Output</u> A=Status: 0=Success, otherwise failure
Selects the actual VDA device/unit to be targeted for emulation.. Register C is set to the VDA device/unit to be selected. Register E specifies the terminal emulation to be used (0=TTY, 1=ANSI).	

Emulation Query –EMUQRY (\$39)

<u>Input</u> B=\$39 (function)	<u>Output</u> A=Status: 0=Success, otherwise failure C=VDA Device/Unit E=Terminal Emulation
Returns current information about the active emulation session. Register C is set to the VDA device/unit currently targeted. Register E returns the terminal emulation in use (0=TTY, 1=ANSI).	

Video Display Adapter (VDA)

The VDA functions are provided as a common interface to Video Display Adapters. Not all VDAs will include keyboard hardware. In this case, the keyboard functions should return a failure status.

The VDA functions require that a VDA device/unit be specified in the C register. The upper nibble (upper 4 bits) specifies the device. The lower nibble specifies the unit (not currently used).

The currently defined video devices are:

















VDA ID	Value	Device
VDA_NONE	0	No VDA
VDA_VDU	1	ECB VDU board
VDA_CVDU	2	ECB Color VDU board
VDA_7220	3	ECB uPD7220 video display board
VDA_N8	4	TMS9918 video display built-in to N8

Depending on the capabilities of the hardware, the use of colors and attributes may or may not be supported. If the hardware does not support these capabilities, they will be ignored.

Color byte values are constructed using typical RGBI (Red/Green/Blue/Intensity) bits. The high four bits of the value determine the background color and the low four bits determine the foreground color. This results in 16 unique color values for both foreground and background. The following table illustrates the color byte value construction:

	Bit	Color
Background	7	Intensity
	6	Blue
	5	Green
	4	Red
Foreground	3	Intensity
	2	Blue
	1	Green
	0	Red

The following table illustrates the resultant color for each of the possible 16 values for foreground or background:

Foreground	Background	Color	Sample
0 0000	0_ 0000	Black	
1 0001	1_ 0001	Red	
2 0010	2_ 0010	Green	
3 0011	3_ 0011	Brown	
4 0100	4_ 0100	Blue	
5 0101	5_ 0101	Magenta	
6 0110	6_ 0110	Cyan	
7 0111	7_ 0111	White	
8 1000	8_ 1000	Gray	
9 1001	9_ 1001	Light Red	
A 1010	A_ 1010	Light Green	
B 1011	B_ 1011	Yellow	
C 1100	C_ 1100	Light Blue	
D 1101	D_ 1101	Light Magenta	
E 1110	E_ 1110	Light Cyan	
F 1111	F_ 1111	Bright White	

Attribute byte values are constructed using the following bit encoding:

Bit	Effect
7	n/a (0)
6	n/a (0)
5	n/a (0)
4	n/a (0)
3	n/a (0)
2	Reverse
1	Underline
0	Blink

The following codes are returned by a keyboard read to signify non-ASCII keystrokes:

Value	Keystroke	Value	Keystroke
E0	F1	F0	Insert
E1	F2	F1	Delete
E2	F3	F2	Home
E3	F4	F3	End
E4	F5	F4	PageUp
E5	F6	F5	PadeDown
E6	F7	F6	UpArrow
E7	F8	F7	DownArrow
E8	F9	F8	LeftArrow
E9	F10	F9	RightArrow
EA	F11	FA	Power
EB	F12	FB	Sleep
EC	SysReq	FC	Wake
ED	PrintScreen	FD	Break
EE	Pause	FE	
EF	App	FF	

Video Display Adapter Initialize –VDAINI (\$40)

<u>Input</u>	<u>Output</u>
B=\$40 (function) C=Device/Unit E=Video Mode (device specific) HL=Character Bitmap (optional)	A=Status: 0=Success, otherwise failure
<p>Performs a full (re)initialization of the specified video device. The screen is cleared and the keyboard buffer is flushed. If the specified VDA supports multiple video modes, the requested mode can be specified in E (set to 0 for default/not specified). Mode values are specific to each VDA.</p> <p>HL may point to a location in memory with the character bitmap to be loaded into the VDA video processor. The location MUST be in the top 32K of the CPU memory space. HL must be set to zero if no character bitmap is specified (the VDA video processor will utilize a default character bitmap).</p>	

Video Display Adapter Query –VDAQRY (\$41)

<u>Input</u>	<u>Output</u>
B=\$41 (function) C=Device/Unit HL=Character Bitmap Data (optional)	A=Status: 0=Success, otherwise failure C=Video Mode D=Row Count E=Column Count HL=Character Bitmap Data (zero if none)
<p>Return information about the specified video device. C will be set to the current video mode. DE will return the dimensions of the video display as measured in rows and columns. Note that this is the count of rows and columns, not the last row/column number.</p> <p>If HL is not zero, it must point to a suitably sized memory buffer in the upper 32K of CPU address space that will be filled with the current character bitmap data. It is critical that HL be set to zero if it does not point to a proper buffer area or memory corruption will result. The video device driver may not have the ability to provide character bitmap data. In this case, on return, HL will be set to zero.</p>	

Video Display Adapter Reset –VDARES (\$42)

<u>Input</u>	<u>Output</u>
B=\$42 (function) C=Device/Unit	A=Status: 0=Success, otherwise failure
<p>Performs a soft reset of the Video Display Adapter. Should clear the screen, home the cursor, restore active attribute and color to defaults. Keyboard should be flushed.</p>	

Video Display Adapter Set Cursor Style –VDASCS (\$43)

<u>Input</u> B=\$43 (function) C=Device/Unit D=Start/End pixel E=Style	<u>Output</u> A=Status: 0=Success, otherwise failure
<p>If supported by the video hardware, adjust the format of the cursor such that the cursor starts at the pixel specified in the top nibble of D and end at the pixel specified in the bottom nibble of D. So, if D=\$08, a block cursor would be used that starts at the top pixel of the character cell and ends at the ninth pixel of the character cell.</p> <p>Register E is reserved to control the style of the cursor (blink, visibility, etc.), but is not yet implemented.</p> <p>Adjustments to the cursor style may or may not be possible for any given video hardware.</p>	

Video Display Adapter Set Cursor Position –VDASCP (\$44)

<u>Input</u> B=\$44 (function) C=Device/Unit D=Row E=Column	<u>Output</u> A=Status: 0=Success, otherwise failure
<p>Reposition the cursor to the specified row and column. Specifying a row/column that exceeds the boundaries of the display results in undefined behavior. Cursor coordinates are 0 based (0,0 is the upper left corner of the display).</p>	

Video Display Adapter Set Character Attribute –VDASAT (\$45)

<u>Input</u> B=\$45 (function) C=Device/Unit E=Character Attribute Code	<u>Output</u> A=Status: 0=Success, otherwise failure
<p>Assign the specified character attribute code to be used for all subsequent character writes/fills. This attribute is used to fill new lines generated by scroll operations. Refer to the character attribute for a list of the available attribute codes. Note that a given video display may or may not support any/all attributes.</p>	

Video Display Adapter Set Character Color –VDASCO (\$46)

<u>Input</u> B=\$46 (function) C=Device/Unit E=Color Code	<u>Output</u> A=Status: 0=Success, otherwise failure
<p>Assign the specified color code to be used for all subsequent character writes/fills. This color is also used to fill new lines generated by scroll operations. Refer to color code table for a list of the available color codes. Note that a given video display may or may not support any/all colors.</p>	

Video Display Adapter Write Character –VDAWRC (\$47)

<u>Input</u> B=\$47 (function) C=Device/Unit E=Character	<u>Output</u> A=Status: 0=Success, otherwise failure
<p>Write the character specified in E. The character is written starting at the current cursor position and the cursor is advanced. If the end of the line is encountered, the cursor will be advanced to the start of the next line. The display will not scroll if the end of the screen is exceeded.</p>	

Video Display Adapter Fill –VDAFIL (\$48)

<u>Input</u> B=\$48 (function) C=Device/Unit E=Character HL=Count	<u>Output</u> A=Status: 0=Success, otherwise failure
<p>Write the character specified in E to the display the number of times specified in HL. Characters are written starting at the current cursor position and the cursor is advanced by the number of characters written. If the end of the line is encountered, the characters will continue to be written starting at the next line as needed. The display will not scroll if the end of the screen is exceeded.</p>	

Video Display Adapter Copy –VDACPY (\$49)

<u>Input</u> B=\$48 (function) C=Device/Unit D=Source Row E=Source Column L=Count (max 255)	<u>Output</u> A=Status: 0=Success, otherwise failure
<p>Copy count (L) bytes from the source row/column (DE) to current cursor position. The cursor position is not updated. The maximum count is 255. Copying to/from overlapping areas is not supported and will have an undefined behavior. The display will not scroll if the end of the screen is exceeded. Copying beyond the active screen buffer area is not supported and results in undefined behavior.</p>	

Video Display Adapter Scroll –VDASCR (\$4A)

<u>Input</u> B=\$49 (function) C=Device/Unit E=Scroll distance (# lines)	<u>Output</u> A=Status: 0=Success, otherwise failure
Scroll the video display by the number of lines specified in E. If E contains a negative number, then reverse scroll should be performed.	

Video Display Adapter Keyboard Status –VDAKST (\$4B)

<u>Input</u> B=\$4A (function) C=Device/Unit	<u>Output</u> A=Status: # key codes in keyboard buffer
Return a count of the number of key codes in the keyboard buffer. If it is not possible to determine the actual number in the buffer, it is acceptable to return 1 to indicate there are key codes available to read and 0 if there are none available.	

Video Display Adapter Keyboard Flush –VDAKFL (\$4C)

<u>Input</u> B=\$4B (function) C=Device/Unit	<u>Output</u> A=Status: 0=Success, otherwise failure
If a keyboard buffer is in use, it should be purged and all contents discarded.	

Video Display Adapter Keyboard Read –VDAKRD (\$4D)

<u>Input</u>	<u>Output</u>
B=\$4C (function) C=Device/Unit	A=Status: 0=Success, otherwise failure C=Scancode D=Keystate E=Keycode
<p>Read next key code from keyboard. If a keyboard buffer is used, return the next key code in the buffer. If no key codes are available, wait for a keypress and return the keycode.</p> <p>The scancode value is the raw scancode from the keyboard for the keypress. Scancodes are from scancode set 2 standard.</p> <p>The keystate is a bitmap representing the value of all modifier keys and shift states as they existed at the time of the keystroke. The bitmap is defined as:</p> <ul style="list-style-type: none">Bit 7: Set to indicate key pressed was from the num padBit 6: Set to indicate Caps Lock was activeBit 5: Set to indicate Num Lock was activeBit 4: Set to indicate Scroll Lock was activeBit 3: Set to indicate Windows key was held downBit 2: Set to indicate Alt key was held downBit 1: Set to indicate control key was held downBit 0: Set to indicate Shift key was held down <p>Keycodes are generally returned as appropriate ASCII values, if possible. Special keys, like function keys, are returned as reserved codes as described at the start of this section.</p>	

System (SYS)

Set Bank – SYSSETBNK (\$F0)

<u>Input</u>	<u>Output</u>
B=\$F0 (function) C=Bank ID	A=Previously active Bank ID
Activates the Bank ID specified in C and returns the previously active Bank ID in A. The caller MUST be invoked from code located in the upper 32K and the stack MUST be in the upper 32K.	

Get Bank – SYSGETBNK (\$F1)

<u>Input</u>	<u>Output</u>
B=\$F1 (function)	A=Active Bank ID
Returns the currently active Bank ID in A.	

Memory Copy – SYSCPY (\$F2)

<u>Input</u>	<u>Output</u>
B=\$F2 (function) DE=Destination address HL=Source address IX=Count of bytes to copy	A=Status: 0=Success, otherwise failure
<p>This function will perform a bulk memory copy. If SYSXCPY is invoked before this function, the memory copy will performed to/from the memory banks specified in the SYSXCPY function. Otherwise, the memory will be copied to/from the default user memory bank (typically TPA).</p> <p>WARNING: The memory copy is performed from low byte to high byte, so be careful of a memory copy where the source range overlaps the destination range.</p> <p>WARNING: Directly manipulating memory banks can easily corrupt critical areas of the system.</p> <p>WARNING: This function uses the IX register as a parameter. The caller should save the original value of IX and restore it upon return from this function to comply with normal CP/M register handling expectations.</p>	

Extended Memory Copy – SYSXCPY (\$F3)

<u>Input</u>	<u>Output</u>
B=\$F3 (function) D=Destination Bank ID E=Source Bank ID	A=Status: 0=Success, otherwise failure
<p>This function controls the source and destination banks to be used by the SYSCPY function. The source bank is set using E and the destination bank is set using D.</p> <p>Note that the SYSCPY function will reset the source and destination bank numbers after it is called, so this function must be called prior to every invocation of SYSCPY that needs to perform an interbank memory copy.</p>	

Get Configuration – SYSGETCFG (\$F4)

<u>Input</u>	<u>Output</u>
B=\$F4 (function) C=Config Version (not implemented) DE=Destination address	A=Status: 0=Success, otherwise failure
<p>Copies the 256 byte block of configuration data into the destination memory address specified in DE. The destination memory address must be in high memory (upper 32K). At present, you will need to consult the source code for information on the contents of the configuration block.</p>	

Set Configuration – SYSSETCFG (\$F5)

<u>Input</u>	<u>Output</u>
B=\$F5 (function) C=Config Version (not implemented) DE=Source address	A=Status: 0=Success, otherwise failure
<p>Loads a 256 byte block of configuration data into the BIOS from the source memory address specified in DE. The source memory address must be in high memory (upper 32K). At present, you will need to consult the source code for information on the contents of the configuration block.</p> <p>NOTE: At present, the effects of this function are undefined. The BIOS will not dynamically adapt to a changed configuration.</p>	

Get Version – SYSGETVER (\$F6)

<u>Input</u>	<u>Output</u>
B=\$F6 (function)	A=Status: 0=Success, otherwise failure DE=Version
<p>This function will return the HBIOS version number. The version number is returned in DE. High nibble of D is the major version, low nibble of D is the minor version, high nibble of E is the patch number, and low nibble of E is the build number.</p>	

Core Functions

In order to support more advanced functionality (primarily memory management), there are a set of HBIOS functions which are accessed via direct vectors into a jump table at the start of the HBIOS proxy.

Core Function Summary

HBIOS Initialize (INIT)	\$FC00
HBIOS Invoke (INVOKE)	\$FC03
HBIOS Set Bank (SETBNK)	\$FC06
HBIOS Get Bank (GETBNK)	\$FC09
HBIOS Memory Copy (COPY)	\$FC0C
HBIOS Extended Memory Copy (XCOPY)	\$FC0F
HBIOS Far Get Byte (FRGETB)	\$FC12
HBIOS Far Get Word (FRGETW)	\$FC15
HBIOS Far Put Byte (FRPUTB)	\$FC18
HBIOS Far Put Word (FRPUTW)	\$FC1B

Core Function Reference

HBIOS Initialize (INIT) - \$FC00

<u>Input</u>	<u>Output</u>
Perform full HBIOS initialization including full hardware reset. All registers used.	

HBIOS Invoke (INVOKE) - \$FC03

<u>Input</u>	<u>Output</u>
B=HB IOS Function HB IOS Function Parm s	According to HBIOS function Uses all primary registers
Wait for a single character to be available at the specified device and return the character in E. Function will wait indefinitely for a character to be available.	

HBIOS Set Bank (SETBNK) - \$FC06

<u>Input</u>	<u>Output</u>
A=Bank to activate	None Register A used
Select bank identified by register A. The selected bank is mapped into the lower 32K of memory. Bank may be ROM or RAM depending on bank id. The bank identified becomes the current bank. The caller is responsible for stack management.	

HBIOS Get Bank (GETBNK) - \$FC09

<u>Input</u>	<u>Output</u>
None	A=Current Bank Id Uses no registers
Returns the Bank Id of the currently selected memory bank.	

HBIOS Memory Copy (COPY) - \$FC0C

<u>Input</u>	<u>Output</u>
HL=Source Address DE=Destination Address BC=Number of bytes to copy	None Uses all primary registers
Copies a block of memory from the address in HL to the address in DE. The number of bytes to copy is indicated by BC. Will copy between banks if XCOPY is called to set a source and destination bank prior to invocation. Source and destination banks are reset to standard user bank after invocation.	

HBIOS Extended Memory Copy (XCOPY) - \$FC0F

<u>Input</u> B=Source Bank Id C=Destination Bank Id	<u>Output</u> None Uses all primary registers
Sets the source and destination Bank Id's for the next call to COPY. The source and destination Bank Id's are reset to the standard user bank after the next call to COPY.	

HBIOS Far Get Byte (FRGETB) - \$FC12

<u>Input</u> HL=Address of Byte C=Bank Id of Byte	<u>Output</u> A=Byte value at C:(HL) Uses AF
Retrieve the byte value from Bank Id in C at Address in HL.	

HBIOS Far Get Word (FRGETW) - \$FC15

<u>Input</u> HL=Address of Word C=Bank Id of Word	<u>Output</u> DE=Word value at C:(HL) Uses AF, DE
Retrieve the word value from Bank Id in C at Address in HL.	

HBIOS Far Put Byte (FRPUTB) - \$FC18

<u>Input</u> HL=Address of Word C=Bank Id of Word A=Byte Value	<u>Output</u> None Uses AF
Stores the byte value in A to Bank Id in C at address in HL.	

HBIOS Far Put Word (FRPUTW) - \$FC1B

<u>Input</u> HL=Address of Word C=Bank Id of Word DE=Word Value	<u>Output</u> None Uses AF
Stores the word value in DE to Bank Id in C at address in HL.	

Memory Layout Detail

ROM Page 0

Loc	Org	Size	Source	Contents
0000	0000	0100	pgzero.asm	Page Zero
0100	0100	0100	bootrom.asm	ROM Bootstrap
0200	0100	0200	syscfg.asm	System Configuration
0400	8400	0C00	loader.asm	Loader
1000	1000	3000	romfill.asm	Filler
4000	C000	1000	dbgmon.asm	Debug Monitor
5000	D000	0800	<ccp>	Command Processor (CCP, ZCPR, etc.)
5800	D800	0E00	<dos>	Disk Operating System (BDOS, ZSDOS, etc.)
6600	E600	1600	<osbios>	OS BIOS (CBIOS, ZBIOS)
7C00	FC00	0400	hbfill	Filler for HBIOS Proxy

ROM Page 1

Loc	Org	Size	Source	Contents
0000	0000	0100	pgzero.asm	Page Zero
0100	0100	0100	bootrom.asm	Reserved (unused)
0200	0200	0200	syscfg.asm	System Configuration
0400	0400	0C00	loader.asm	Reserved (unused)
1000	1000	7000	bnk1.asm	Bank 1 HBIOS Extension (drivers)

COM File Image

Loc	Org	Size	Source	Contents
0100	0100	0100	bootapp.asm	Application Bootstrap
0200	0200	0200	syscfg.asm	System Configuration
0400	8400	0C00	loader.asm	Loader
1000	1000	7000	bnk1.asm	Bank 1 HBIOS Extension (drivers)
8000	C000	1000	dbgmon.asm	Debug Monitor
9000	D000	0800	<ccp>	Command Processor (CCP, ZCPR, etc.)
9800	D800	0E00	<dos>	Disk Operating System (BDOS, ZSDOS, etc.)
A600	E600	1600	<osbios>	OS BIOS (CBIOS, ZBIOS)
BC00	FC00	0400	hbios.asm	HBIOS Proxy

RAM Page 0 (Applications)

Loc	Org	Size	Contents
0000	0000	0100	Page Zero
0100	0100	7F00	Application (TPA)

RAM Page 1 (HBIOS Extension – Drivers)

Loc	Org	Size	Contents
0000	0000	0100	Page Zero
0100	0100	0100	Reserved (unused)
0200	0200	0200	System Configuration (dynamic)
0400	0400	0C00	Command processor cache area
1000	1000	7000	HBIOS

RAM Page N (Fixed 32K Upper Memory Area)

Loc	Org	Size	Contents
8000	8000	4000	TPA (continued from lower memory)
C000	C000	1000	TPA/Debug Monitor
D000	D000	0800	Command Processor (CCP, ZCPR, etc.)
D800	D800	0E00	Disk Operating System (BDOS, ZSDOS, etc.)
E600	E600	1600	OS BIOS (CBIOS, ZBIOS)
FC00	FC00	0400	HBIOS Proxy (HiMem Stub)