

# IMMANUEL GNANASEELAN

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## EDUCATION

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### Toronto Metropolitan University

Toronto, ON

*Bachelor of Engineering (Software Engineering)*

*Sept. 2022 – Present*

- Relevant Coursework: Data Structures & Algorithms, Digital Systems, Software Systems, Calculus I–III, Communication for Engineering

## TECHNICAL SKILLS

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**Languages:** Java, Python, C#, HTML, JavaScript, SQL, VHDL

**Tools/Technologies:** Jira, SQL Server, AWS S3, .NET, Windows Forms, FPGA, Version Control (Git)

**Professional Skills:** Data validation, documentation accuracy, customer support, testing

**Certifications:** WHMIS

## EXPERIENCE

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### GenesisCircle

May 2025 – Sept. 2025

*Software Quality Assurance Engineer*

*Fort Erie, ON*

- \* Performed functional and usability testing on enterprise fleet management software across multiple device types.
- \* Identified, documented, and categorized software bugs, supplying actionable reports to developers.
- \* Used Jira for bug tracking, workflow management, and ensuring milestone completion.
- \* Collaborated with QA and dev teams to validate updates, maintain app stability, and improve UI/UX performance.
- \* Resolved customer tickets efficiently and supported users in understanding new features and system updates.

## PROJECTS

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### Cloud Storage File Uploader | C#, .NET, AWS SDK, Windows Forms

- \* Developed a secure AWS S3 file uploader with the AWS SDK for .NET to enhance data confidentiality and reliability.
- \* Built an intuitive Windows Forms interface for seamless user file uploads.
- \* Implemented robust error-handling mechanisms for stable cloud upload operations.

### Clothing Retail Store DBMS | Java, Oracle SQL, Swing, JDBC

- \* Designed and implemented a BCNF-normalized Oracle database (8+ tables) including constraints, sample data, and advanced SQL queries.
- \* Built a Java Swing GUI connected through JDBC to manage schema and data operations such as table creation, deletion, and CRUD actions.
- \* Implemented analytical reporting features using joins, aggregation functions, and window functions.
- \* Enabled table browsing, record updates, and transactional integrity checks through modular backend logic.

### CPU & ALU Design | VHDL, FPGA, Digital Logic

- \* Designed a simple CPU architecture featuring an ALU, FSM-based control unit, decoders, and 8-bit registers.
- \* Simulated VHDL components to verify correct arithmetic and logical operations.
- \* Tested CPU functionality on FPGA hardware with 7-segment output visualization.