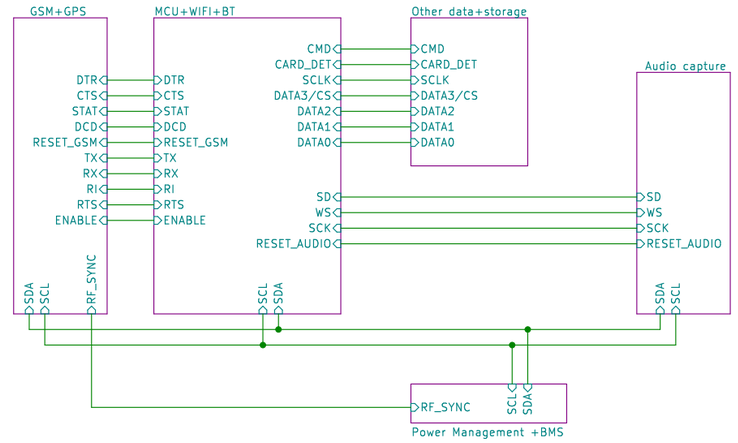
All SMD caps ceramic are 0603 16V X7R or better unless other notes are made
All SMD resistors are 0603
Stack up:
4 layer FR4 sig gnd gnd+sig sig
All non power traces should aim to be 50 ohm single ended impedance
unless other requirements are in place
Delata(T) aimed for is 20C or less



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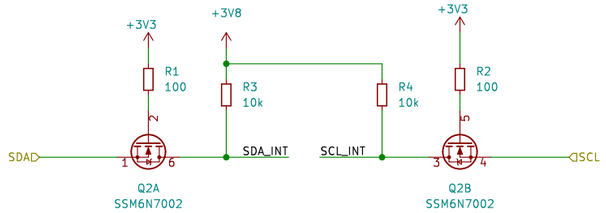

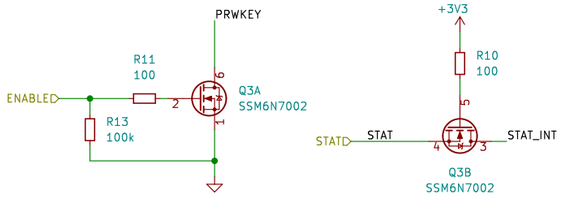

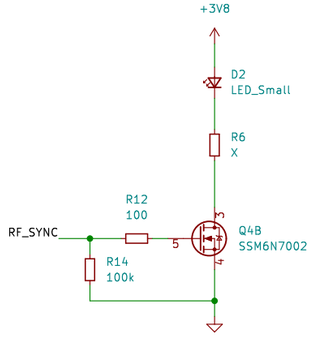

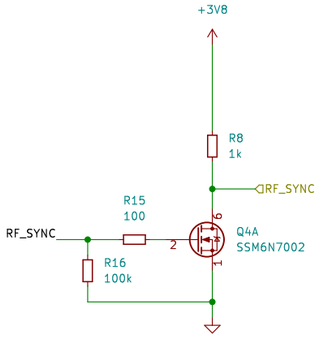

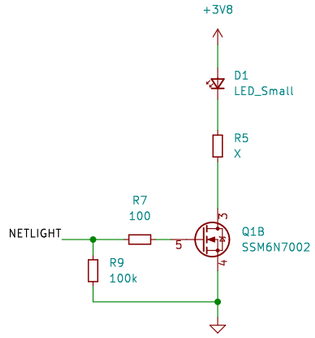

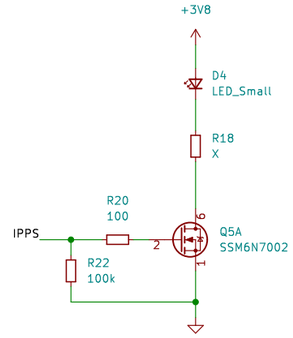

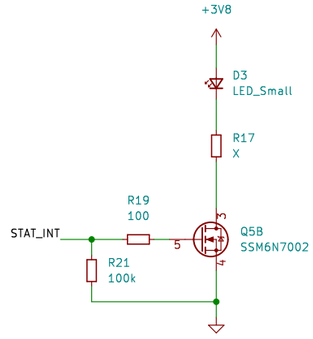

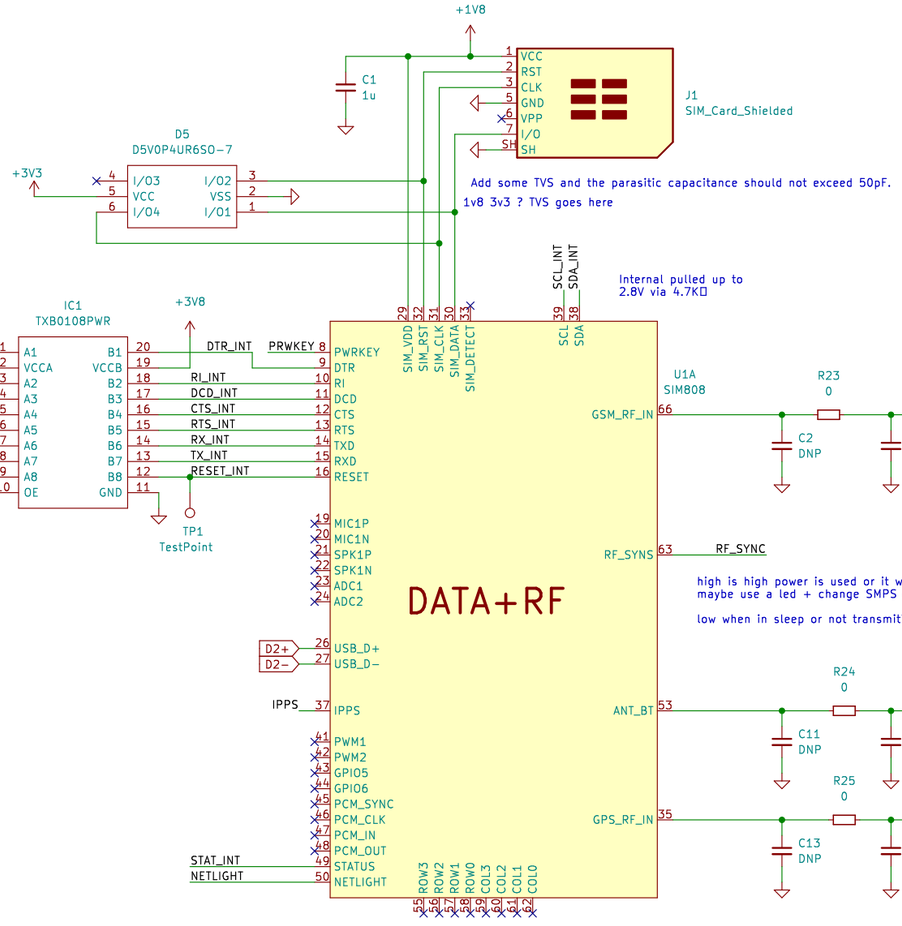

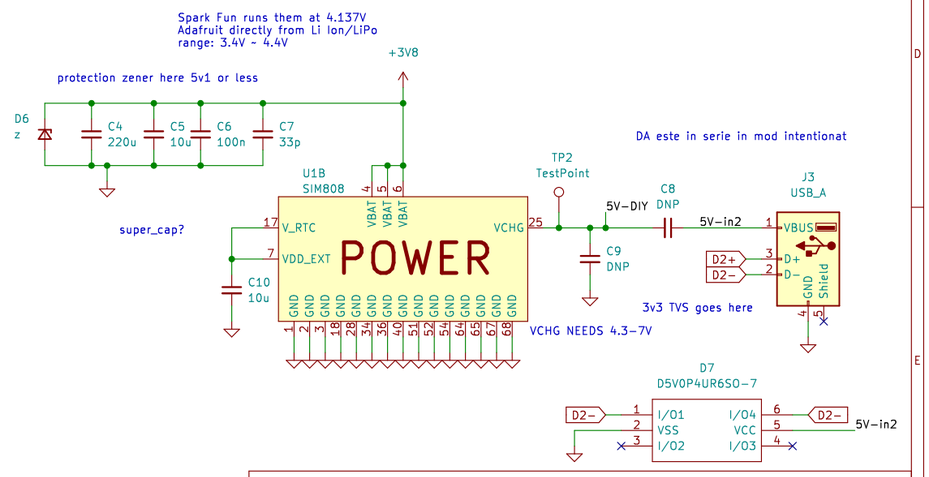

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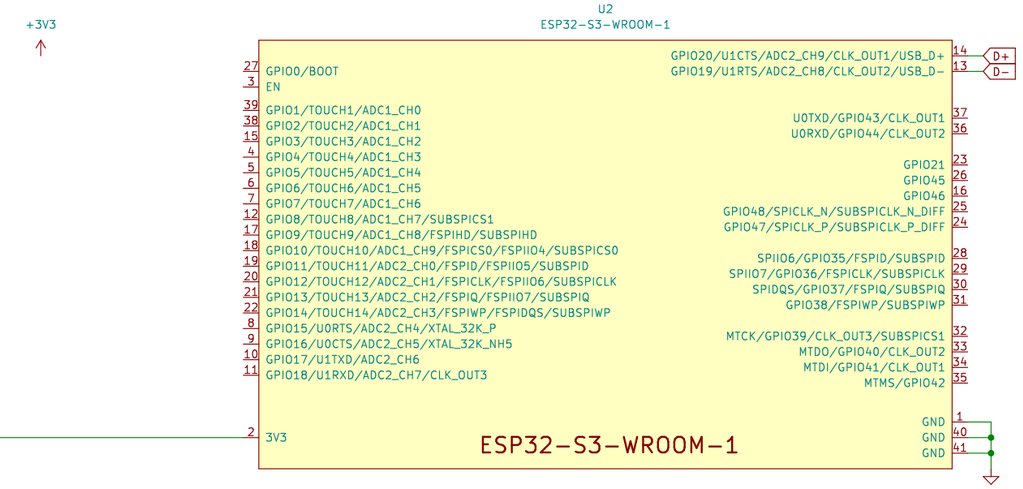
KiCad E.D.A. kicad (6.0.7)		Id: 2/7



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Size: A3	Date:	**Rev:**
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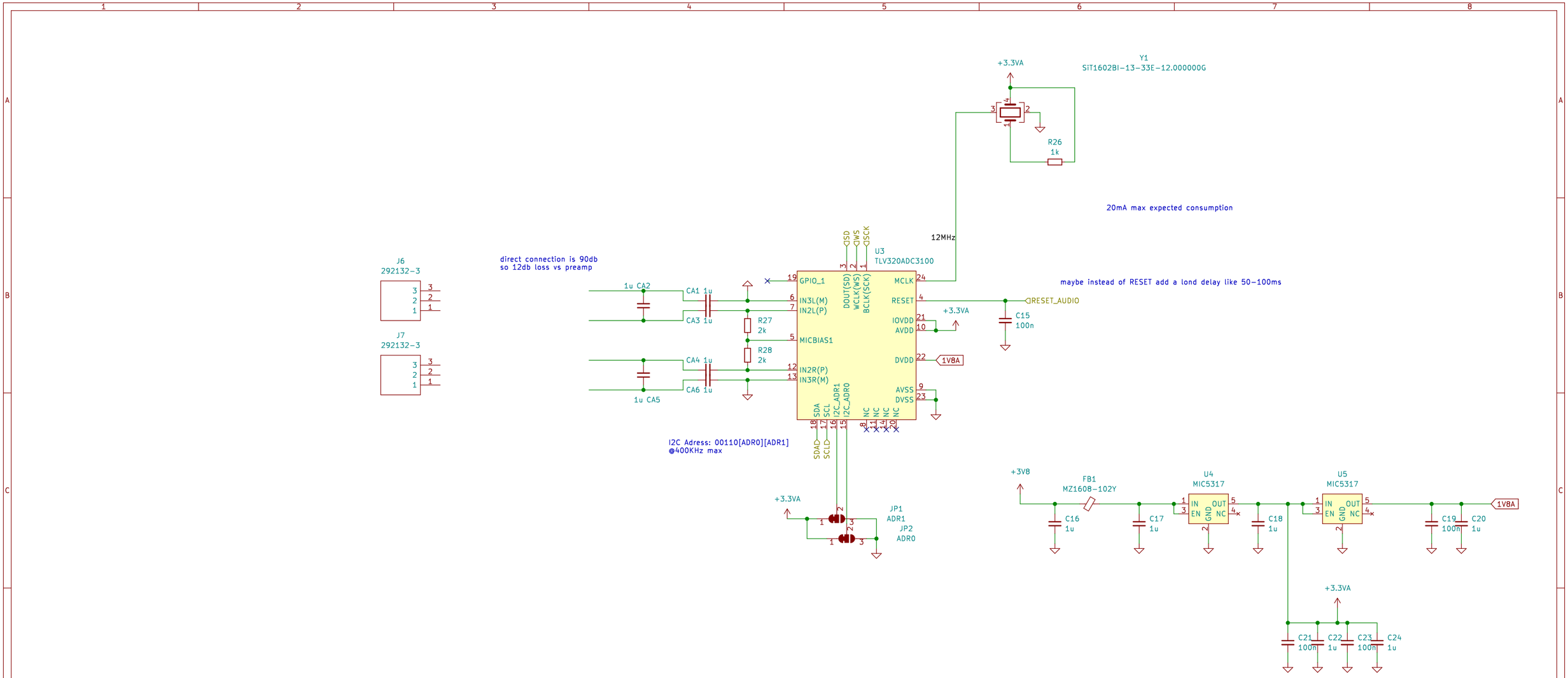

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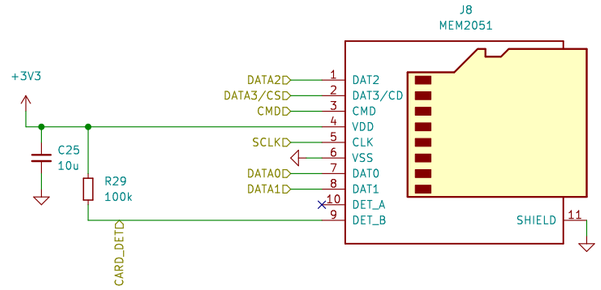
	Id: 4/7



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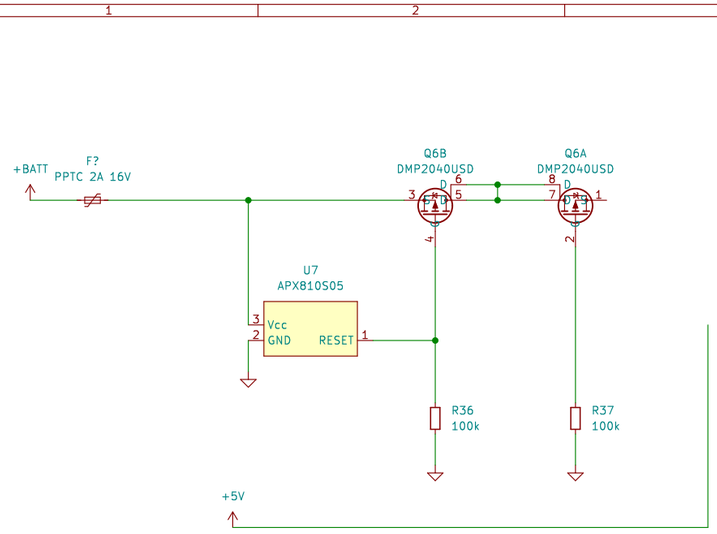
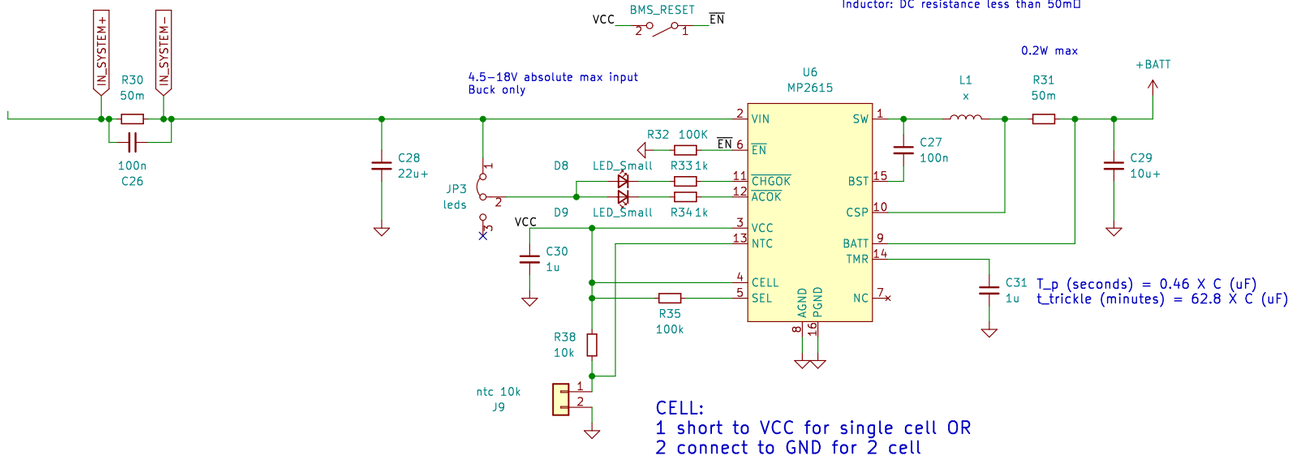
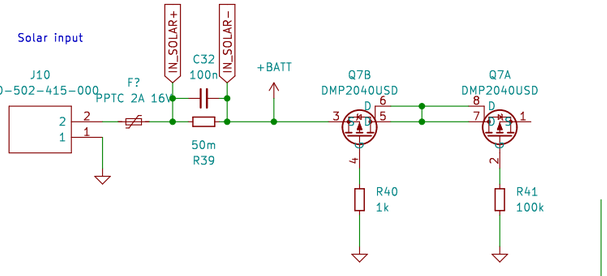


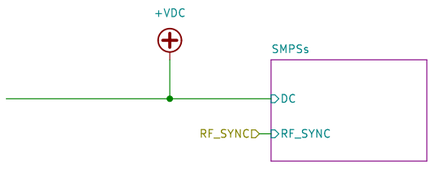


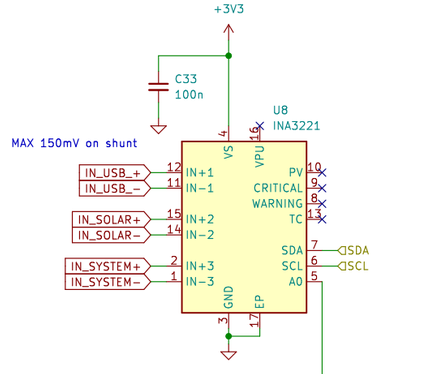
CELL:
1 short to VCC for single cell OR
2 connect to GND for 2 cell
SEL:
1 pull up for 4.1V (longer cell live)
2 pull down or float for 4.2V (more storage)

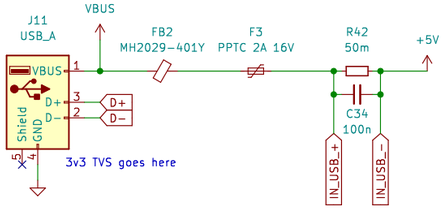
CHGOK:
Charging completion indicator. When this pin goes logic low, the device is charging. The pin becomes an open drain once the charge is completed or suspended.

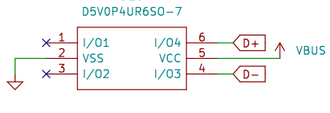
ACOK:
Valid input supply indicator. A logic low on this pin indicates the presence of a valid input power supply.











Sheet: /Power Management +BMS/		
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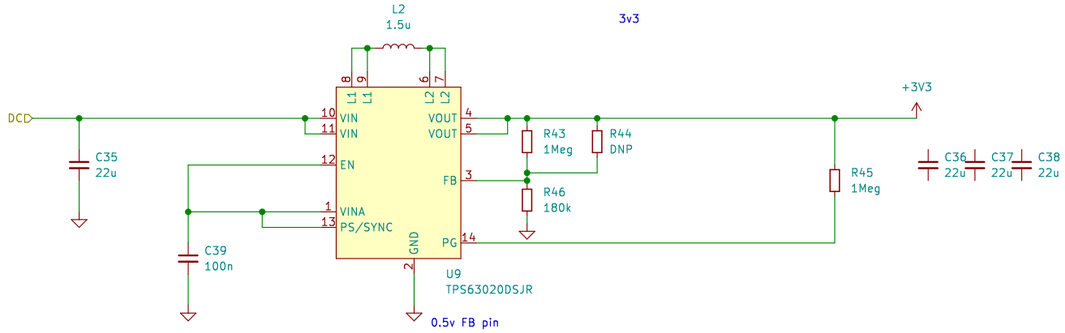
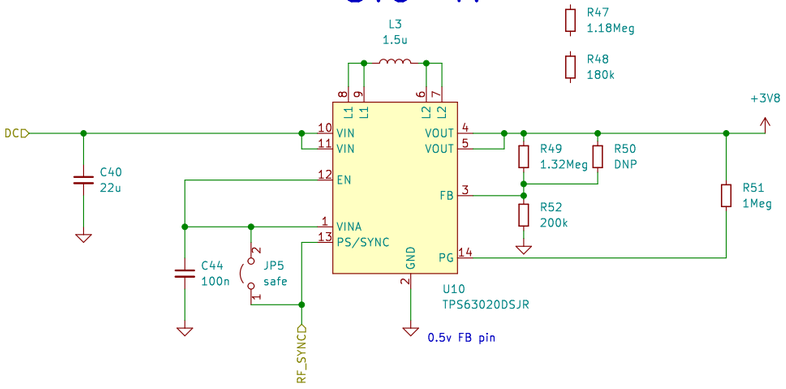
Size: A3	Date:	**Rev:**
KiCad E.D.A. kicad (6.0.7)		Id: 6/7

3V-3V3



3V8-4V



Sheet: /Power Management +BMS/SMPSs/		
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KiCad E.D.A. kicad (6.0.7)		Id: 7/7