Time

| Signal | Value |
|---|---|
| clock=1 | |
| dbg_fsm[3:0]=3 | 0 3 6 3 6 3 6 3 6 3 6 3 6 3 6 3 6 3 6 3 |
| dbg_xxx[3:0]=1 | 0 1 |
| Xi_0_in_0[31:0]=13 | + 59 13 1114374144 |
| Xi_0_in_1[31:0]=13 | + 59 13 1114374144 |
| Xi_1_in_0[31:0]=13 | + 59 13 1114374144 |
| Xi_1_in_1[31:0]=13 | + 59 13 1114374144 |
| io_out_0[31:0]=222784 | 0 222784 10816 0 1213829120 |
| io_out_1[31:0]=222784 | 0 222784 10816 0 1213829120 |
| pe_0_out_0[32:0]=676 | 0 + 13+ 676 0 1180274688 |
| pe_0_out_1[32:0]=676 | 0 13+ 676 0 1180274688 |
| pe_1_out_0[32:0]=676 | 0 + 13+ 676 0 1180274688 |
| pe_1_out_1[32:0]=676 | 0 13+ 676 0 1180274688 |
| pe_2_out_0[32:0]=676 | 0 + 13+ 676 0 1180274688 |
| pe_2_out_1[32:0]=676 | 0 13+ 676 0 1180274688 |
| pe_3_out_0[32:0]=676 | 0 + 13+ 676 0 1180274688 |
| pe_3_out_1[32:0]=676 | 0 13+ 676 0 1180274688 |
| addsubModule_0_out[32:0]=10816 | 0 + 22+ 10816 + + + 10816 0 1213829120 |
| addsubModule_1_out[32:0]=5408 | 0 + 11+ 5408 + + 5408 0 1205440512 |
| addsubModule_2_out[32:0]=5408 | 0 + 11+ 5408 + + 5408 0 1205440512 |
| addsubModule_3_out[32:0]=2704 | 0 + 55+ 2704 + 2704 0 1197051904 |
| addsubModule_4_out[32:0]=2704 | 0 + 55+ 2704 + 2704 0 1197051904 |
| addsubModule_5_out[32:0]=2704 | 0 + 55+ 2704 + 2704 0 1197051904 |
| addsubModule_6_out[32:0]=2704 | 0 + 55+ 2704 + 2704 0 1197051904 |
| use_int=x | |