

Diode current equation

$$I_{D} = I_{o}(e^{V_{D}/\eta V_{T}} - 1)$$

= $I_{o}e^{V_{D}/\eta V_{T}} - I_{o}$

- I_D is diode current
- I_o is reverse saturation current
- V_D is voltage across diode
- V_T is thermal voltage = T / 11600
- η is a constant = 1 for Ge and 2 for Si

 $\eta = Ideality Factor - indicates the nearness with which the considered diode behaves with respect to the ideal diode$

If it is 1, diode considered behaves as exactly as ideal diode

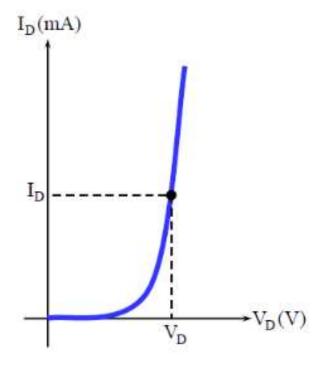
If it increases from 1, behavior of considered diode deviates from the ideal diode, greater is the deviation, greater is the value of η

Diode resistances

Static or DC resistance:

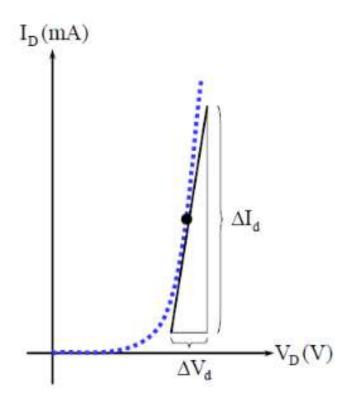
 ratio of diode voltage and diode current

$$R_D = \frac{V_D}{I_D}$$



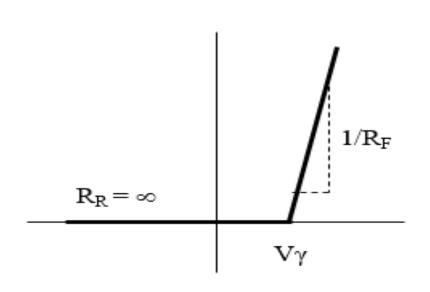
AC resistance:

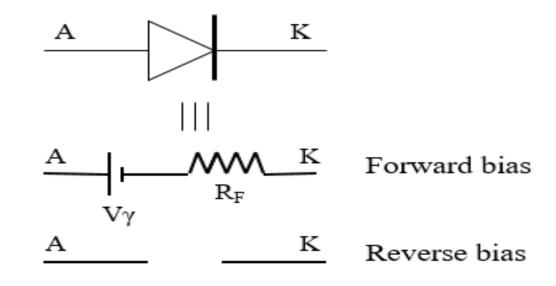
$$r_d = \frac{\Delta V_D}{\Delta I_D}$$
 $r_d = \frac{\Delta V_D}{\Delta I_D} \approx \frac{\eta V_T}{I_D}$



Diode Equivalent Circuit

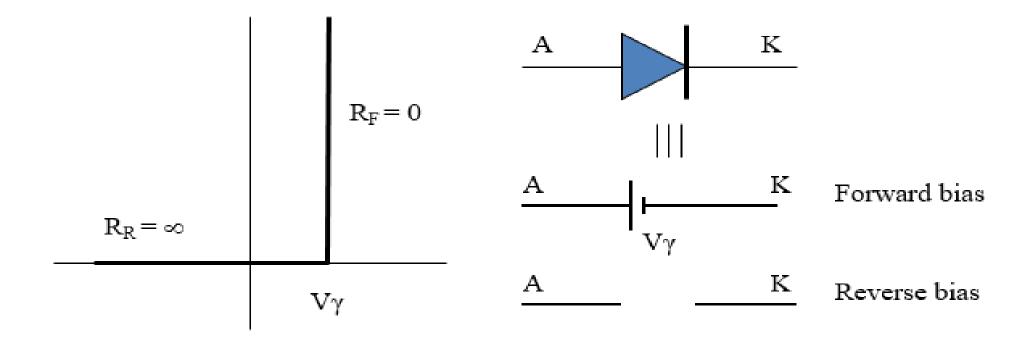
- Used during circuit analysis
- Characteristic curve replaced by straight-line segments





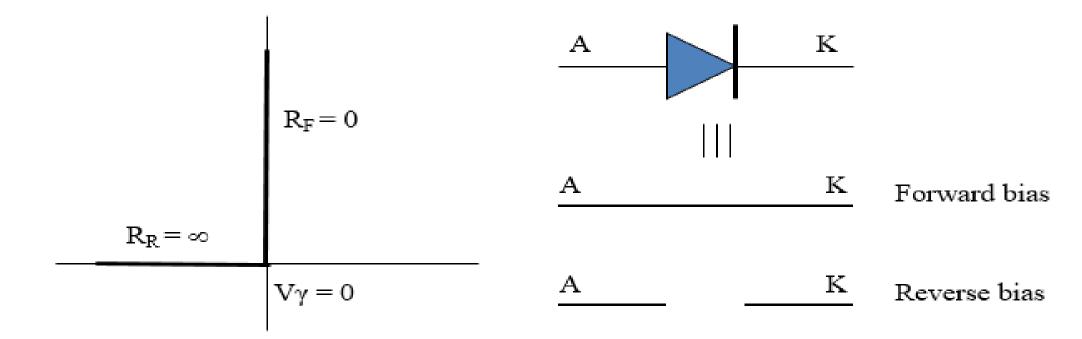
Diode Equivalent Circuit

 As further approximation, we can neglect the slope of the characteristic i.e., R_F = 0



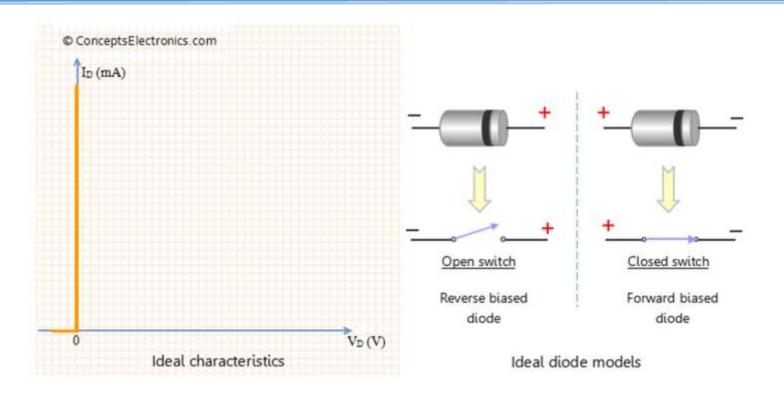
Diode Equivalent Circuit

 As third approximation, even the cut-in voltage can be neglected (Ideal diode)





Ideal diode: V-I characteristics



V-I characteristic of Ideal diode and ideal models

[http://conceptselectronics.com/diodes/diode-equivalent-models/].



Effect of Temperature on the Reverse current

Reverse saturation current approximately **doubles** for every **10 degree rise** in temperature.

$$I_{o2} = I_{o1} 2^{(T_2 - T_1)/10}$$

 I_{01} = Reverse saturation current at temperature T_1

 I_{02} = Reverse saturation current at temperature T_2

Note: Rise in temperature increases conductivity and thus increase in current

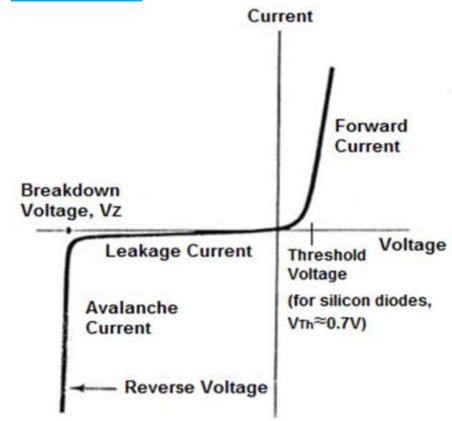


Breakdown in Reverse Biased Diodes

Large reverse current can damage the diode: this effect is called **breakdown** of diode

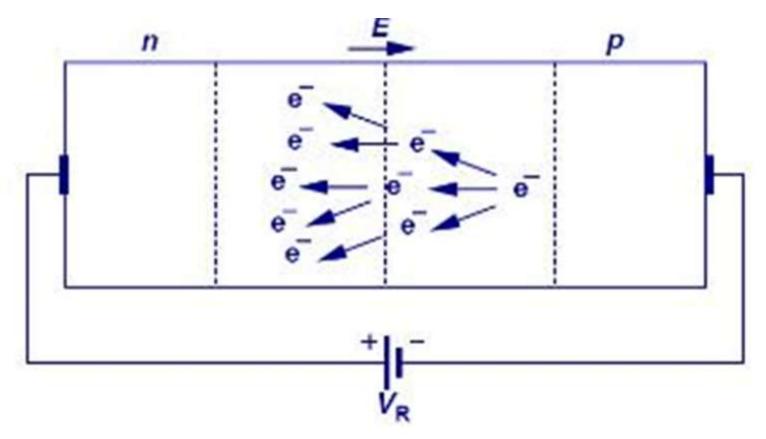
Two breakdown mechanisms:

- Avalanche breakdown :
 - Occurs in Lightly doped diodes,
 - Occurs at high reverse Voltage.
- Zener Breakdown:
 - Occurs in heavily doped diodes.
 - at lower reverse bias voltages.





Avalanche Breakdown

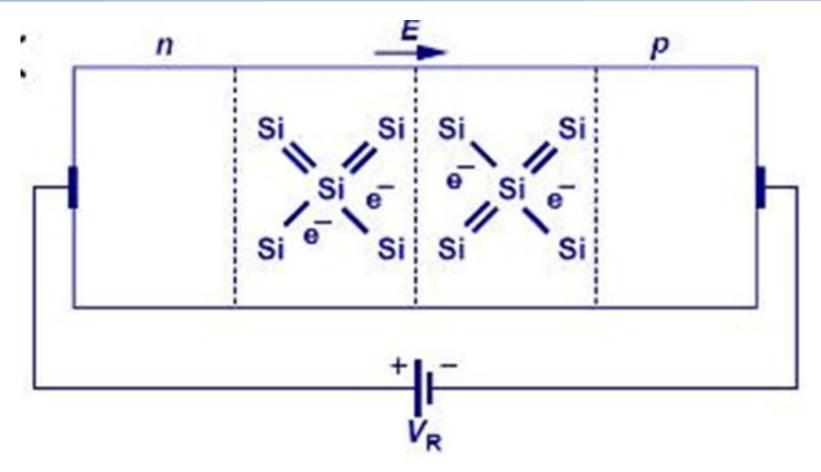


Schematic of Avalanche phenomenon

http://shrdocs.com/presentations/12656/index.html



Zener Breakdown



Schematic of Zener phenomenon

http://shrdocs.com/presentations/12656/index.html

P-N Junction Diode

1. A Silicon diode has a saturation current of 1pA at 20°C. Determine (a) Diode bias voltage when diode current is 3mA (b) Diode bias current when the temperature changes to 100°C, for the same bias voltage.

a.

$$I_D = I_0 \left(e^{\frac{V_D}{\eta V_T}} - 1 \right)$$

$$V_T = \frac{T}{11600} = \frac{293}{11600} = 25.25 \, mV$$

$$V_D = \eta V_T \ln \left(1 + \frac{I_D}{I_0} \right) = 1.103 V$$

b.

$$I_{02} = I_{01} 2^{(T_2 - T_1)/10} = 10^{-12} \left(2^{\frac{(100 - 20)}{10}} \right) = 256 pA$$

$$I_D = I_0 \left(e^{\frac{V_D}{\eta V_T}} - 1 \right)$$

$$I_{D} = 256x10^{-12} \left(e^{\frac{1.103}{(2x32.15x10^{-3} - 1)}} \right) = 7.21 mA$$



Application of P-N Junction Diode

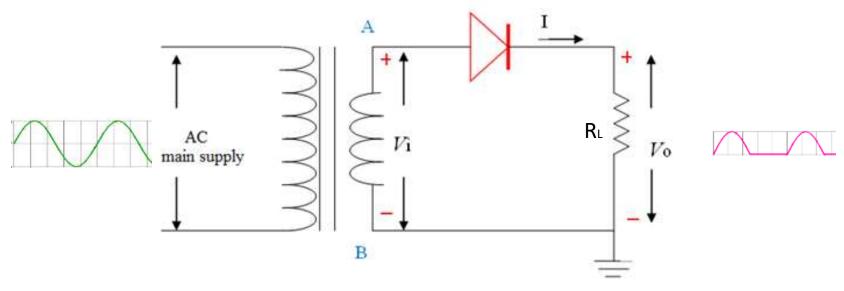
RECTIFIER

Converts AC signal to pulsating DC

Primary element: Diode

- Two types of rectifiers are: Half wave rectifier (HWR)
 Full wave rectifier (FWR)
- Full wave rectifier
 - Center-tapped transformer FWR
 - Bridge FWR



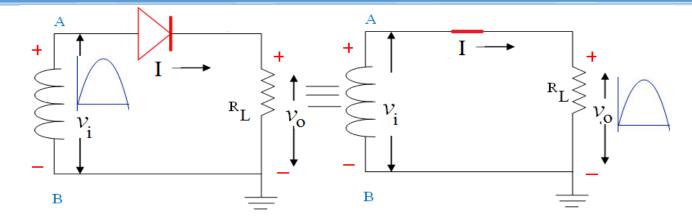


Circuit Diagram of HWR

- In HWR, the rectifying element conducts only during one half cycle of the input. Mostly during positive half cycle.
- Diode passes current only for half of the signal time period. Hence the name HWR.

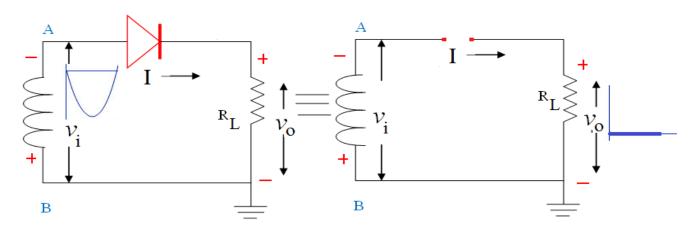


For positive half cycle



Equivalent Circuit of HWR, when node A is positive w.r.t node B

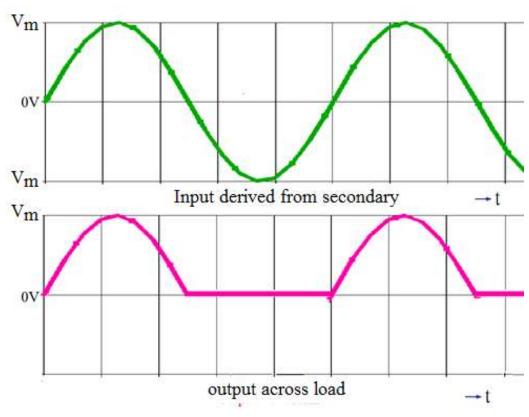
For negative half cycle



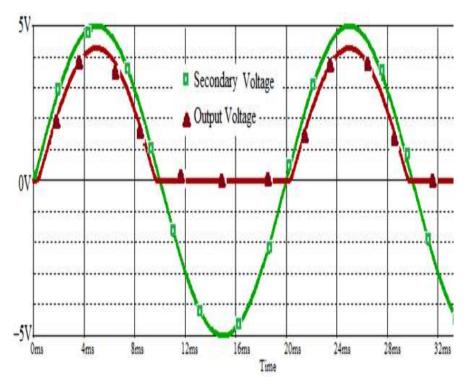
Equivalent Circuit of HWR, when node A is negative w.r.t node B

Note: Current through load exist only for one half cycle





Input and rectified output with ideal diode



Input and rectified output with practical diode



Performance of Rectifiers is measured using the following parameters:

Φ During positive half cycle of the input, $i_{\perp} = I_m \sin(\omega t)$, 0 to π

Peak current
$$(I_m)$$

$$I_m = \frac{V_m - V_{\gamma}}{R_L + R_F} \approx \frac{V_m}{R_L}$$

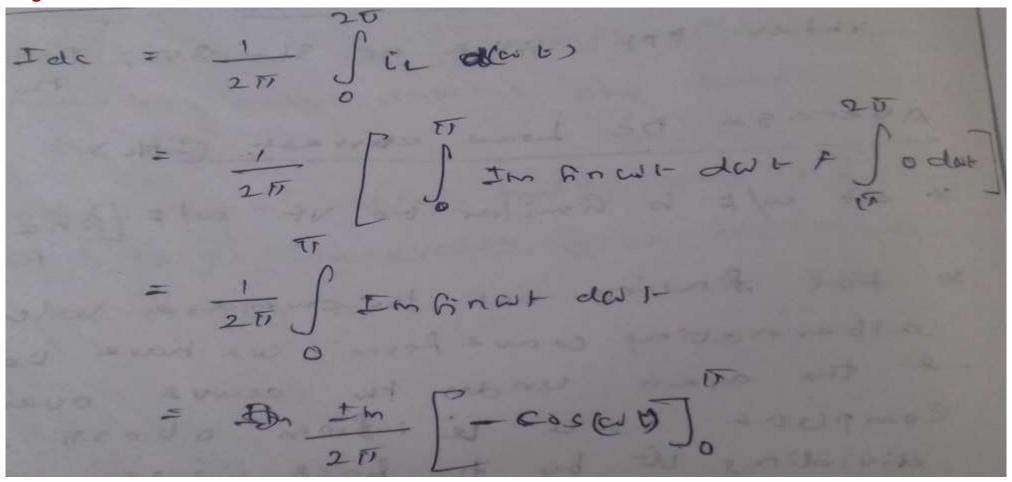
Φ During negative half cycle of the input, $i_L = 0$, π to 2π

Average value of load current in half wave rectifier is non zero.

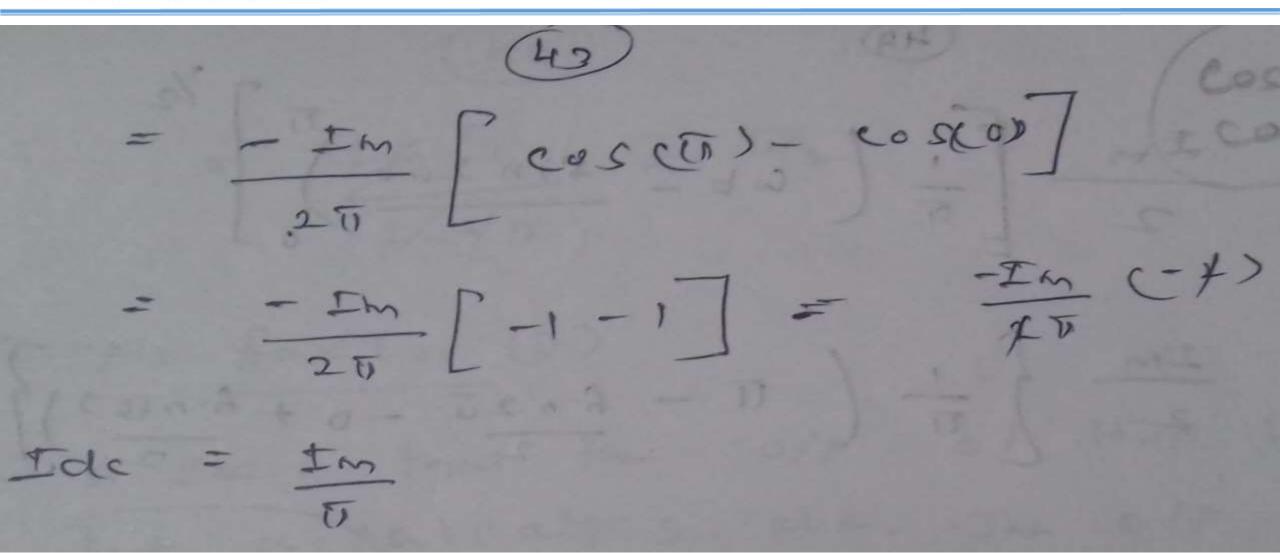
$$I_{dc} = \frac{1}{2\pi} \int_{0}^{2\pi} i_{L} d(\omega t) = \frac{I_{m}}{\pi}$$



Average value of load current :









• Average output voltage is $V_{dc} = I_{dc} R_{L}$

$$V_{dc} = I_{dc}R_{L}$$

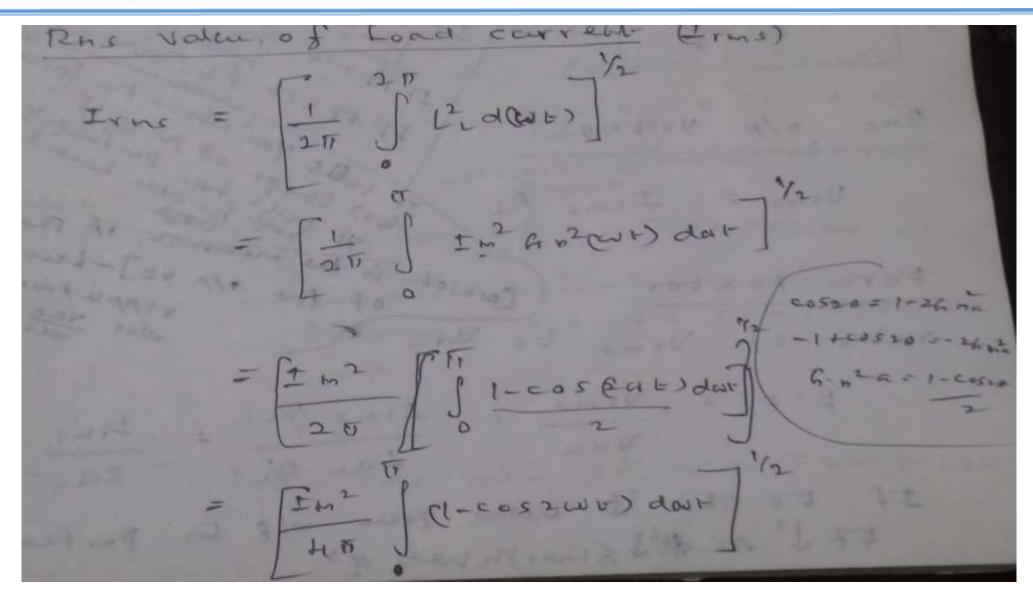
■ RMS value of load current in half wave rectifier is:

$$I_{rms} = \left[\frac{1}{2\pi} \int_{0}^{2\pi} i_{L}^{2} d(\omega t)\right]^{\frac{1}{2}} = \frac{I_{m}}{2}$$

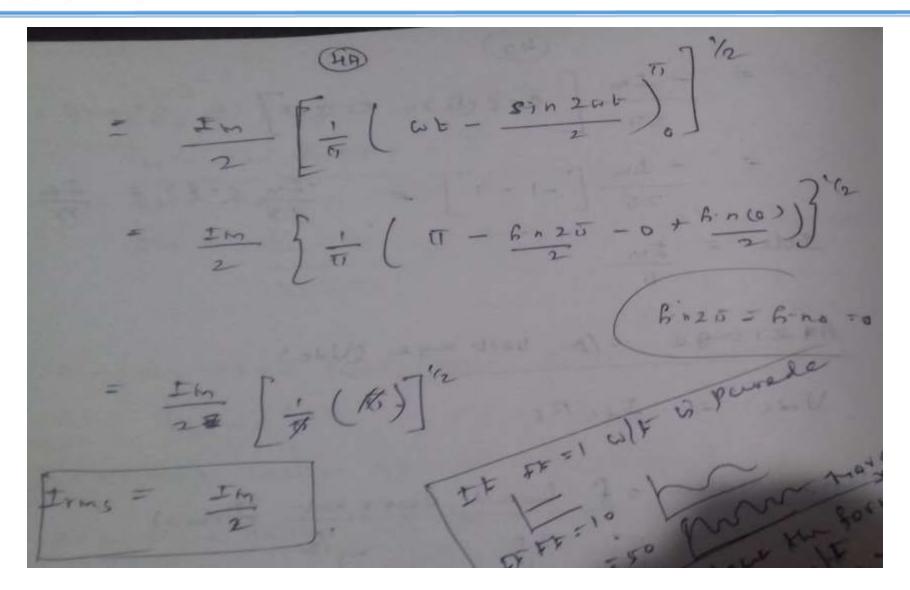
• RMS output voltage is $V_{rms} = I_{rms} R_{I}$

$$V_{rms} = I_{rms}R_{L}$$









Ripple Factor (γ):

It helps in the measure of ripples (pulsating components) present in the output of rectifier.

It tells how smooth is the output. Smaller the ripple factor closer is the output to pure D.C

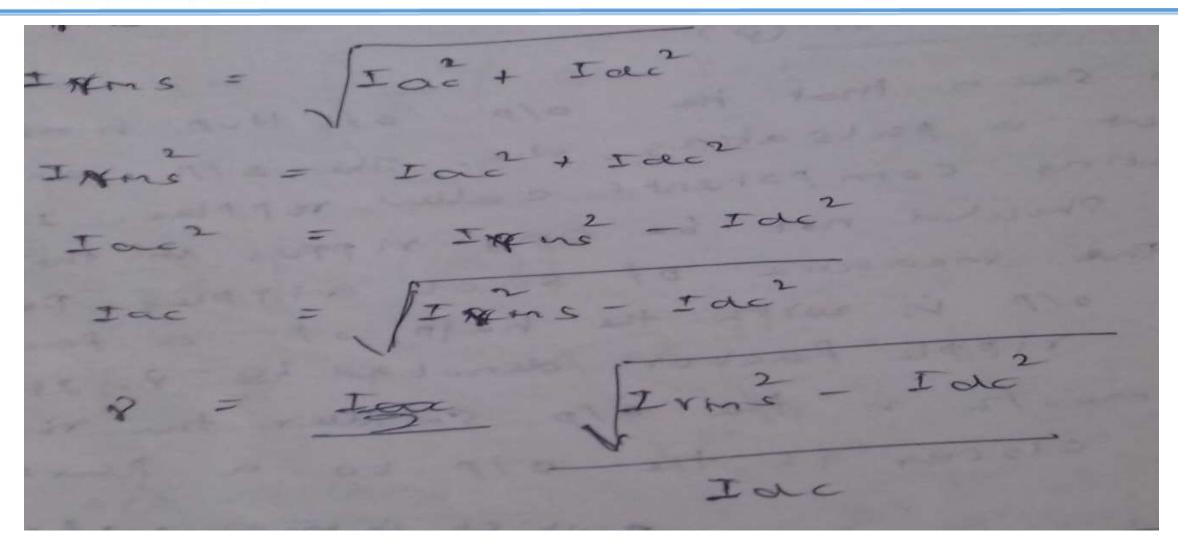
Ripple Factor
$$= \frac{RMS \ value \ of \ A.C \ component \ of \ output}{Average \ (or) \ D.C \ component \ of \ output} = \frac{I_{ac}}{I_{dc}}$$

lac = RMS value of A.C component present in output

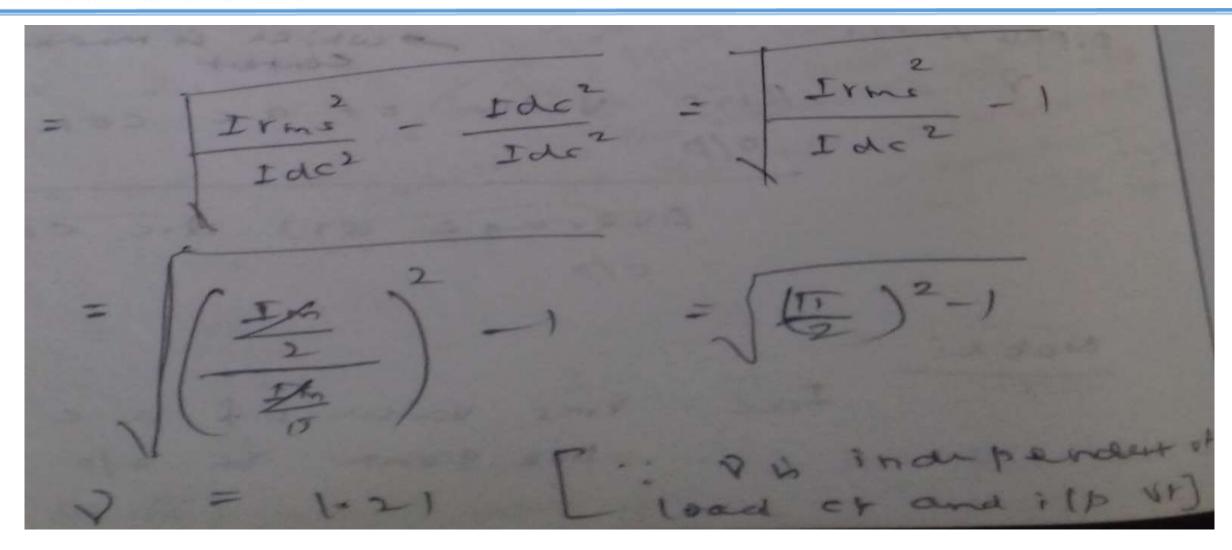
Idc = D.C component present in output

Irms = RMS value of total output current











Pie Power of P (Pdc)

$$Pdc = Idc^{2} R_{L}$$

$$= \left(\frac{Im}{D}\right)^{2} R_{L} = \frac{Im^{2}}{D^{2}} R_{L}$$

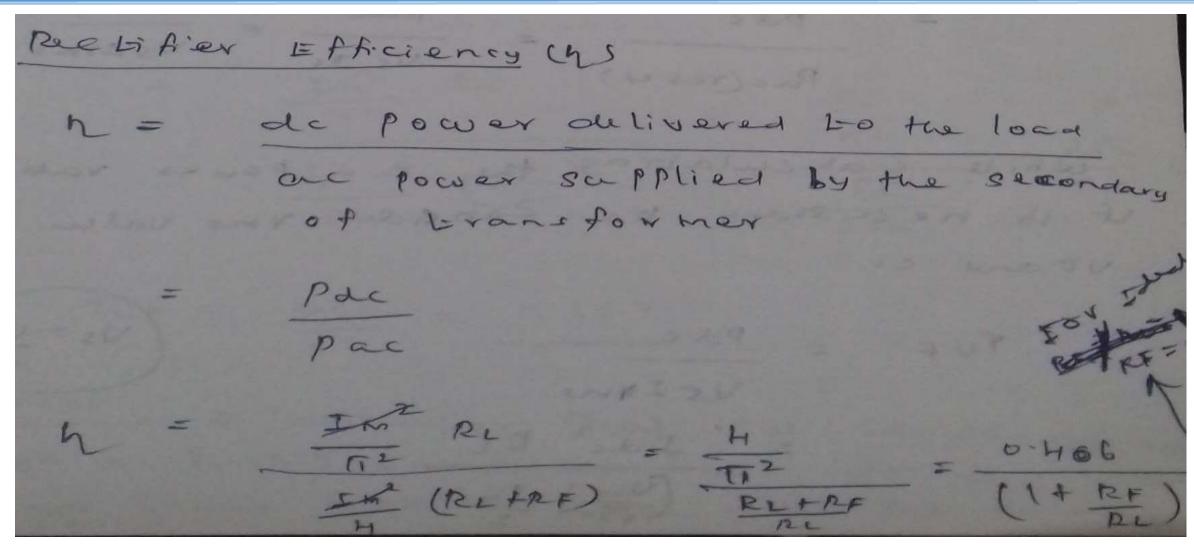
$$Pac = Idc^{2} R_{L} + R_{E}$$

$$Pac = Idc^{2} R_{L} + R_{E}$$

$$Pac = \left(\frac{Im}{D}\right)^{2} (R_{L} + R_{E})$$

$$= \frac{Im^{2}}{D} (R_{L} + R_{E})$$



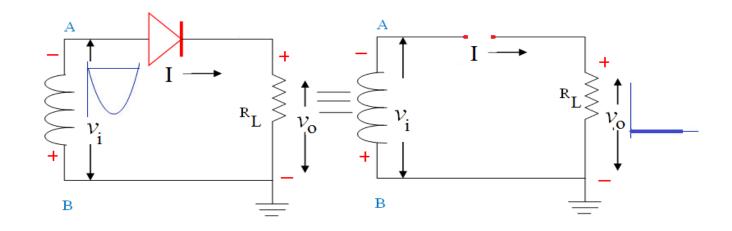


$$\eta = 40.6\%$$

Peak Inverse Voltage (PIV):

PIV is the peak voltage across the diode in the reverse biased condition

Reverse biased condition



 $PIV = V_m$



Advantages of HWR

- Simple circuit
- Single diode
- PIV rating is V_m

Disadvantages of HWR

- High ripple factor
- Low efficiency



A half clove the ther circumt is supplied from a 230 V, 50 H2 Scr ppy with a step. down valo of all to a verone load of toka. The divde forward YP filtrance is 75. while transformer secondary registance is los. Ral culate moximum, averase, values of curvent, De o/p voltage; vactification afficiency and vipple factor



Solution:

