

Z80_SBC2, 16KB (0KB paged) ROM, 48K (64K paged) RAM
Mix of Z80_SBC1 with RC2014 Pageable ROM and some additional logic

cpu

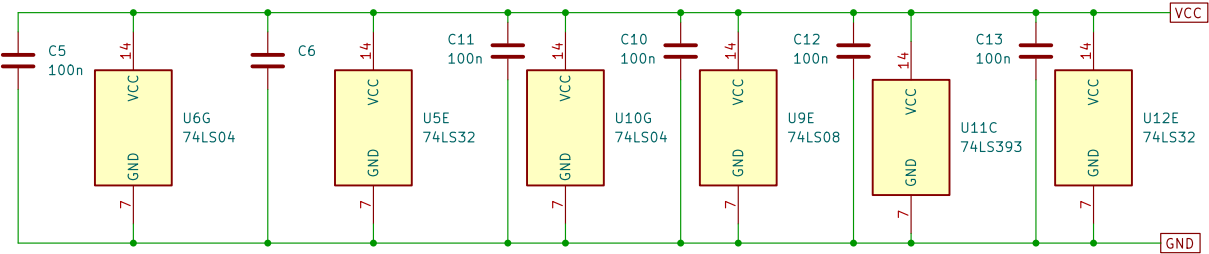
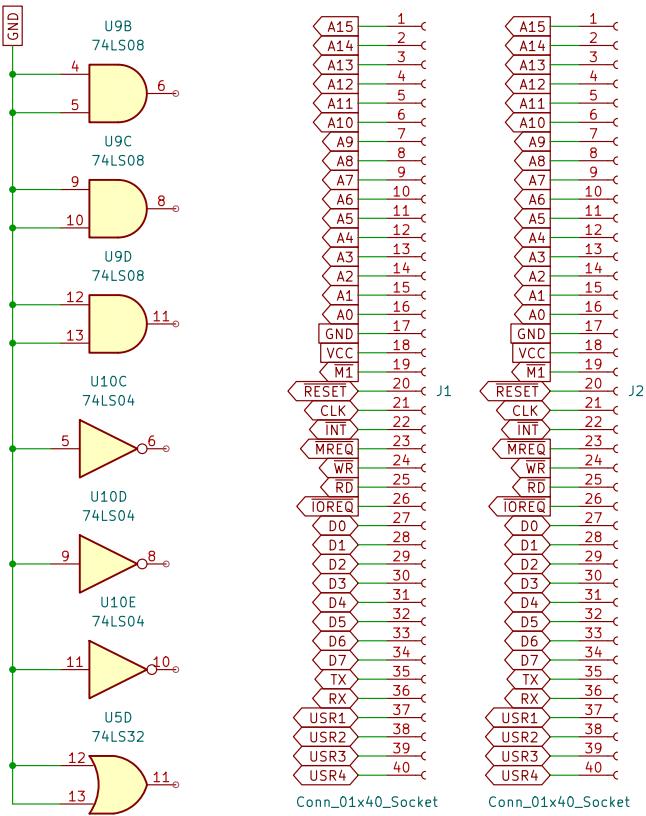


File: cpu.kicad_sch

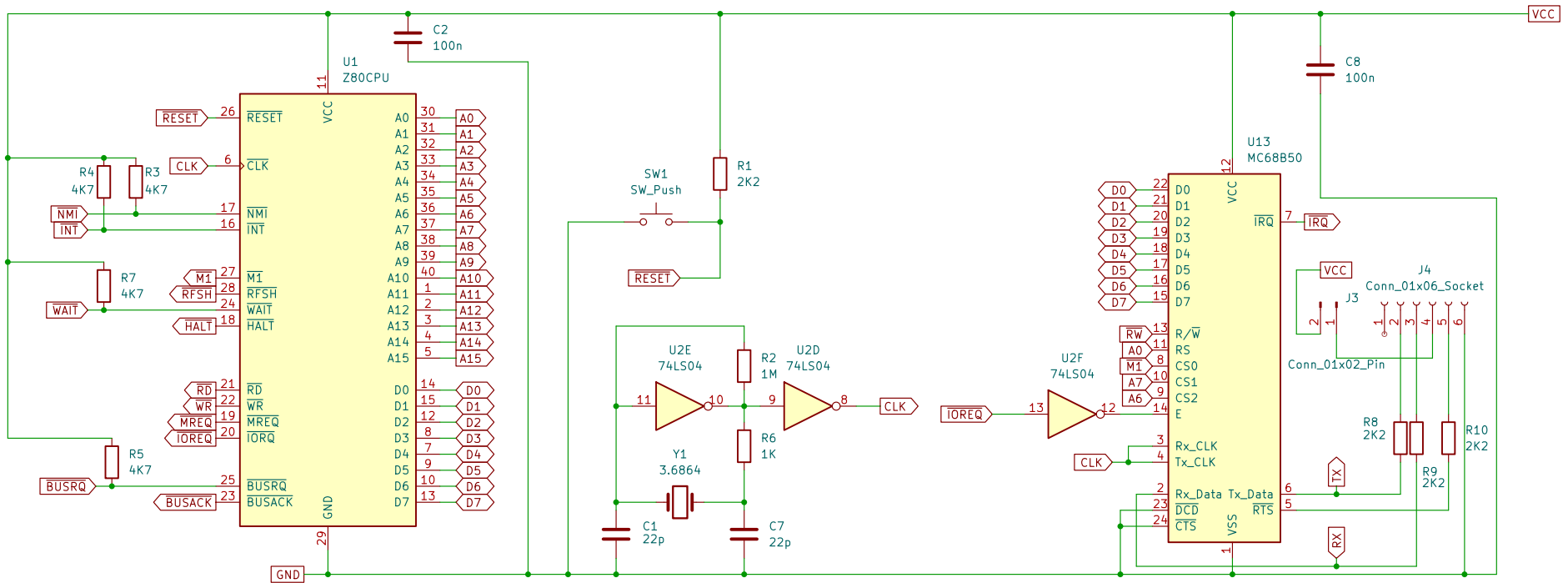
mem



File: mem.kicad_sch



Z80_SBC2, 16KB (0KB paged) ROM, 48K (64K paged) RAM
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