

A

A

B

B

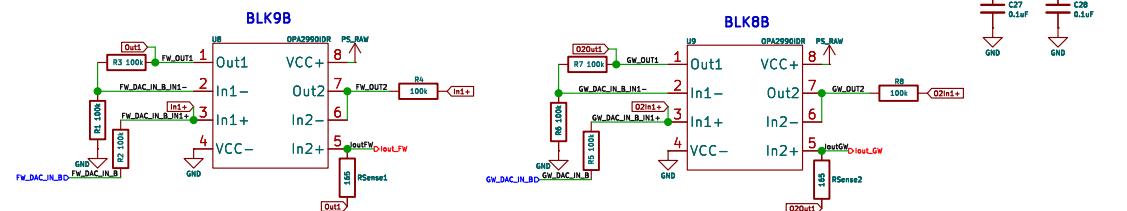
C

C

D

D

OPAMs powered by +12 out of the BLKB 8/9 each  
 Current OUT will be between 0–20mA range

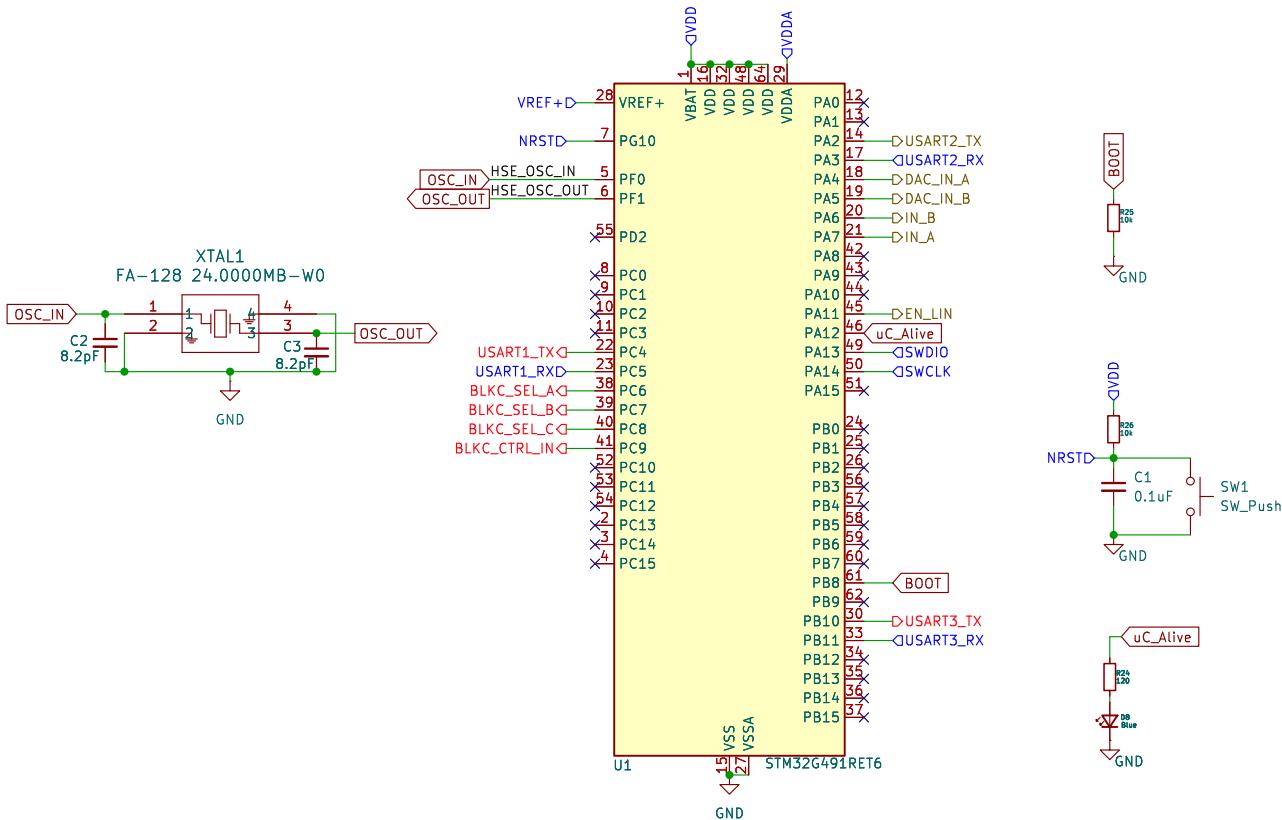


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 KiCad E.D.A. 9.0.6

**Rev:**  
 Id: 2/6

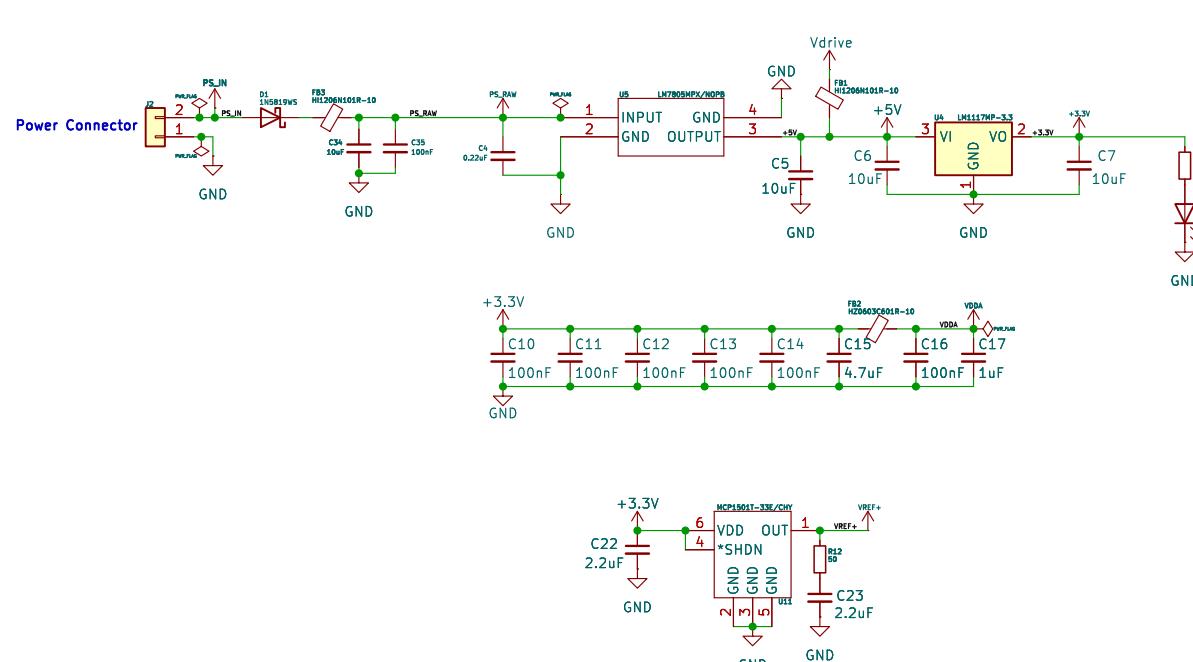


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**Rev:**  
Id: 3/6



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KiCad E.D.A. 9.0.6

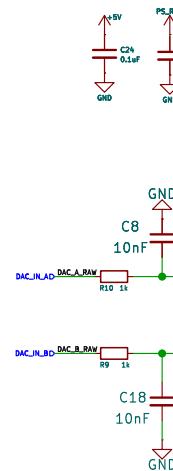
**Rev:**  
Id: 4/6

With a LOW Input(IN1 or IN2), NC channel is connected to COM by default

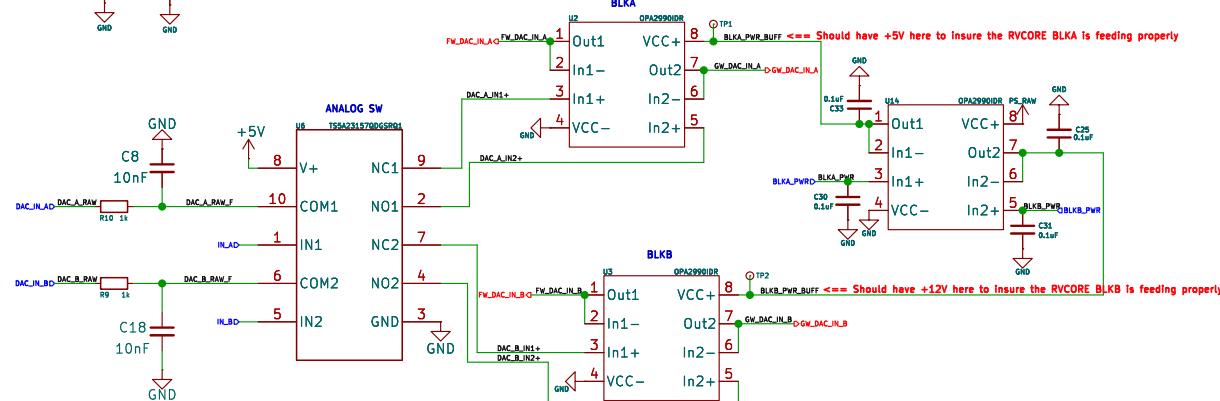
A

A

Bypass filters for the AN. SW.



These DUAL OPAMPS CHIPS  
are acting as Buffers



B

B

C

C

D

D

Sheet: /DAC\_PROT&SWITCH/  
File: dac\_prot&switch.kicad\_sch

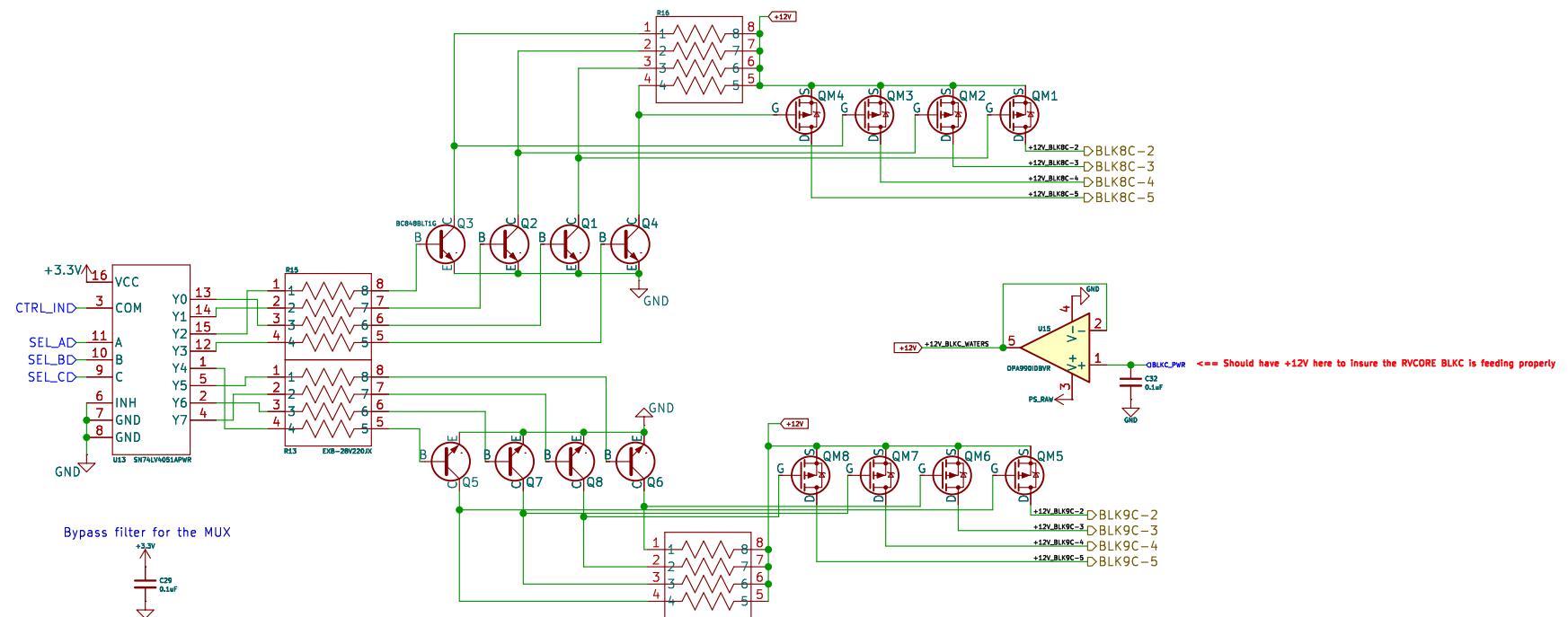
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**Rev:**

Id: 5/6



Sheet: /BLK-C-Water/  
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Size: A4 Date:  
KiCad E.D.A. 9.0.6

**Rev:**  
Id: 6/6