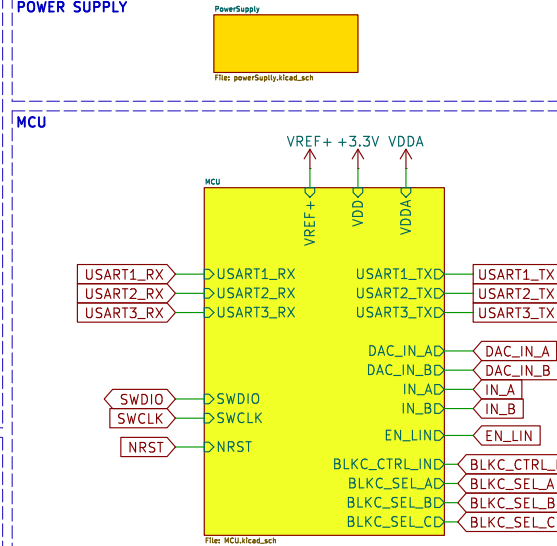
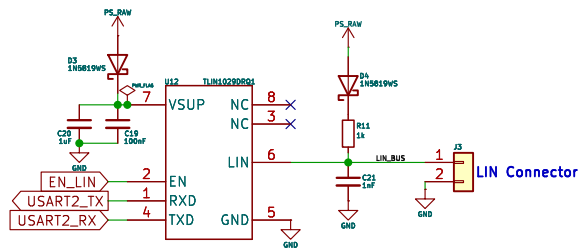


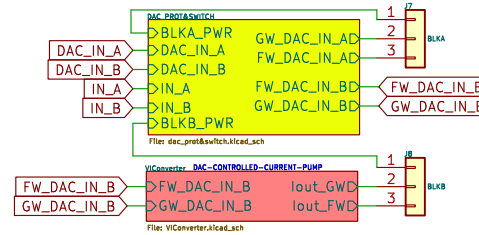
POWER SUPPLY



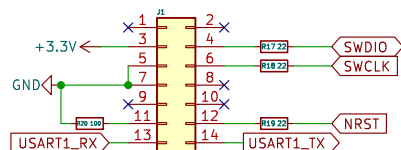
LIN-UART



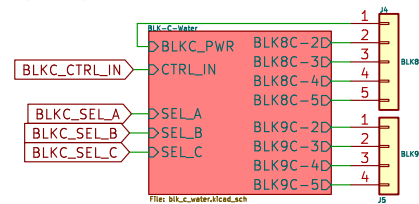
DAC-ADAPTER



ST-LINK



BLKC-WATER-CTRL



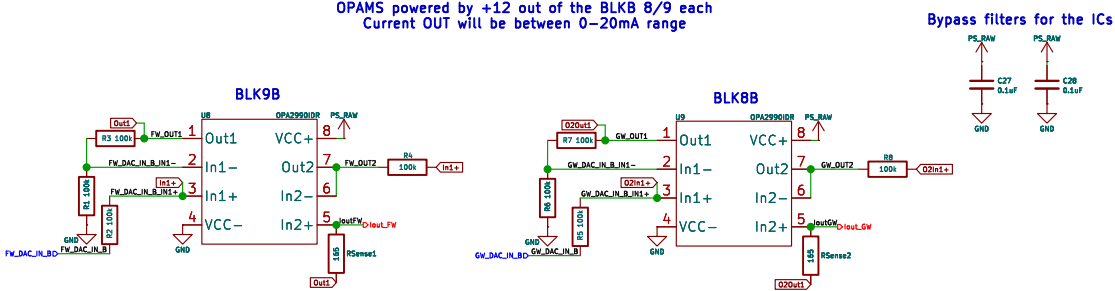
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Rev:
Id: 1/6

OPAMS powered by +12 out of the BLKB 8/9 each
Current OUT will be between 0–20mA range



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Title:

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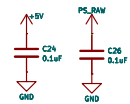
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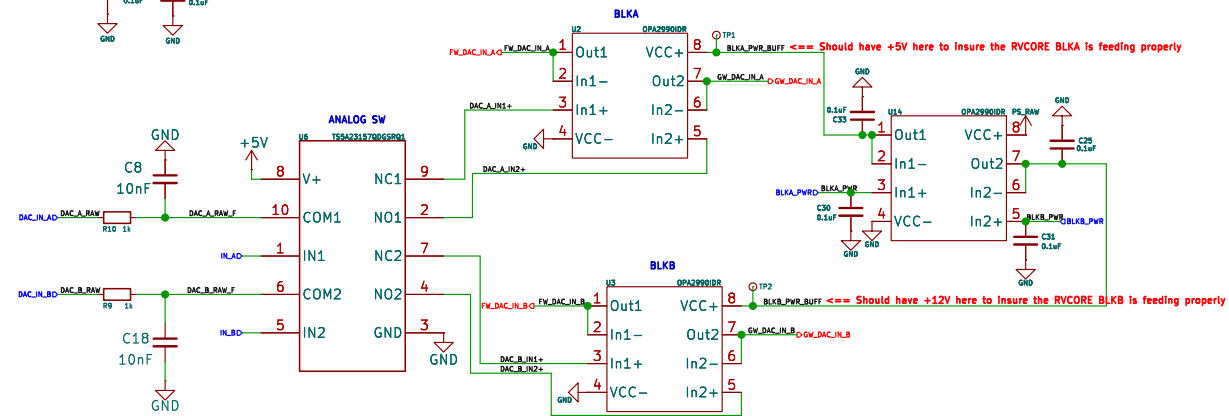
Id: 4/6

With a LOW Input(IN1 or IN2), NC channel is connected to COM by default

Bypass filters for the AN. SW.



These DUAL OPAMPS CHIPS
are acting as Buffers



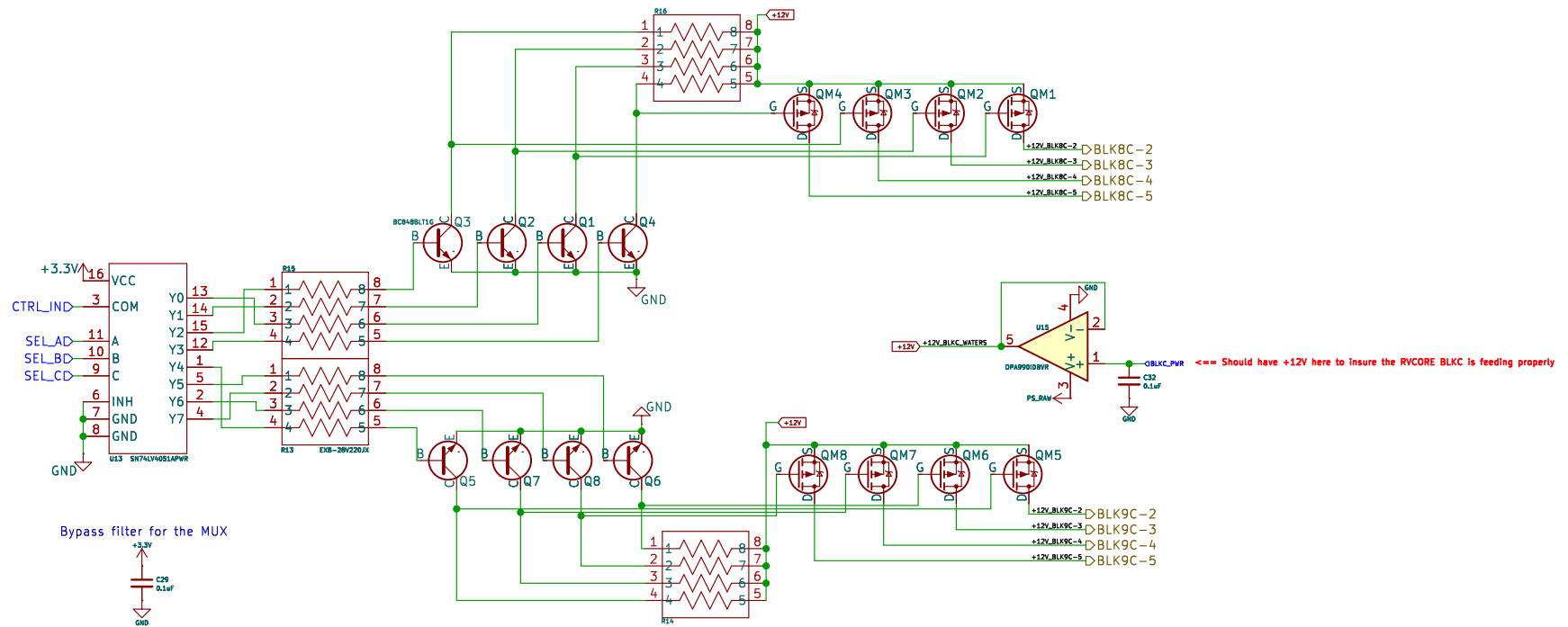
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Date:

Rev:
Id: 5/6



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KiCad E.D.A. 9.0.6

Rev:

Id: 6/6