

| List Of Subjects Allocated |  |   |       |         |                 |                         |
|----------------------------|--|---|-------|---------|-----------------|-------------------------|
| Subject No                 | Subject Name                           | Faculty Name  | Ltp   | Credits | Slot            | Room                    |
| EC21201                    | BASIC ELECTRONICS                      | Basudev Lahiri,Tarun Kanti Bhattacharyya,Shailendra Kumar Varshney,Aniket Singha  | 3-1-0 | 4       | C41,C42,C43,C44 | NR121,NR221,NR222,NR321 |
| EC21202                    | DIGITAL ELECTRONIC CIRCUITS            | Goutam Saha   | 3-1-0 | 4       | C41,C42,C43,C44 | V4                      |
| EC21204                    | LINEAR ALGEBRA AND OPTIMIZATION MODELS | Arijit De,Mrityunjoy Chakraborty  | 3-1-0 | 4       | E41,E42,E43,E44 | V4,V4                   |
| EC21206                    | ELECTROMAGNETIC ENGINEERING            | Sarang Pendharker   | 3-1-0 | 4       | F41,F42,F43,F44 | V4                      |
| EC21208                    | SIGNALS AND SYSTEMS                    | Subhadip Mukherjee  | 3-1-0 | 4       | D41,D42,D43,D44 | V4                      |
| EC21210                    | SYSTEMS AND CONTROL                    | Ritwik Kumar Layek  | 3-1-0 | 4       | U41,U42,U43,U44 | V4                      |
| EC29003                    | INTRODUCTION TO ELECTRONICS LAB.       | Shailendra Kumar Varshney,Bratin Ghosh,Ratnam Varada Raja Kumar   | 0-0-3 | 2       | X               |                         |
| EC29201                    | BASIC ELECTRONICS LABORATORY           | Pechetti Sasi Vinay,Rajat Roy,Ratnam Varada Raja Kumar,Shailendra Kumar Varshney,Amitabha Bhattacharya,Prasanta Kumar Guha,Rajarshi Roy,Bratin Ghosh,Sudipta Mukhopadhyay,Subhadip Mukherjee,Shailendra Kumar Varshney,Sarang Pendharker,Sarang Pendharker,Mrinal Kanti Mandal,Sudipta Mukhopadhyay | 0-0-3 | 2       | J,L,N,P,X       |                         |
| EC29202                    | DIGITAL CIRCUITS LABORATORY            | Chetna Singhal,Chetna Singhal,Indrajit Chakrabarti,Goutam Saha,Sharba Bandyopadhyay,Goutam Saha   | 0-0-3 | 2       | N,P,X41,X42,X43 |                         |
| EC30202                    | COMPUTER ARCHITECTURE                  | Prabir Kumar Biswas   | 3-0-0 | 3       | F31,F32,F33     | NR322                   |
| EC31002                    | DIGITAL COMMUNICATION                  | Amitalok Jayant Budkuley  | 3-1-0 | 4       | D41,D42,D43,D44 |                         |
| EC31003                    | DIGITAL ELECTRONIC CIRCUITS            | Goutam Saha   | 3-1-0 | 4       | C41,C42,C43,C44 | NR322                   |
| EC31004                    | VLSI ENGG.                             | Gourab Dutta  | 3-0-0 | 3       | A31,A32,A33     | NR422                   |
| EC31202                    | DIGITAL SIGNAL PROCESSING- II          | Sharba Bandyopadhyay,Arijit De  | 3-1-0 | 4       | C41,C42,C43,C44 | NR422,NR422             |
| EC31204                    | COMMUNICATION-II                       | Amitalok Jayant Budkuley  | 3-1-0 | 4       | D41,D42,D43,D44 | NR322                   |
| EC39002                    | DIGITAL COMMUNICATION LABORATORY       | Jithin R,Jithin R,Amitalok Jayant Budkuley,Pechetti Sasi Vinay  | 0-0-3 | 2       | J,L             |                         |
| EC39003                    | DIGITAL ELECTRONIC CIRCUITS LAB.       | Goutam Saha,Chetna Singhal  | 0-0-3 | 2       |                 |                         |
| EC39004                    | VLSI LABORATORY                        | Anindya Sundar Dhar,Gourab Dutta,Gourab Dutta,Vivek Dixit   | 0-0-3 | 2       | J,N             |                         |
| EC39202                    | EMBEDDED SYSTEMS LABORATORY            | Prabir Kumar Biswas,Pradip Mandal,Sudipta Mahapatra,Sudipta   | 0-0-0 | 2       | N,P             |                         |

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|         |  | Mahapatra   | 3     |   |                 |             |
| EC49001 | MICROCONTROLLER SYSTEMS LABORATORY                       | Sudipta Mahapatra,Prabir Kumar Biswas   | 0-0-3 | 2 | P               |             |
| EC49202 | DESIGN LABORATORY  | Jithin R,Subhadip Mukherjee,Saumik Bhattacharya,Ritwik Kumar Layek,Sarang Pendharker,Pradip Mandal,Pechetti Sasi Vinay,Indrajit Chakrabarti,Debashis Sen,Arijit De,Amitalok Jayant Budkuley | 0-0-3 | 2 | X               |             |
| EC60002 | COMPUTER VISION  | Debashis Sen  | 3-1-0 | 4 | V41,V42,V43,V44 | F12         |
| EC60004 | NEURONAL CODING OF SENSORY INFORMATION                   | Sharba Bandyopadhyay  | 3-0-0 | 3 |                 |             |
| EC60012 | ADVANCED OPERATING SYSTEMS DESIGN                        | Raja Datta  | 3-1-0 | 4 | E41,E42,E43,E44 | F3          |
| EC60016 | TRANSPORT PHENOMENA IN MESOSCOPIC AND NANOSCOPIC SYSTEMS | Aniket Singha   | 3-0-0 | 3 | S31,S32,S33     | R32         |
| EC60064 | BIOMEDICAL SYSTEM ENGINEERING & AUTOMATION               | Sudipta Mukhopadhyay  | 3-1-0 | 4 | F41,F42,F43,F44 | F3          |
| EC60082 | LIGHTWAVE NETWORKS                                       | Aneek Adhya   | 3-1-0 | 4 | X41,X42,X43,X44 | F14         |
| EC60088 | RF & MICROWAVE NETWORKS                                  | Rajat Roy   | 3-1-0 | 4 | F41,F42,F43,F44 | A12         |
| EC60090 | EMI AND EMC TECHNIQUES                                   | Amitabha Bhattacharya   | 3-1-0 | 4 | E41,E42,E43,E44 | A12         |
| EC60094 | RADAR SIGNATURE ANALYSIS AND RADAR IMAGING               | Arijit De   | 3-1-0 | 4 | V41,V42,V43,V44 | F3          |
| EC60104 | MULTIMEDIA SYSTEMS & APPLICATIONS                        | Chetna Singhal  | 3-0-0 | 3 | B31,B32,B33     | F3          |
| EC60110 | TECHNOLOGY CAD   | Vivek Dixit   | 3-0-0 | 3 | B31,B32,B33     | R32         |
| EC60128 | LINEAR ALGEBRA AND ERROR CONTROL TECHNIQUES              | Ratnam Varada Raja Kumar,Rajarshi Roy   | 3-0-0 | 3 | G31,G32,G33     | A12         |
| EC60132 | SECURE COMMUNICATIONS                                    | Rajarshi Roy  | 3-0-0 | 3 | S31,S32,S33     | NR422,NR422 |
| EC60204 | DIGITAL VLSI CIRCUITS                                    | Indrajit Chakrabarti  | 3-1-0 | 4 | D41,D42,D43,D44 | R32         |
| EC60294 | NANOELECTRONICS  | Prasanta Kumar Guha   | 3-0-0 | 3 | A31,A32,A33     | R34         |
| EC60296 | MIXED SIGNAL AND RF DESIGN                               | Pradip Mandal   | 3-0-0 | 3 | G31,G32,G33     | NR323       |
| EC60298 | WIRELESS COMMUNICATION                                   | Pechetti Sasi Vinay   | 3-0-0 | 3 | C31,C32,C33     | F3          |
| EC60332 | INFORMATION THEORY AND CODING                            | Jithin R  | 3-0-0 | 3 | D31,D32,D33     | F3          |
| EC60334 | DEEP LEARNING  | Saumik Bhattacharya   | 3-0-  | 3 | C31,C32,C33     | V3          |

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| EC60336 | MICROWAVE AND MILLIMETER WAVE INTEGRATED CIRCUITS        | Mrinal Kanti Mandal   | 3-0-0 | 3 | D31,D32,D33     | A12 |
| EC60338 | VISION AND VISUALIZATION                                 | Ritwik Kumar Layek  | 3-0-0 | 3 | D31,D32,D33     | V3  |
| EC60504 | COMPUTER COMMUNICATION & NETWORKING                      | Sudipta Mahapatra   | 3-0-0 | 3 | A31,A32,A33     | F3  |
| EC61036 | INTEGRATED NANOPHOTONICS                                 | Basudev Lahiri  | 3-1-0 | 4 | V41,V42,V43,V44 | R32 |
| EC61066 | ADAPTIVE SYSTEMS AND SIGNAL PROCESSING                   | Mrityunjoy Chakraborty  | 3-1-0 | 4 | V41,V42,V43,V44 | A12 |
| EC61202 | CAD FOR VLSI   | Anindya Sundar Dhar   | 3-1-0 | 4 | C41,C42,C43,C44 | R32 |
| EC61204 | ANTENNA THEORY AND PRACTICE                              | Bratin Ghosh  | 3-1-0 | 4 | C41,C42,C43,C44 | A12 |
| EC61471 | ADVANCED MATHEMATICS IN ELECTRONICS ENGINEERING          | Ritwik Kumar Layek,Saumik Bhattacharya,Debashis Sen               | 3-0-0 | 3 | S31,S32,S33     | A12 |
| EC69012 | DESIGN LABORATORY  | Mrinal Kanti Mandal,Rajat Roy                                     | 0-0-3 | 2 | L               |     |
| EC69210 | INTELLIGENT SYSTEMS DESIGN LABORATORY                    | Saumik Bhattacharya,Saumik Bhattacharya,Debashis Sen,Debashis Sen | 0-0-3 | 2 | J,L             |     |
| EC69212 | DESIGN AND SIMULATION LABORATORY: COMMUNICATION NETWORKS | Raja Datta,Amitalok Jayant Budkuley                               | 0-0-3 | 2 | J               |     |
| EC69214 | VLSI TECHNOLOGY AND PROCESS MODELLING LAB                | Prasanta Kumar Guha,Vivek Dixit                                   | 0-0-3 | 2 | L               |     |
| EC69216 | VLSI DESIGN LABORATORY                                   | Anindya Sundar Dhar,Tarun Kanti Bhattacharyya                     | 0-0-3 | 2 | J               |     |
| EC69218 | ANTENNA AND EMI/EMC LABORATORY                           | Amitabha Bhattacharya,Bratin Ghosh                                | 0-0-3 | 2 | J               |     |