**{PCAdder0out[31:28],(Instr <<2)[27:0]}**

**regDest**

**regWrite**

**ALUOp**

**ALUsrc**

**memtoReg**

**memWrite**

**memRead**

**jump**

**Zero**

**Branch**

concat

Lfs0

Lfs1

Add1

PCadder1

ALU0

+4

Mjump0

MBRA0

RF0

MDM0

DM0

MALU0

MRF0

IRM0

PC0