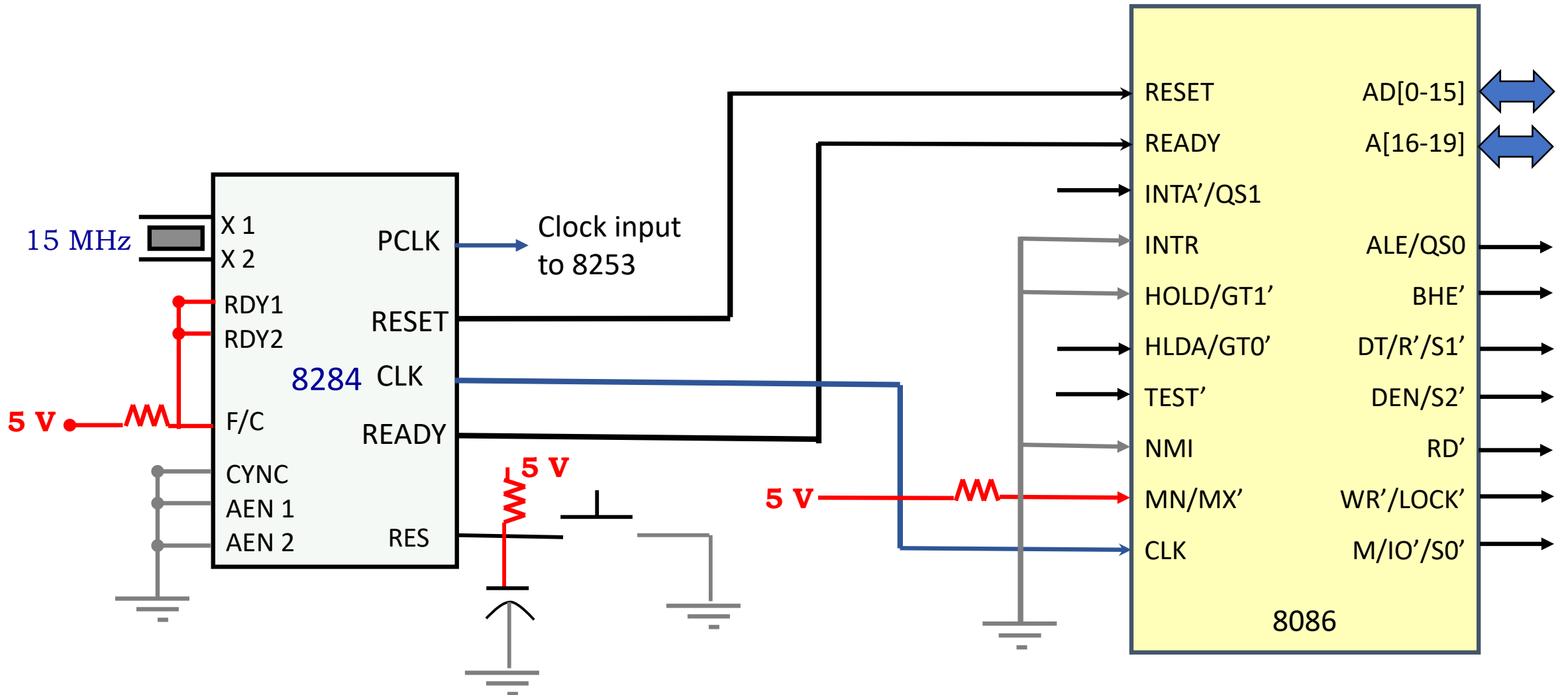
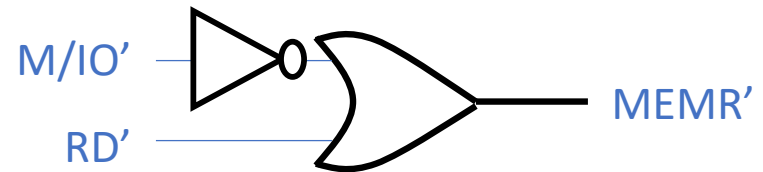
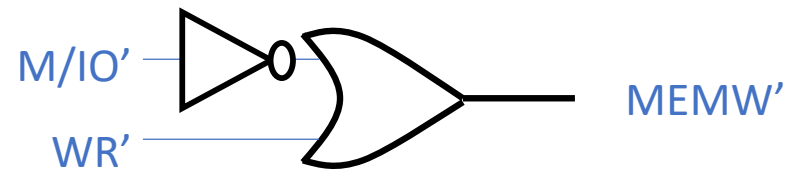
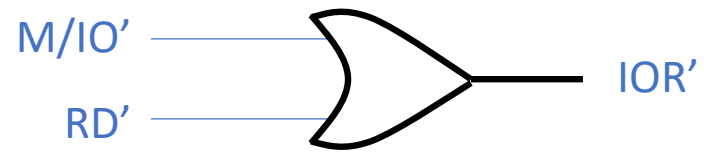
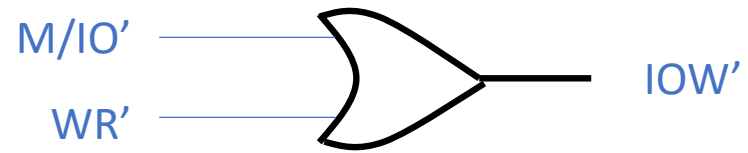


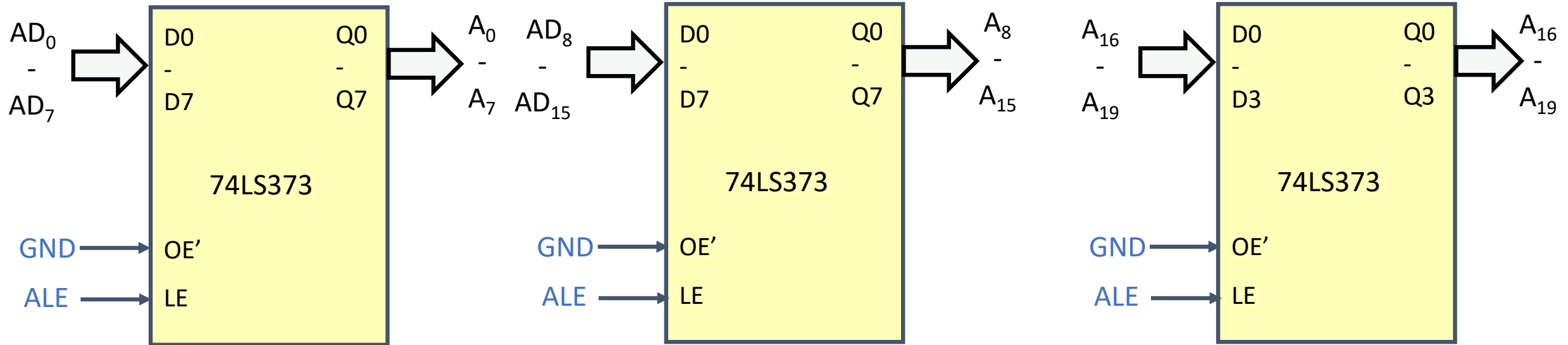
Microprocessor-8086 with 8284 clock



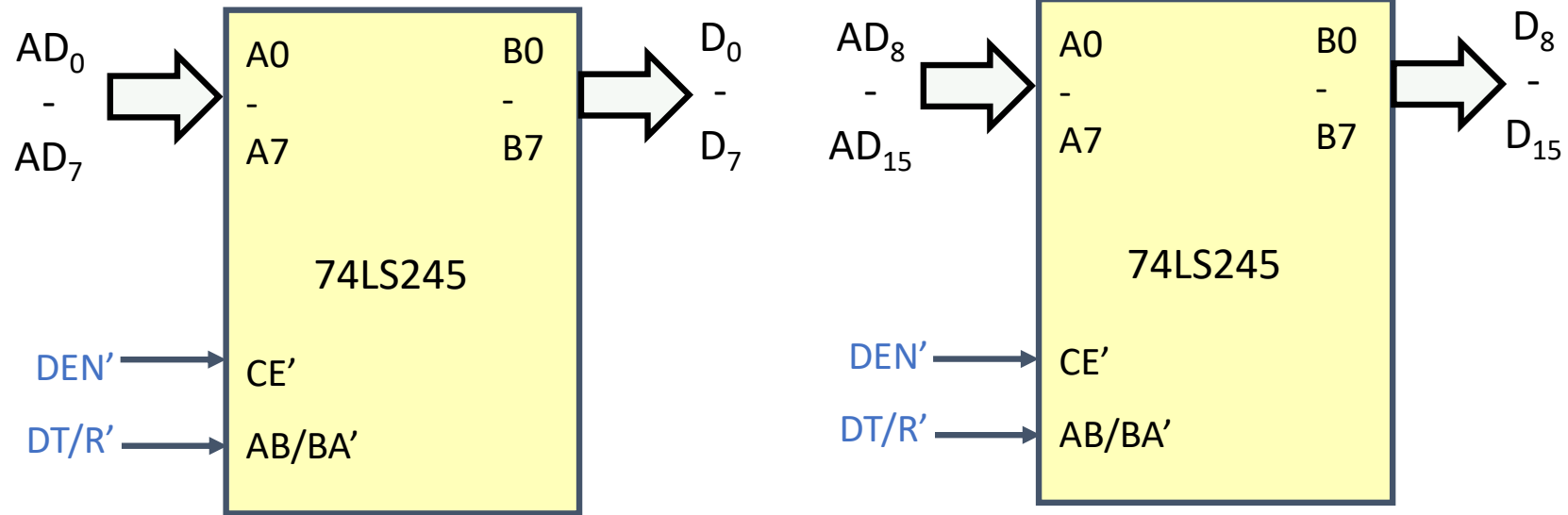
IOR/IOW and MEMR/MEMW SIGNAL GENERATION



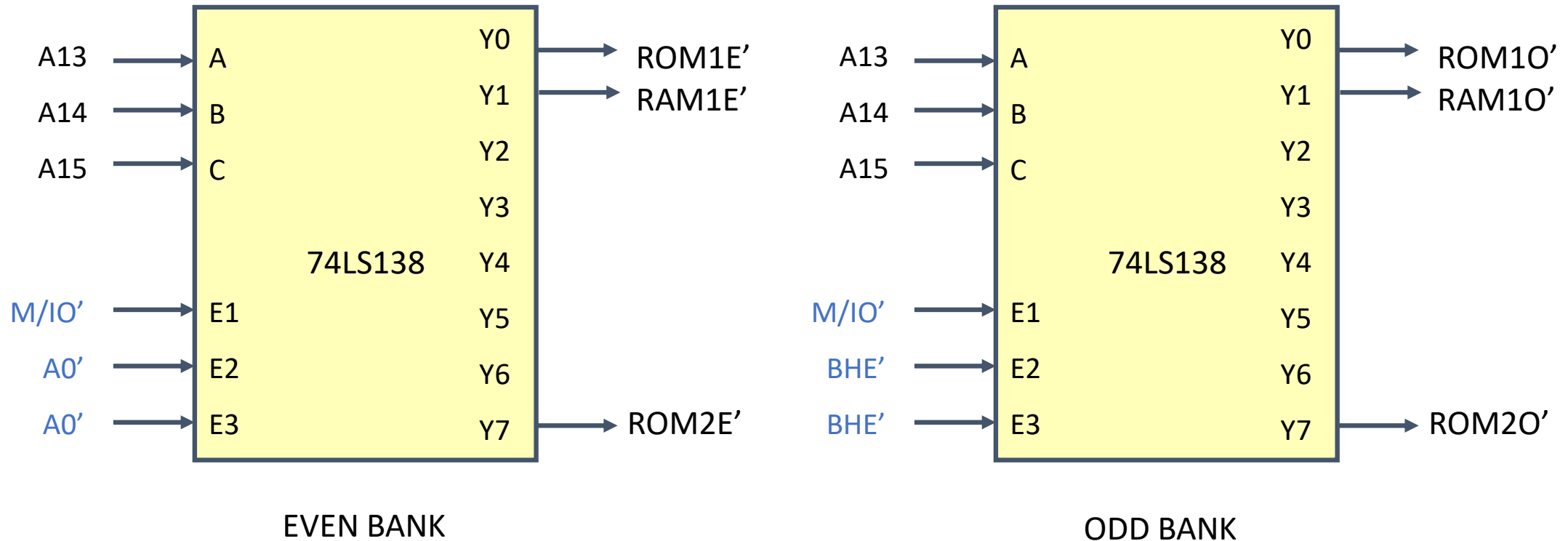
Address demultiplexing



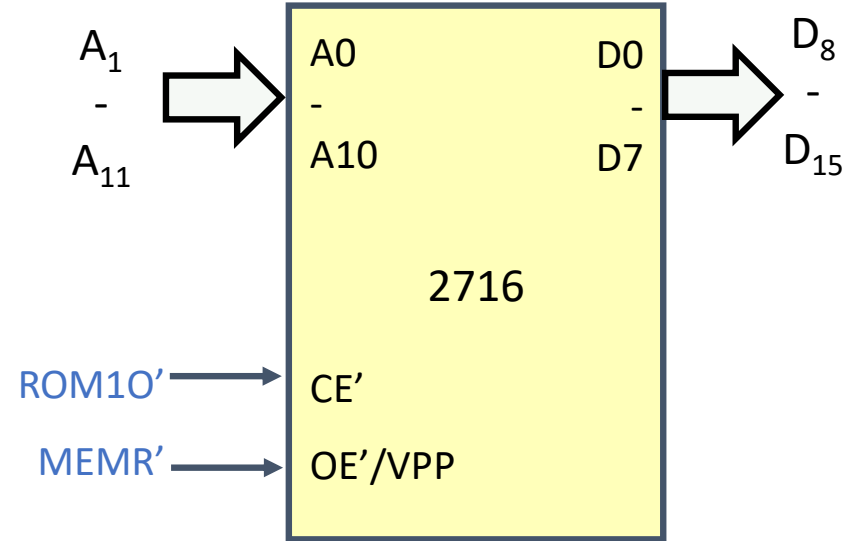
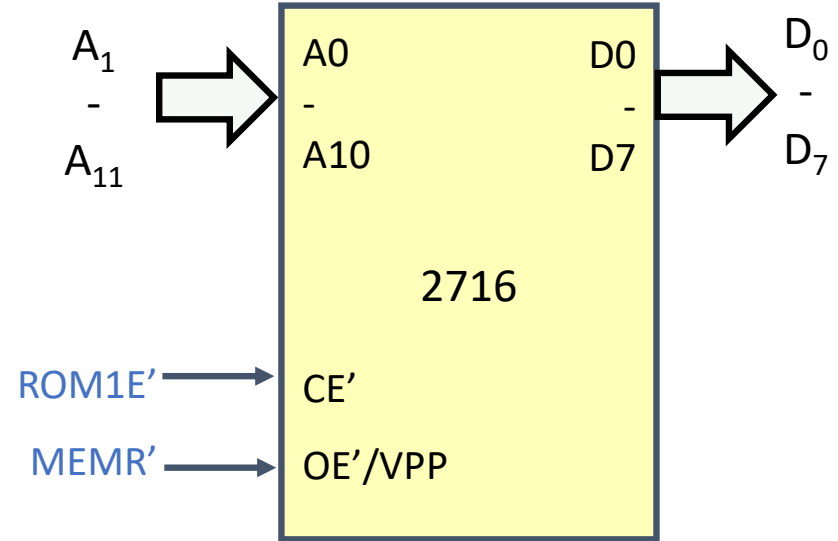
Data demultiplexing



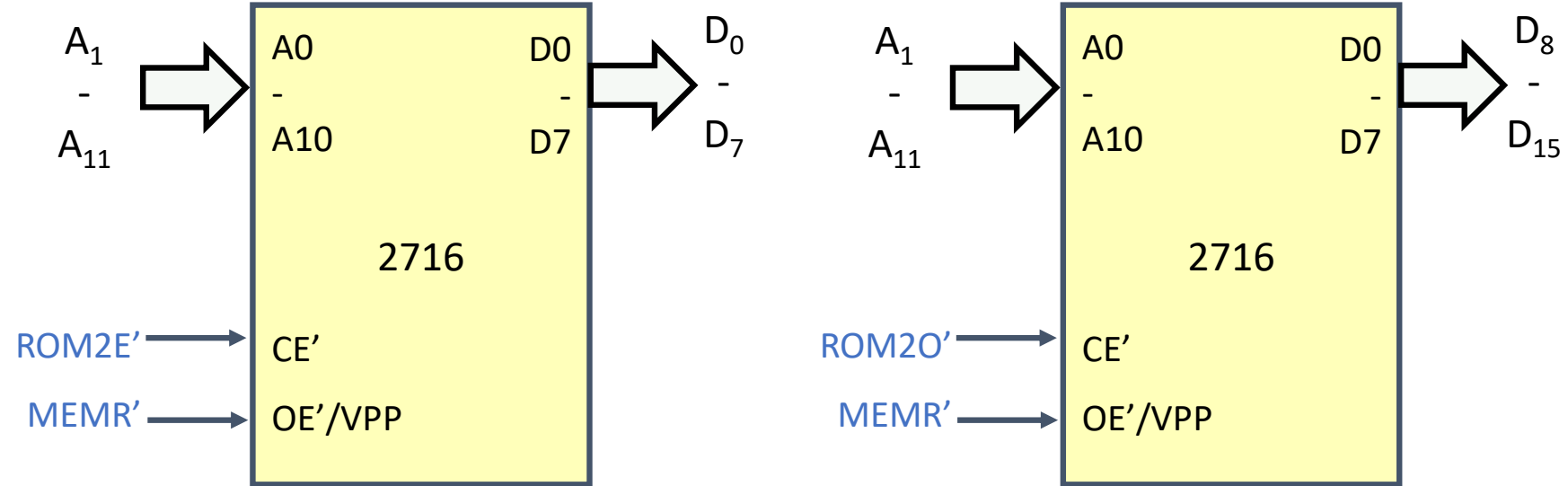
Decoders for even and odd banks of ROM /RAM



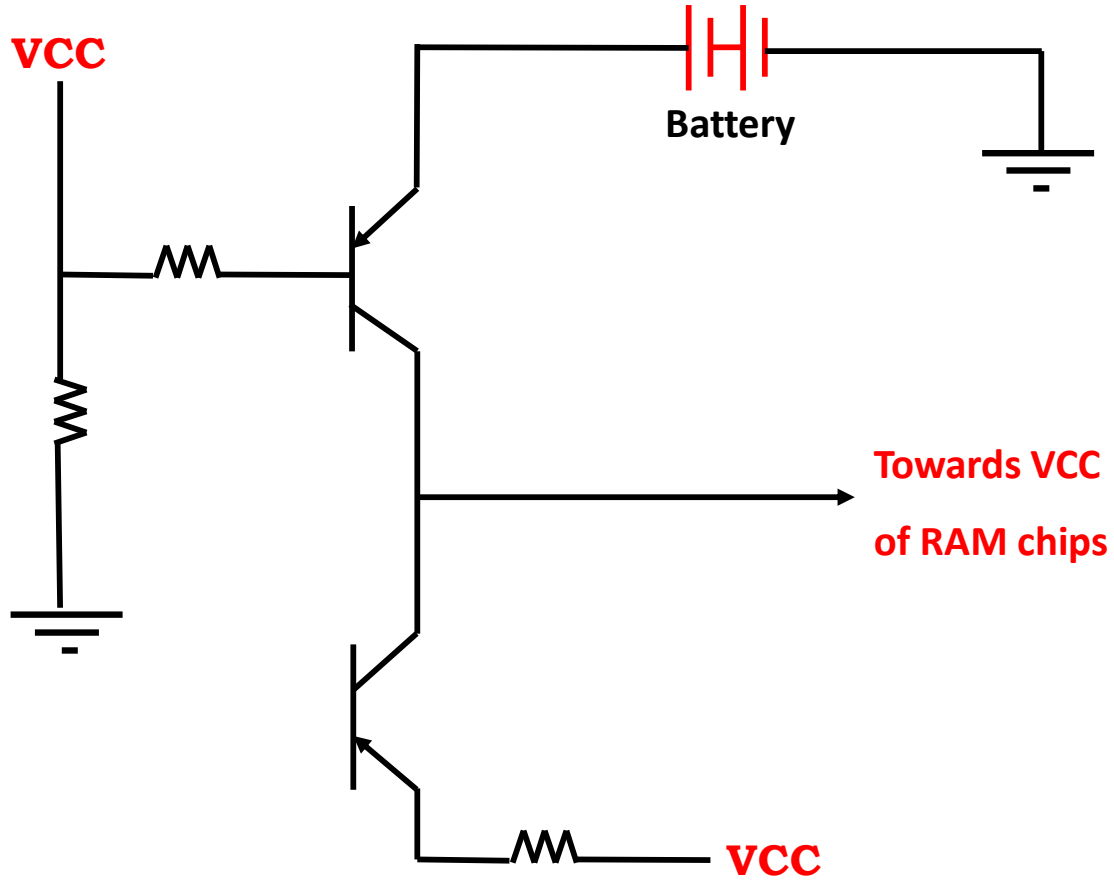
ROM1



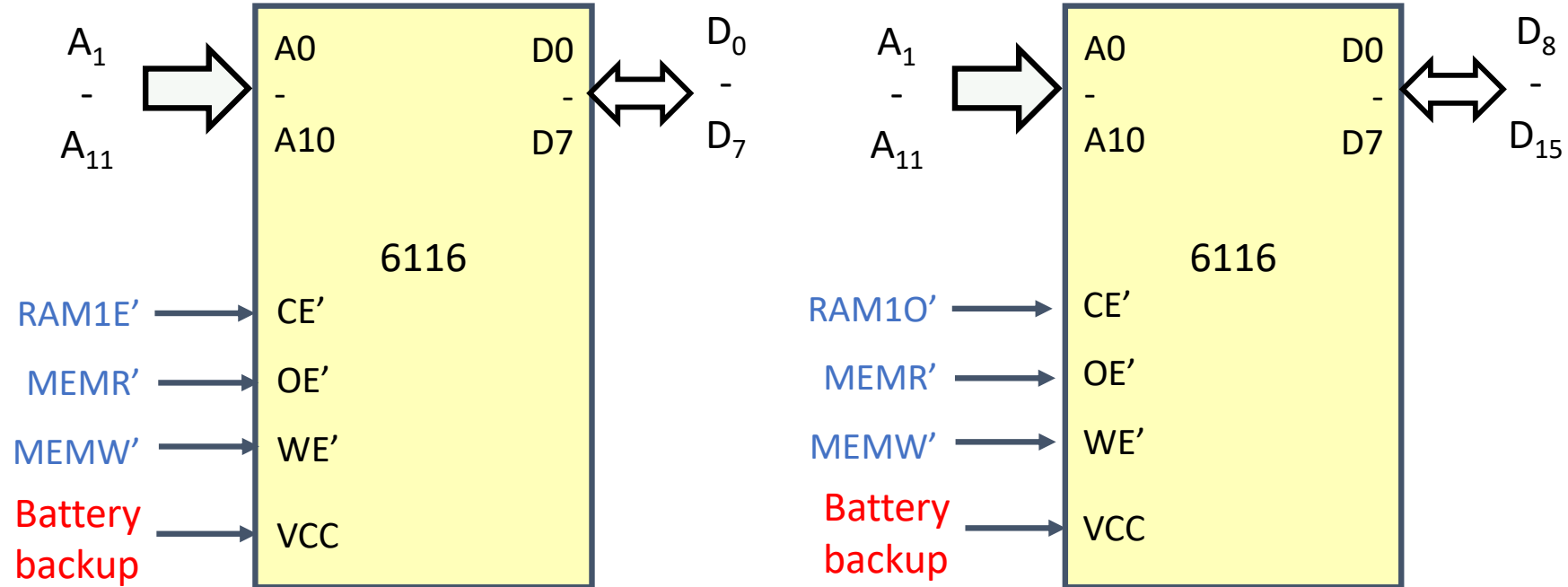
ROM2



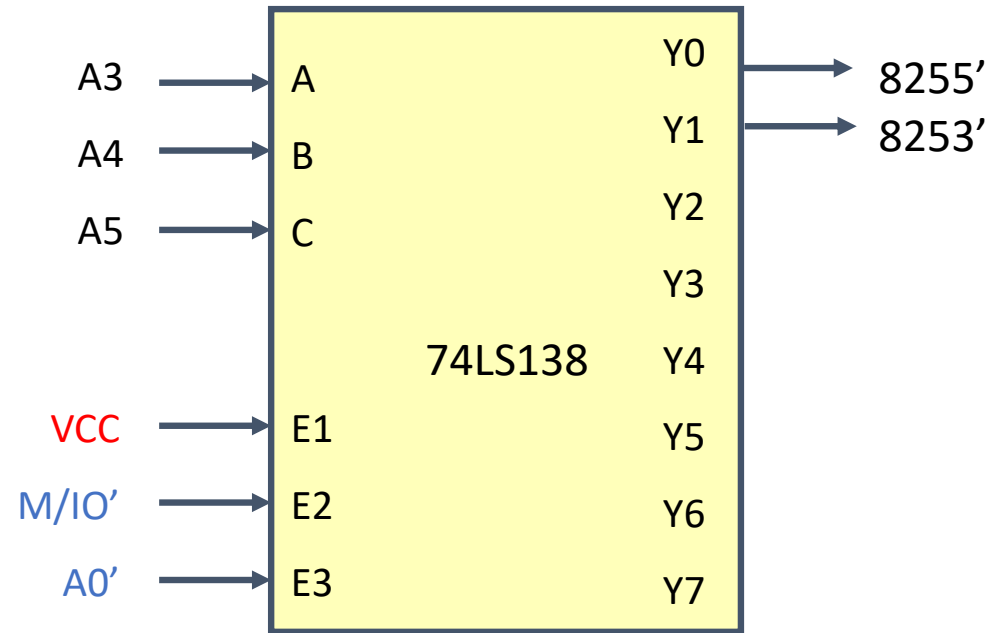
Battery backup for RAM



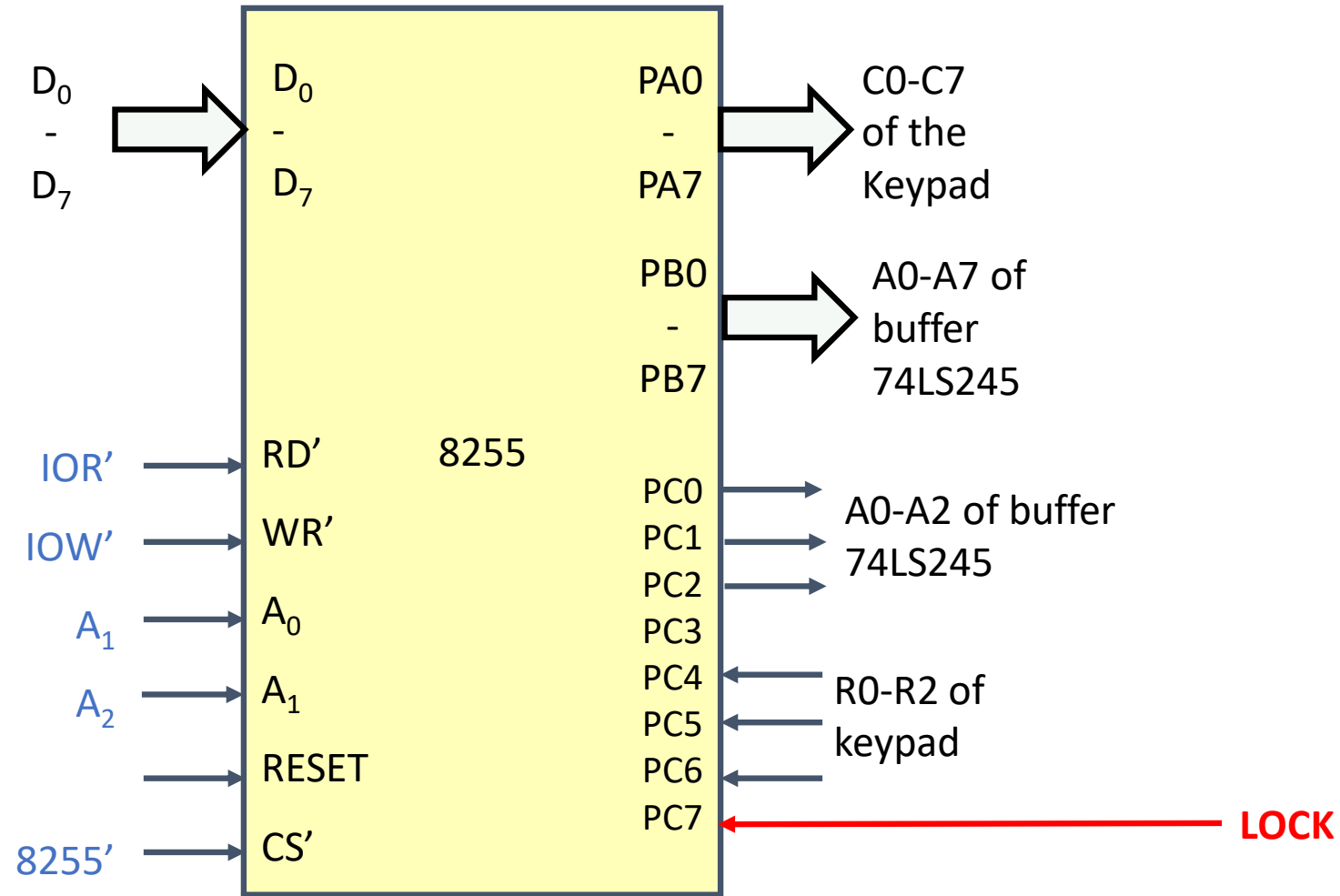
RAM1



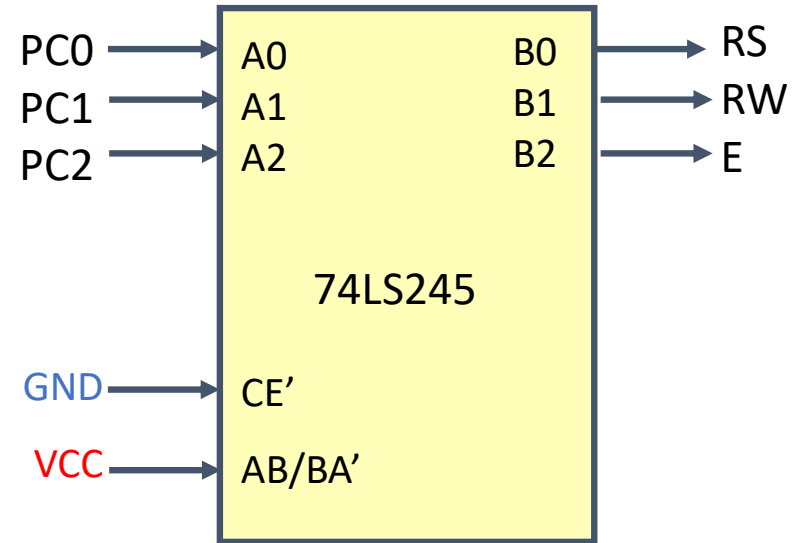
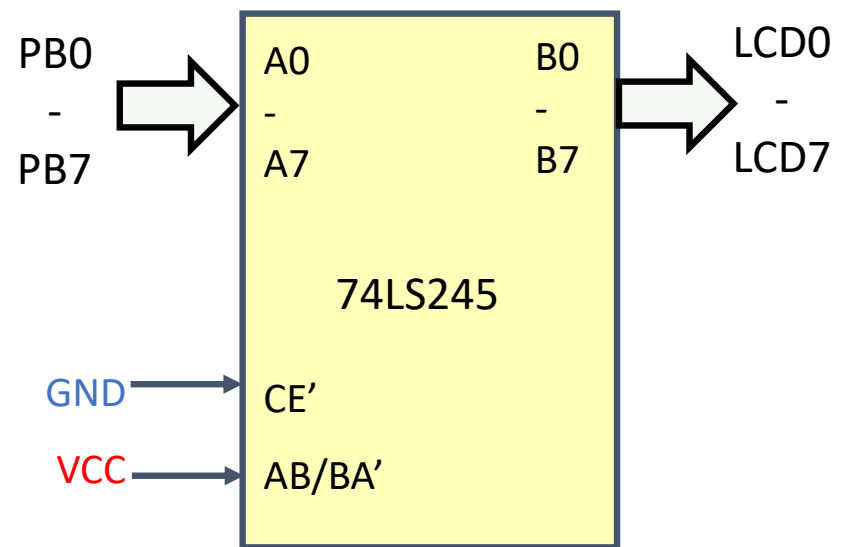
Decoder for 8255/8253 chip select signals



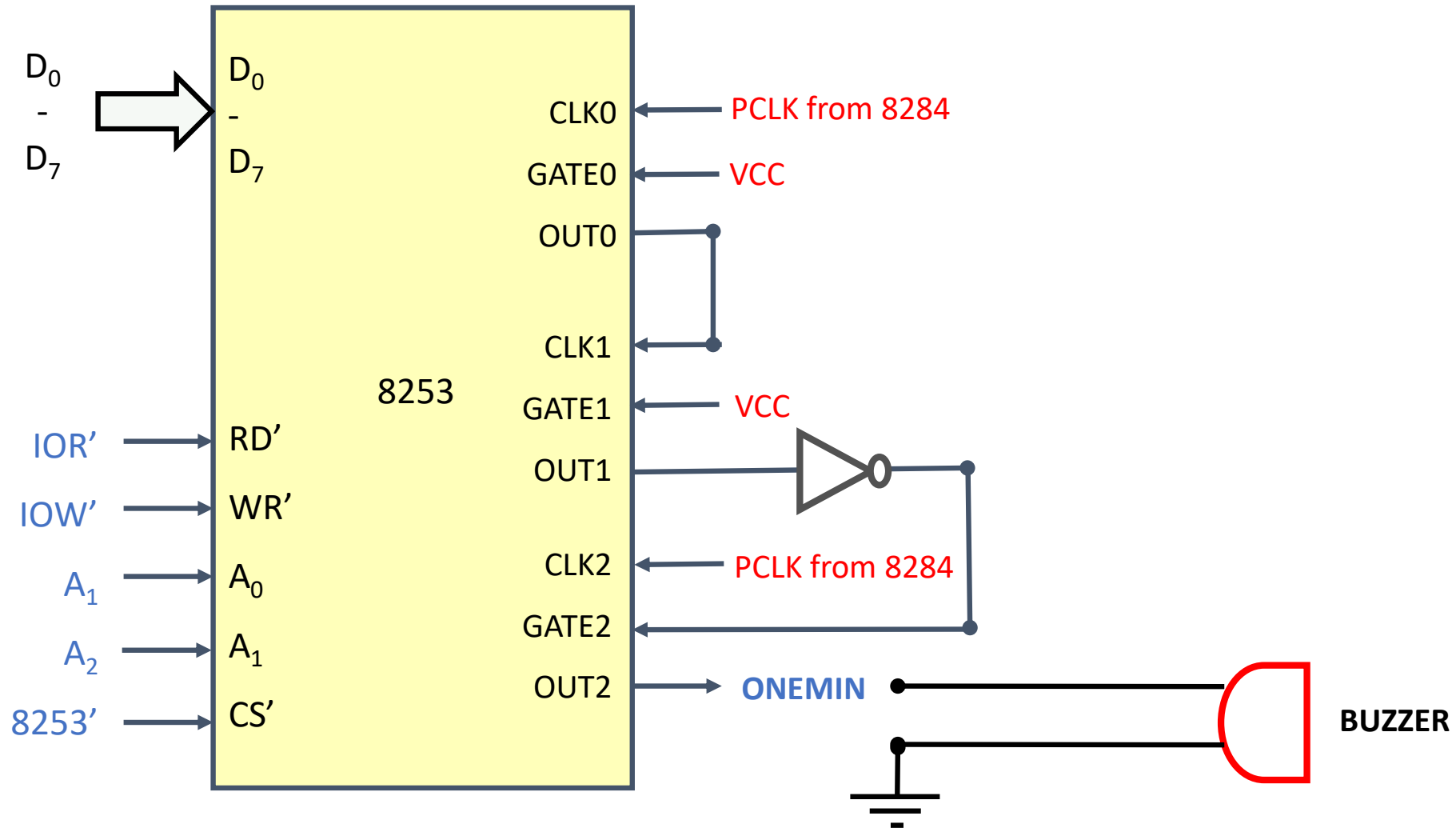
8255 for Keypad and LCD interfacing



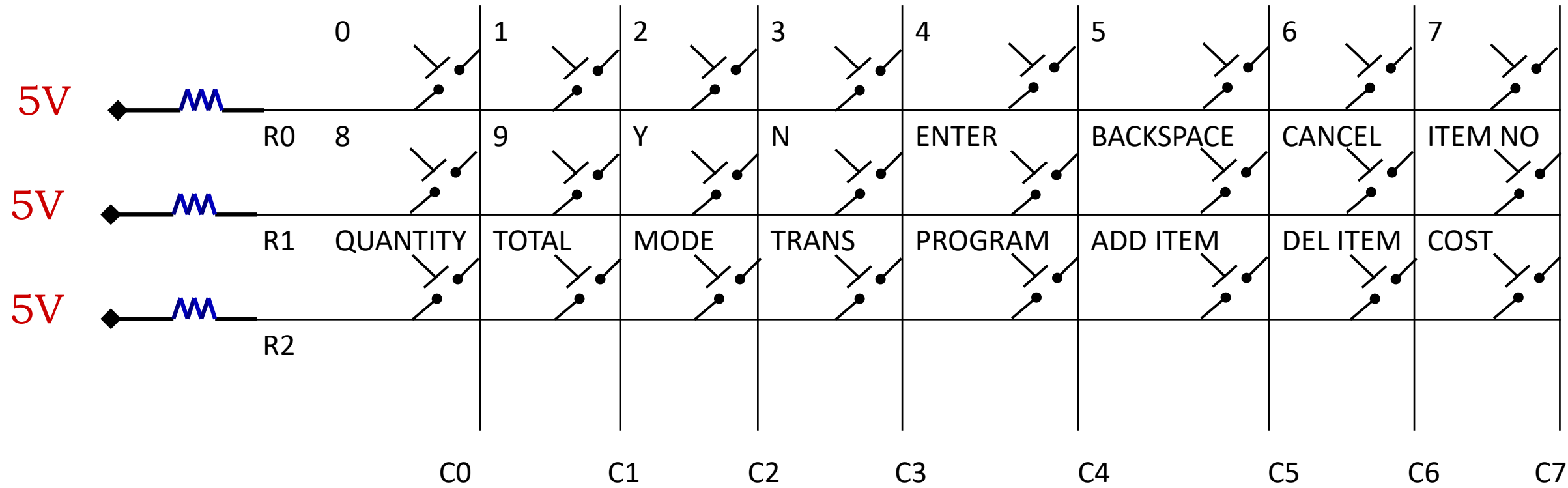
Buffers between 8255 and LCD



8253 for Buzzer timing



Keypad



LCD

