

# DEVIREDDY INDRASENAREDDY

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LinkedIn

GitHub

## CAREER OBJECTIVE

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A focused and dedicated individual with a positive mindset, committed to continuous learning and personal growth. Approaches responsibilities with sincerity and discipline, and is open to new opportunities that enhance technical and professional development.

## EDUCATION

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### Bachelor of Engineering – Electronics Engineering (VLSI Design and Technology)

R.M.K Engineering College, Chennai, Tamil Nadu

2023 – 2027

CGPA: 7.47 (Till 5<sup>th</sup> Semester)

### Intermediate – MPC

Sri Chaitanya Junior College, Vijayawada, Andhra Pradesh

2021 – 2023

Percentage: 90.5%

### Secondary Education

Sri Sai Vidyanikethan High School, Badvel, Kadapa(Dist), Andhra Pradesh

2020 – 2021

Percentage: 94%

## TECHNICAL SKILLS

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**Languages:** C , Verilog

**Technologies/Frameworks:** Excel, Ms Word

**Tools:** Xilinx , Vivado

**Web Development:** HTML, CSS.

## PROJECTS

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### Solar Panel with Sun Position Tracking System

- Automated sun-tracking solar panel using Arduino, LDR sensors, and servo motors.
- Real-time adjustment to maximize solar energy absorption.
- Achieved 20–40 efficiency improvement over fixed panels.
- Low-power, stable, and cost-effective system.
- Scalable design with future scope for IoT-based monitoring.

## INTERNSHIP

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### Technotron – Verilog Design Intern

June 2025 – July 2025

- Completed a hands-on internship focused on Verilog HDL and digital system design.
- Designed and implemented a Hybrid multiplier using adders in verilog.
- Applied reversible logic principles to minimize information loss and power consumption.

## CERTIFICATIONS

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- Completed C Programming Basics from simplilearn.
- System Design Through Verilog (Elite), Digital Circuits, CMOS VLSI Design, Introduction to Semiconductor Devices – NPTEL.
- Digital Electronics, C Programming – Udemy.