

ALSO DO NOT FORGET TO CHECK ALL THE OP AMPS AND OTHER SENSITIVE BITS FOR THEIR APPROPRIATE BYPASS AND DECOUPLING CAPACITORS!

FINISH THE POWER SUPPLY MODULE WITH APPROPRIATE RESISTORS, PROTECTION DIODES, AND FUSEBII

ADD CONTROL JUMPERS FOR THE ARDUINO AND A USB PASSTHROUGH

RUN FINAL DESIGN RULE CHECKS

POSSIBLY REARRANGE THE ORDER OF THE SWITCHING BITS SOMEWHAT

ANALOG +7 TO -5VIA SUPPLY RESISTORS ARE STILL WITHOUT VALUES.

Drafted by Joseph J. Radler  
Induced Polarization Associates, LLC

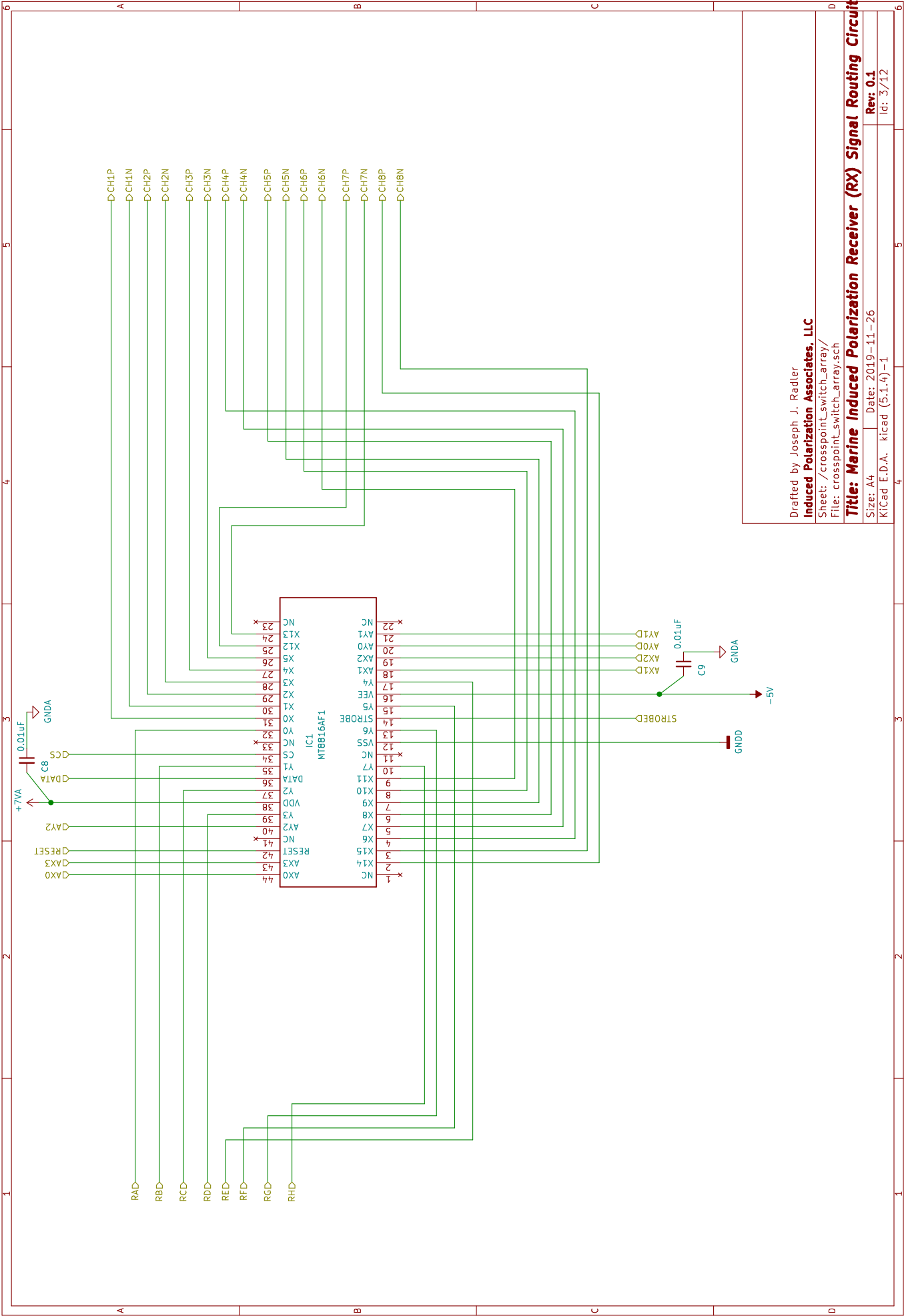
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**Title: Marine Induced Polarization Receiver (RX) Signal Routing Circuit**

Size: B Date: 2019-11-26 Rev: 0.1

KICad E.D.A. kicad 5.1.4-1 ID: 1712



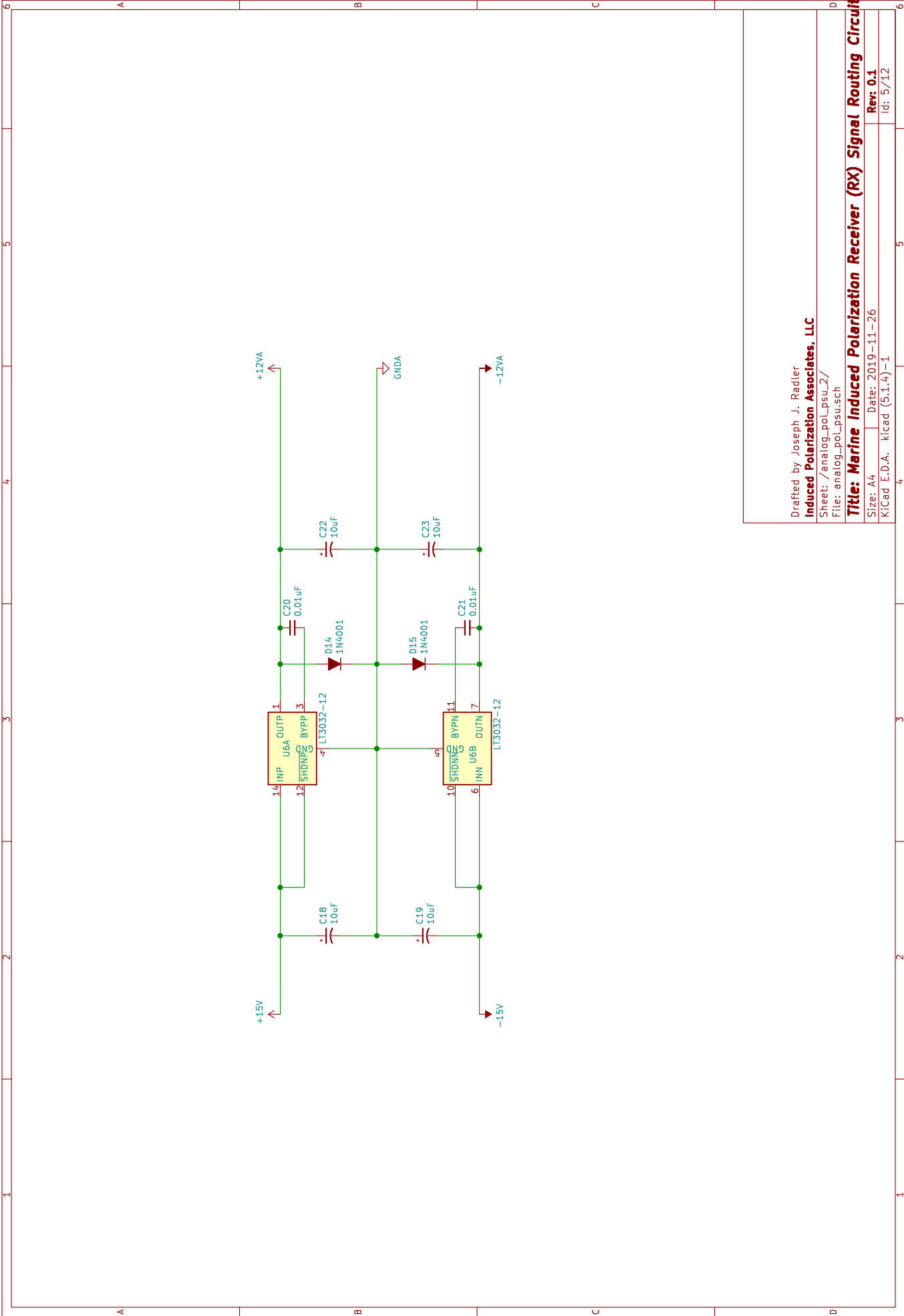


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File: crosspoint\_switch\_array.sch

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Size: A4	Date: 2019-11-26
KiCad E.D.A. kicad (5.1.4)-1	
Rev: 0.1	
Id: 3/12	

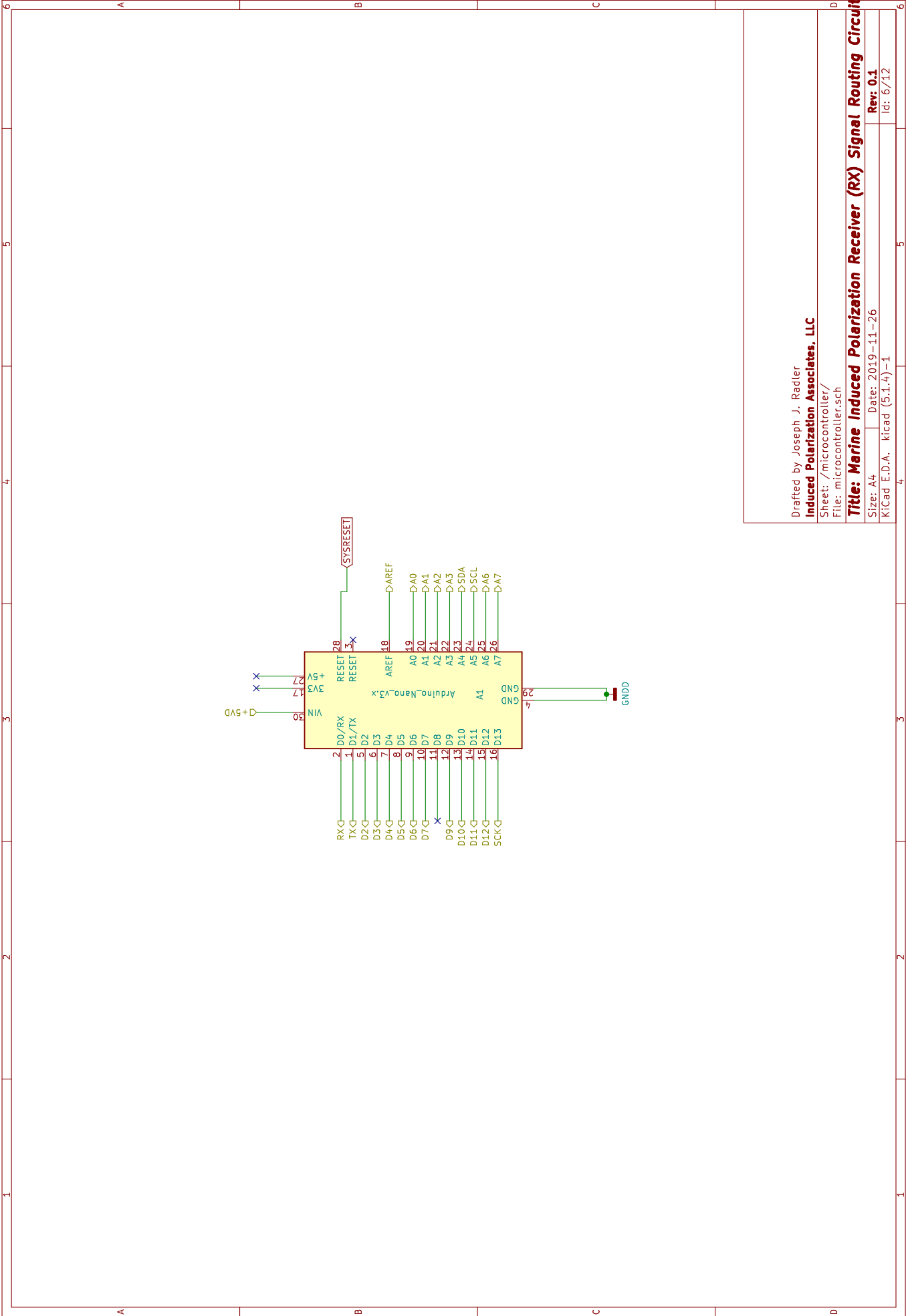




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Sheet: /analog\_pol\_psu\_2/  
File: analog\_pol\_psu.sch

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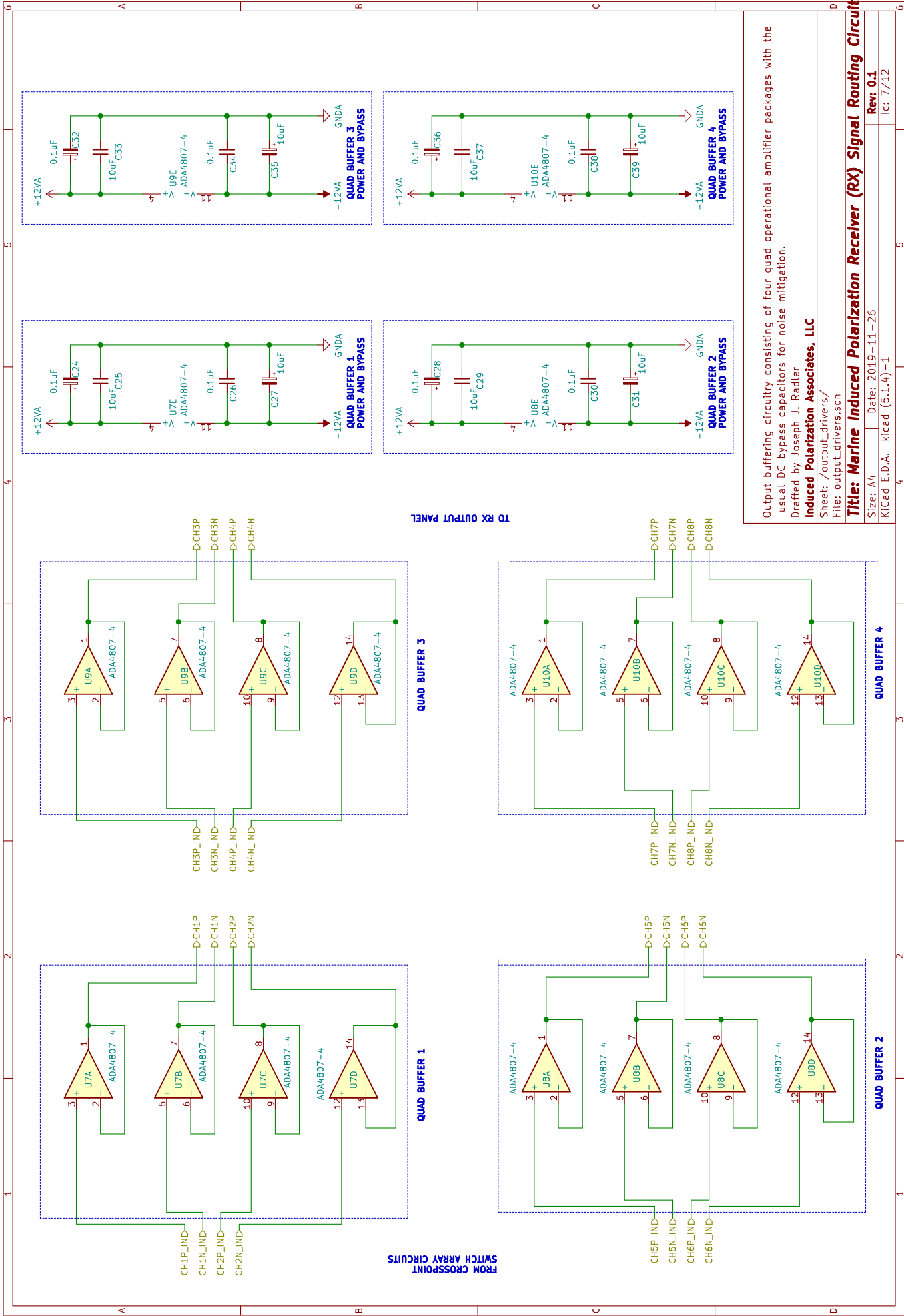
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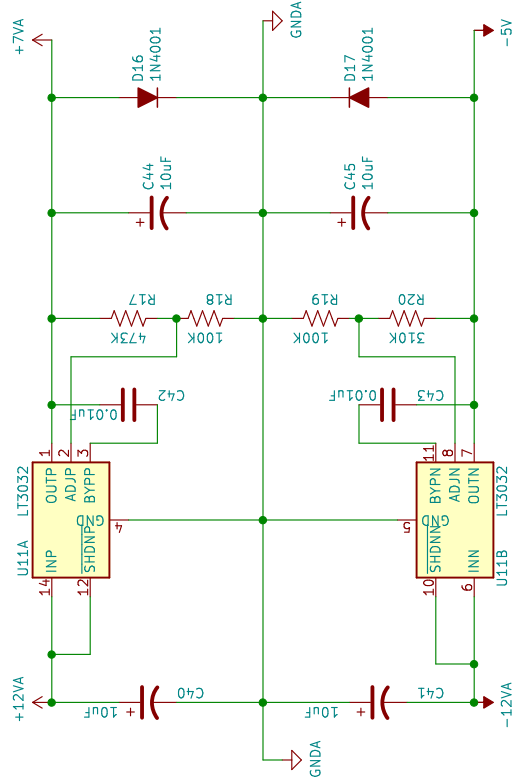


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Sheet: /microcontroller/  
File: microcontroller.sch

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Size: A4	Date: 2019-11-26	Rev: 0.1
KiCad E.D.A. kicad (5.1.4)-1		Id: 6/12





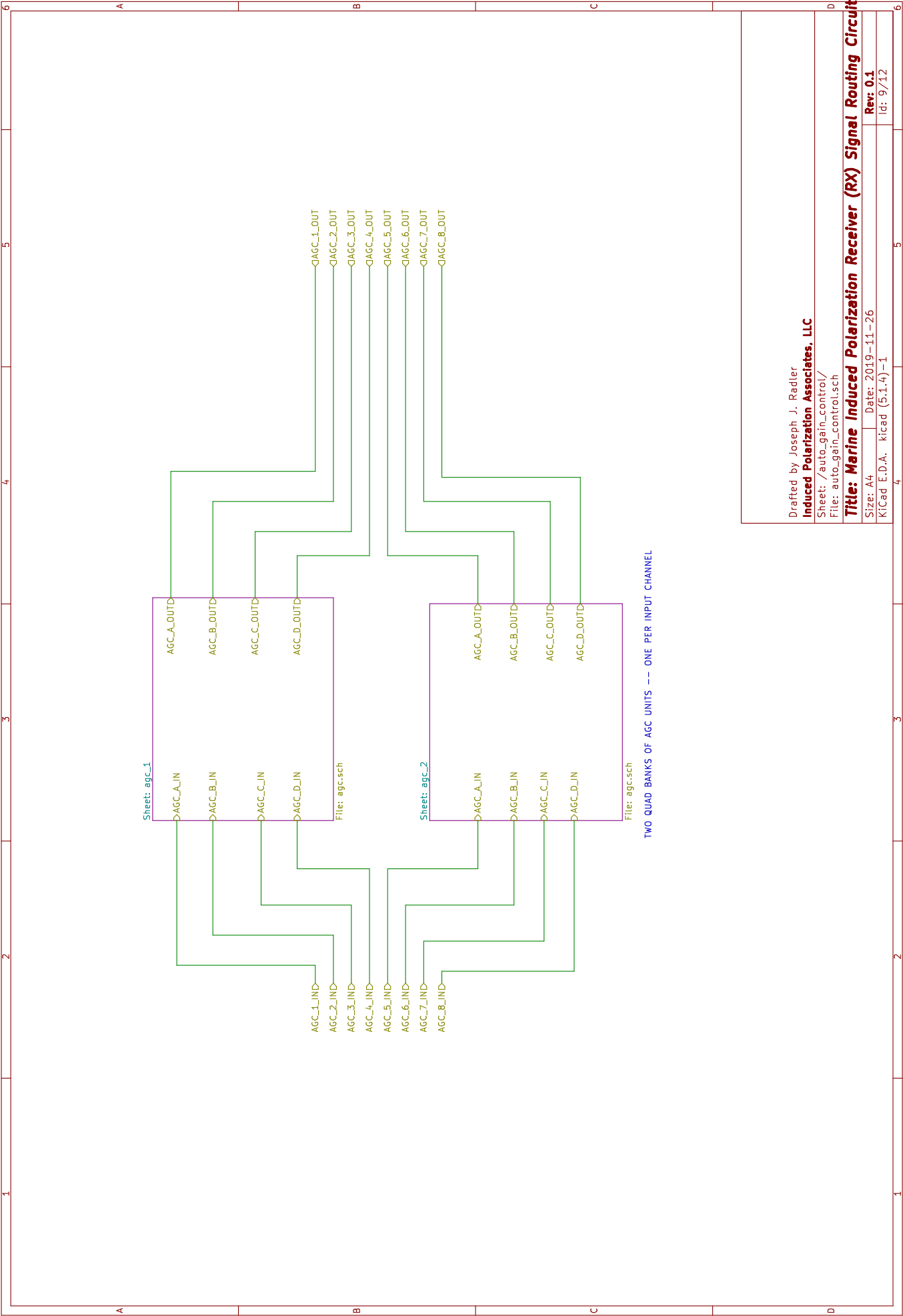
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**Induced Polarization Associates, LLC**  
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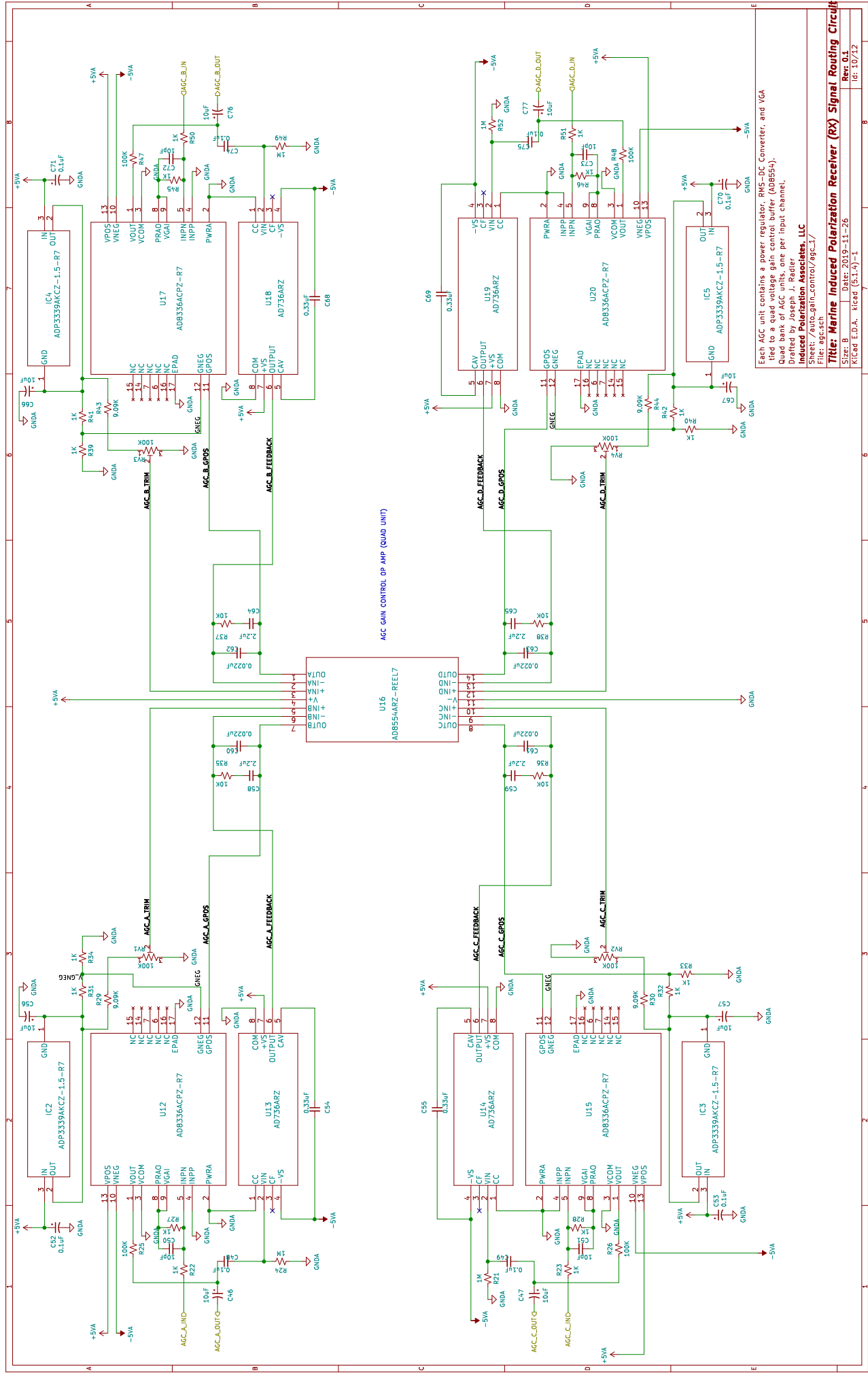
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Size: USLetter | Date: 2019-11-26  
Kicad E.D.A. kicad (5.1.4)-1

Rev: 0.1  
Id: 8/12







Each AGC unit contains a power regulator, RMS-DC Converter, and VGA  
lited to a quad voltage gain control buffer (AD8554).

Quad bank of AGC units, one per input channel.

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Induced Polarization Associates, LLC

Sheet: /auto\_gain\_control/agc\_1/

File: agc\_1.sch

Project: /auto\_gain\_control/agc\_1/

Revision: 1.0

Date: 2019-11-26

Size: B

Kicad E.D.A. kicad (5.1.4)-1

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Rev: 0.1

ID: 10712



