TALKING ELECTRONICS COMPUTER
TEC 1D

100n 100n

7805 UNDER PCB

LOGIC PROBE

10u

RD
WAIT
RESET
M1
RFSH

10k
10k
10k

10k

INT
MREQ

SPEED
20k

10k
2k2
100p

4049

100n

4k7
JMON MOD

4k7
2x1N4148

A15
A14
A13
A12
A11

3800
3000
2800
2000
1800

LOW
HIGH

2732

6116

EXPANSION PORT

Z80 CPU

74LS138

Bit 6
Port (01)

1k

74LS273

74LS273

74C923

100n

74LS138

4049 OSC 1u +
CRYSTAL OSC 10u

100n

BC547

100R

k

330R

LED

SPEAKER

MINI SPKR

6xFND560

ADDRESS

DATA

1k 1k 1k 1k 1k 1k

6xBC547

k

LED

1k

1000uF

4x1N4002

AC/DC

9-12V

RESET

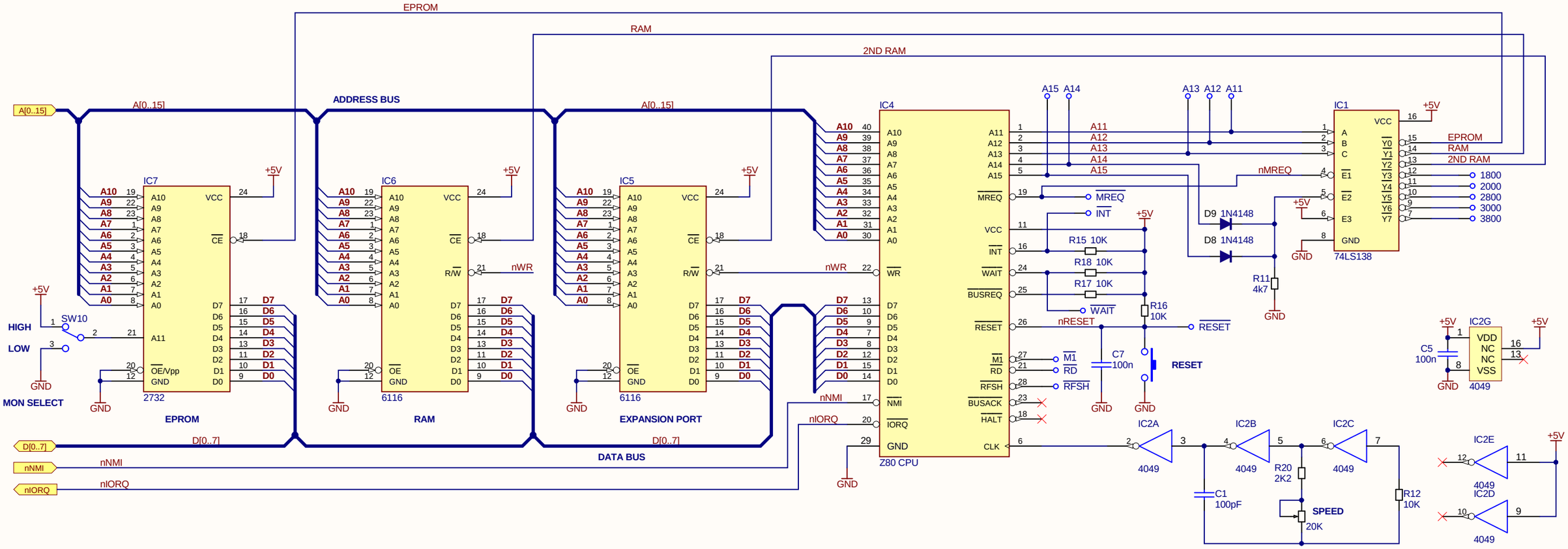
SHIFT

1N4148

47k

AD	3	7	B	F
GO	2	6	A	E
−	1	5	9	D
+	0	4	8	C

250195 REV. 200896



This schematic for the Talking Electronics TEC-1D was reverse engineered by Craig Jones from the original Protel PCB design file provided by, and with the permission of, Talking Electronics Magazine publisher Colin Mitchell.

Title	TALKING ELECTRONICS COMPUTER		
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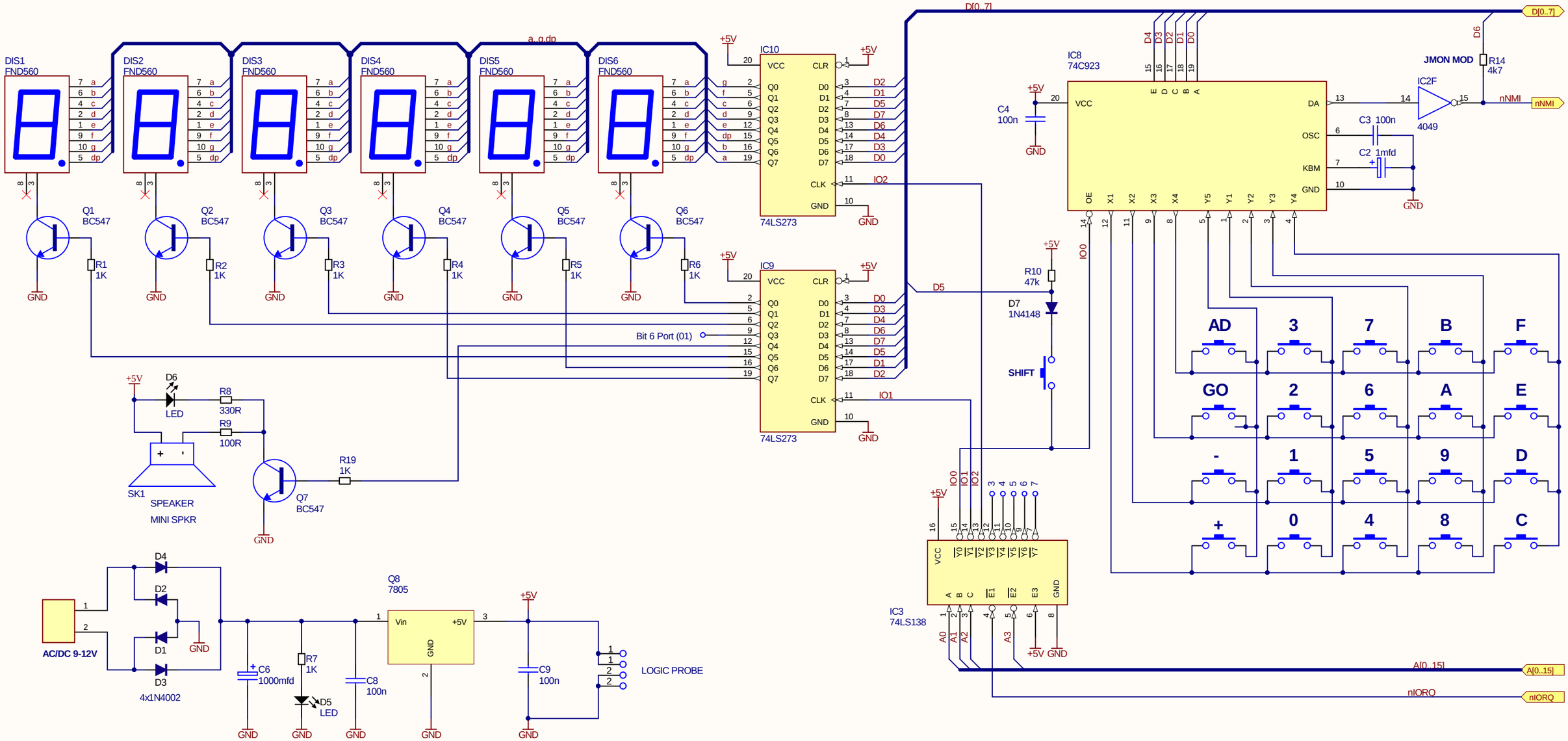
File: TEC-1D ProcessorandMemory.SchDoc			



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File: TEC-1D KeyboardandDisplay.SchDoc		