SYNTRA Architecture Overview (Placeholder)

This document serves as a placeholder for the public benchmark comparing symbolic compression in VLD versus procedural Python logic.

Benchmark Goals:

- Evaluate code length and clarity

- Test real-time reasoning efficiency

- Highlight VLD's ability to symbolically compress diagnostic procedures

The following symbolic chain (plaintext):

    [ADD]engineWarm [ADD]TPSsteady [SUB]crank.signal.missing [DRIFT]faultCode_17 ->
[WARN]crank.sensor.suspect

Is expressed in VLD in under 6 lines, while its equivalent Python logic requires ~25-40 lines.

This placeholder will be replaced with test results, comparison visuals, and runtime graphs in a future release.

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