

MDM9x07&MDM9628 SGMII API MANUAL





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Quectel Wireless Solutions Co., Ltd.

7th Floor, Hongye Building, No.1801 Hongmei Road, Xuhui District, Shanghai 200233, China

Tel: +86 21 5108 6236 Email: info@quectel.com

Or our local office. For more information, please visit:

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About the Document

This document applies to MDM9628 and MDM9X07 platforms.

History

Revision	Date	Author	Description
1.0	2017-11-01	Mike	Initial



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1. Introduction

SGMII is a interface between PHY and MAC which similar to GMII and RGMII, yet GMII and RGMII are parallel, and need to follow the clock, PCB layout is relatively troublesome, and not suitable for backplane applications. The SGMII is serial, no need to provide an additional clock, MAC and PHY need CDR to recover the clock. In addition SGMII is 8B / 10b encoded, the rate is 1.25G.

Currently, the EC20 and AG35 only support the AR8033 PHY chip, 10BASE-Te / 100BASE-Te / 1000BASE-Te / 1000BASE-Te

2. Specification

SGMII will not enabled by default, the interface is as follows.

1. int ql_sgmii_enable(void);

Start SGMII function, call the function will load SGMII driver; After the driver loading successfully, you can see the eth0 network port in the console, as shown in Figure 1:

Figure 1

After the network port start successfully, the qti program will capture the startup event and notify the QCMAP_ConnectionManager process to load the eth0 device to the bridge0, as shown in Figure 2:

```
~ # brctl show
bridge name bridge id STP enabled interfaces
bridge0 8000.fa85eccde650 no eth0
~ #
```

Figure 2

Note:

The MAC address of the eth0 interface is 00: 80: 48: BA: D1: 30 by default. If the client need to change this MAC address, it can be set through the CLI (for example: ifconfig eth0 hw ether 00: 80: 48: BA: d1: 30), you can also modify the SGMII startup script (/etc/init.d/start_emac_le) inside the address settings.



2. int ql_sgmii_disable(void);

Close the SGMII function, call this function, eth0 network port will removed from the bridge0, and the driver will be uninstalled.

int ql_sgmii_speed_set(ql_sgmii_speed_e speed);

Set the network port speed, support 10MHZ / 100MHZ / 1000MHZ, the default is adaptive, the macro is defined as follows:

If setting is QL_SGMII_SPEED_AUTO, the two network interfaces will negotiate the rate through adaptation.

4. int ql_sgmii_speed_get(ql_sgmii_speed_e *speed);

Get the current speed of the network port.

5. int ql_sgmii_duplex_set(ql_sgmii_duplex_e duplex);

Set the network port duplex mode, support half-duplex and full-duplex, the macro is defined as follows:

Note: Network port rate cannot be set as QL_SGMII_SPEED_AUTO by calling this function. For the gigabit rate AR8033 chip, it only supports full duplex.

int ql_sgmii_duplex_get(ql_sgmii_duplex_e *duplex);

Obtain the duplex mode by the network interface

7. int ql_sgmii_speed_duplex_set(ql_sgmii_speed_e speed, ql_sgmii_duplex_e duplex);

Set the network port speed and duplex mode.

8. int ql_smgii_info_get(struct ql_sgmii_info *info);

Obtain the current status of the network interface, including the number of data packets sent and received, the data size, the current running rate, and the current running duplex mode.

Note: Before calling interface 3 ~ 8, you need to call ql_sgmii_enable ();



3. Example

You can refer to example: example/sgmii / example_sgmii.c

```
int main(int argc, char **argv)
          ql_sgmii_enable();
ql_sgmii_speed_duplex_set(QL_SGMII_SPEED_100MHZ, QL_SGMII_DUPLEX_FULL);
return 0;
```

NOTE: Currently Configuration save function is not supported, will support it in the future.