

# TDT 4200 Fall 2024 Parallel Computing

Lect 9: MPI\_Scatter/Gather – time analysis/Performance, More on Caching

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Anne C. Elster NTNU TDT4200, Sept 4, 2024

#### **TDT4200 F2024 Course Information**

- See also https://www.ntnu.edu/studies/courses/TDT4200/
  - Instructor: Professor Anne C. Elster (elster@ntnu.no)
  - Recitation lectures and assignments: Tobias Dyngeland
  - Course supporter: Assoc. Prof. Jan Christian Meyer
  - Web page: http://www.idi.ntnu.no/~elster/tdt4200/fall2024 TBA

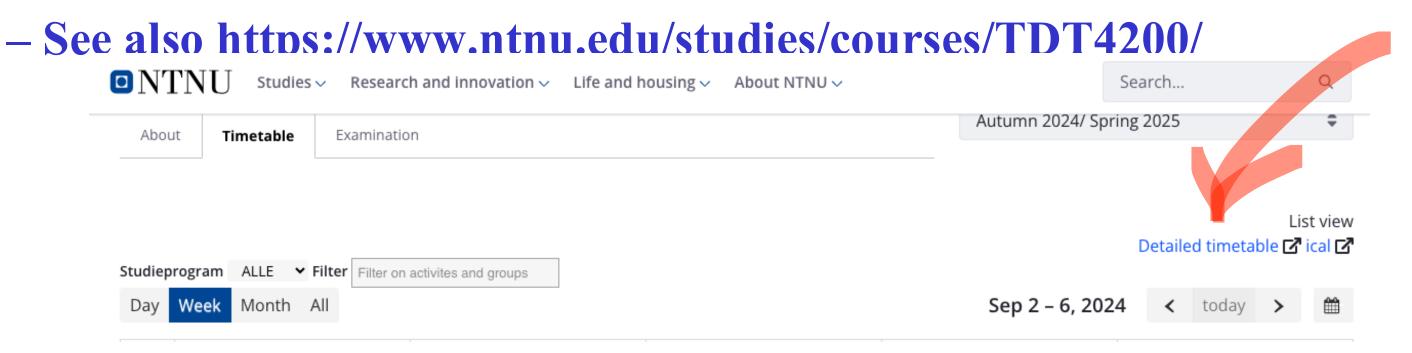
#### • Lectures:

- -Tuesdays: 10:15-12:00 in R5
- -Wednesdays: 9:15-10:00 in R7

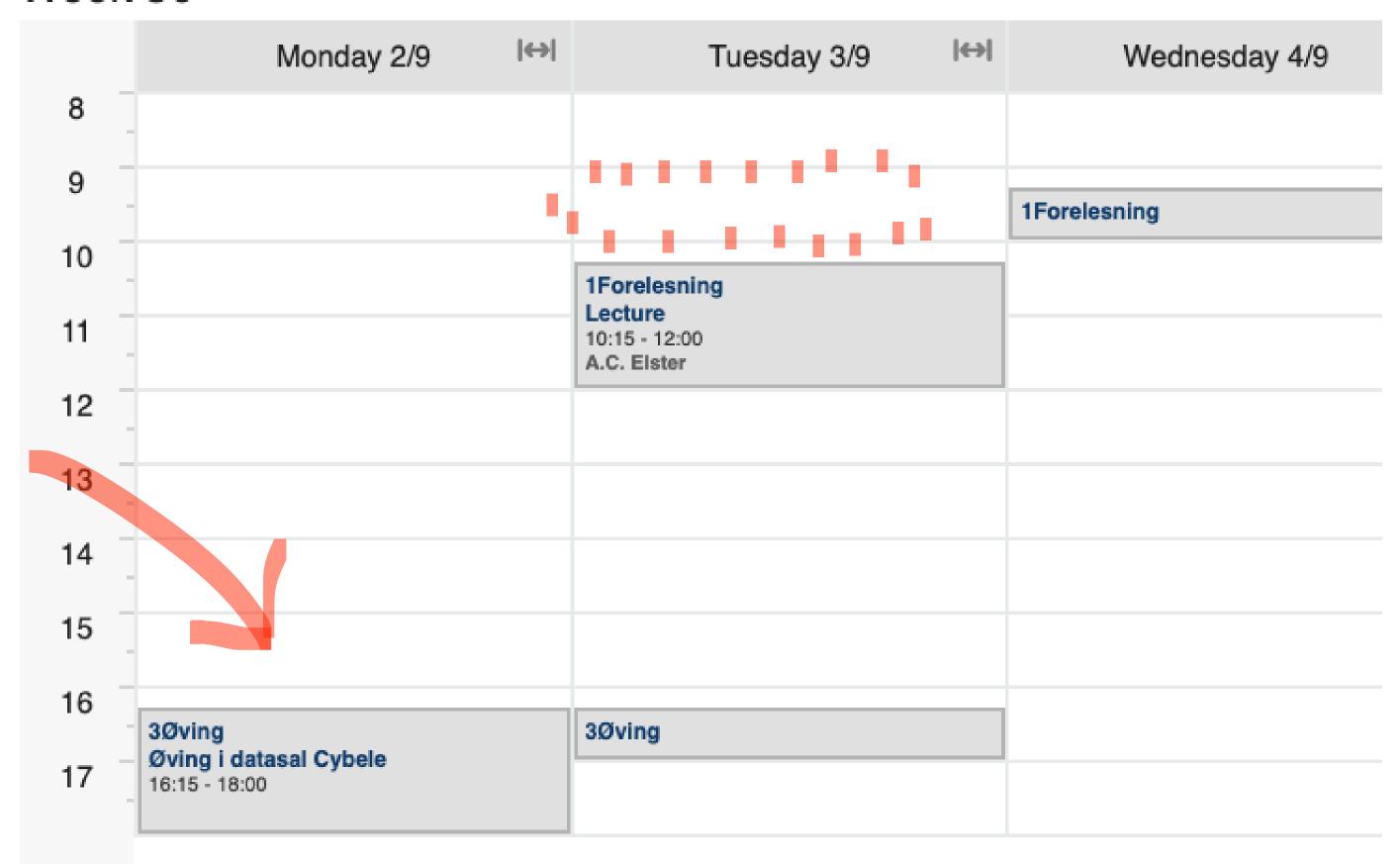
### • Recitation lectures (Øvingstime):

- -Tuesdays: 16:15-17:00 in Kjel 5
- Wednesdays: 8:15-9:00 in Kjel 1 replaced by Cybele hrs on Mondays: (Updated under "Detailed TimeTable")
- Lab help Mondays 16-18 @ Cybele
- Course Tool: BlackBoard

### **TDT4200 F2024 Course Information**



#### Week 36



### **Course Information – continued**

See also https://www.ntnu.edu/studies/courses/TDT4200/

NOTE: Compulsory assignments:

- You need to do and pass ALL
   Problem Sets/Exercises in order to take the final
  - ... also those that are Pass/Fail!!

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## BlackBoard – Problem Sets -- Updated dates!

#### Problem Sets -- Tentative Dates 💿 🗚

Tentative schedule for the problem sets:

Problem set (Exercise)	Available (Tuesdays in Recitation)	Due Mondays 10pm	Topic	Grading
PS 0	Aug 20	Sep 2/9*	C - intro pointers ++ (optional, but highly recommended)	Pass/Fail Optional
PS 1a	Aug 27	Mon Sep 9*	C - Wave Equation	Pass/Fail Required
PS 1b	Aug 29	Mon Sep 9*	MPI Intro (Optional, but highly recommended)	Pass/Fail Optional
PS2	Sep 10	Mon Sep 16	MPI 1D Wave eqn	Pass/Fail - Required
PS3	Sep 17	TBD	MPI 2D Wave eqn	Graded - Required
PS4	Oct 1	TBD	Pthreads/OpenMP	Pass/Fail - Required
PS5	TBD	TBD	CUDA Intro	Pass/Fail - Required
PS6	TBD	TBD	CUDA 2D Wave eqn	Graded - Required

<sup>\*</sup> You can technically submit these until the end of the add/drop period, but are STRONGLY advised to do them ASAP.

#### AN INTRODUCTION TO PARALLEL PROGRAMMING



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## Peter S. Pacheco Matthew Malensek

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# BlackBoard – Forum

→ Th	Thread Actions Collect Delete						
<b>*</b>	DATE 🔝	THREAD	AUTHOR	STATUS	UNREAD POSTS	UNREAD REPLIES TO ME	TOTAL POSTS
	9/1/24 6:54 PM	Problem with movie creation	Arthus Guy Daimez	Published	0	0	3
	8/28/24 1:58 PM	Issue Running Assignment 1 on Snotra Server	Simone Deidier	Published	0	0	2
	8/25/24 8:13 PM	Can i deliver assignment 0 on the 2nd of september?	Kristian Sørli	Published	0	0	4
	8/23/24 2:30 PM	Problem Set resubmission	Eivind Kløvjan	Published	0	0	2
	8/21/24 10:57 AM	Comments/Questions Main lectures	Anne Cathrine Elster	Published	0	0	2
	8/20/24 4:40 PM	Recommendations for learning C	Henriette Marie Eltvik	Published	0	0	3
	8/20/24 1:00 PM	Exercise submission	Guillaume Carraux	Published	0	0	3 ②
	8/19/24 1:53 PM	Welcome to the TDT4200 Forum / Discussion Board for Fall 2024	Anne Cathrine Elster	Published	0	0	1
Th	read Actions Collect	Delete					

# Outline

- Course Info.
- MPI Scatter/gather and timing on blackboard and JCM slides 13, slides 14-32
  - Timing, incl. Latency & Bandwidth
  - Implementation impact on performance (serial vs trees etc)
- More on Caching on black board, these slides + JCM slides24

# The Von Neuman Architecture → sequential computer (SISD)

**CPU** 

#### Von Neuman bottleneck

- since CPU 500-1000 times faster than RAM!

Memory (RAM)

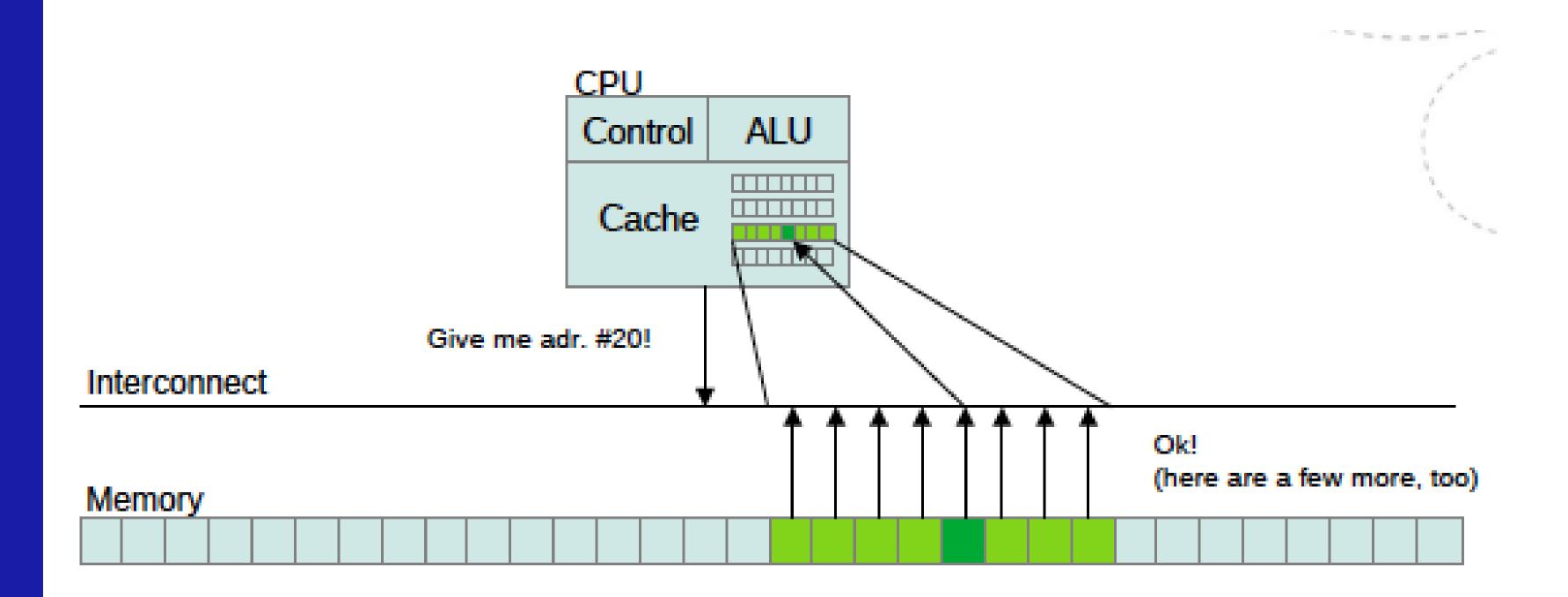
→ Add caches!

(Illustrated system with 3 cache levels on the blackboad)

# Caching

(from JCM slides26)

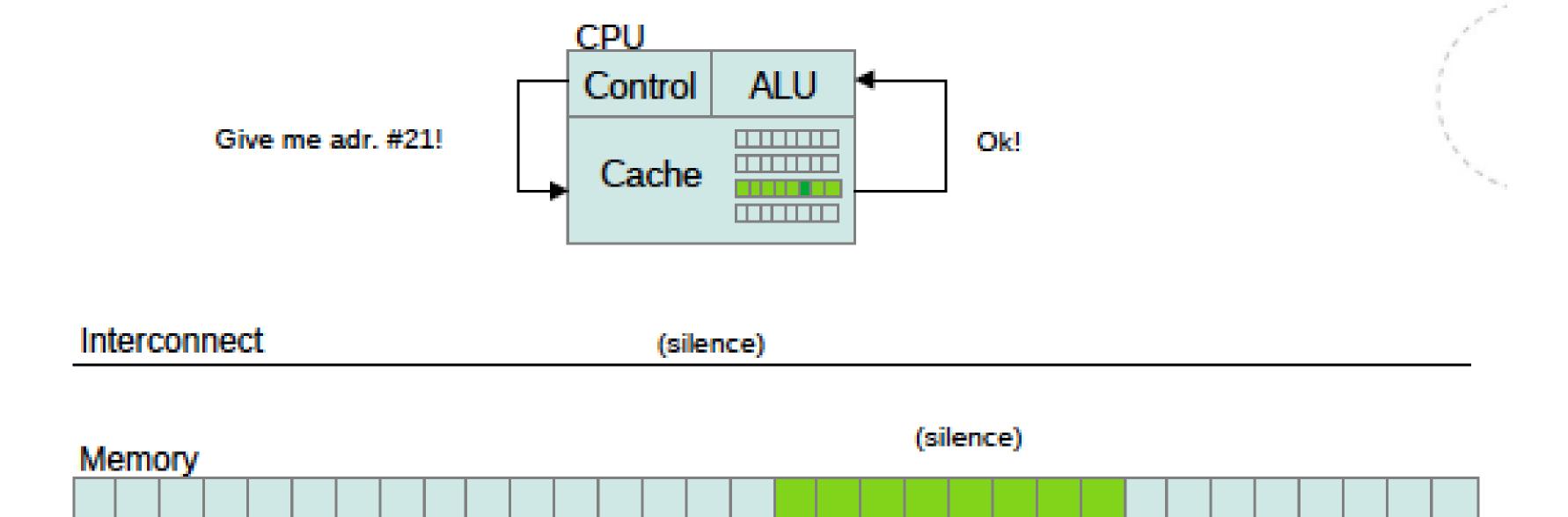
# Reading un-cached value:



# Caching

(based on JCM slides26)

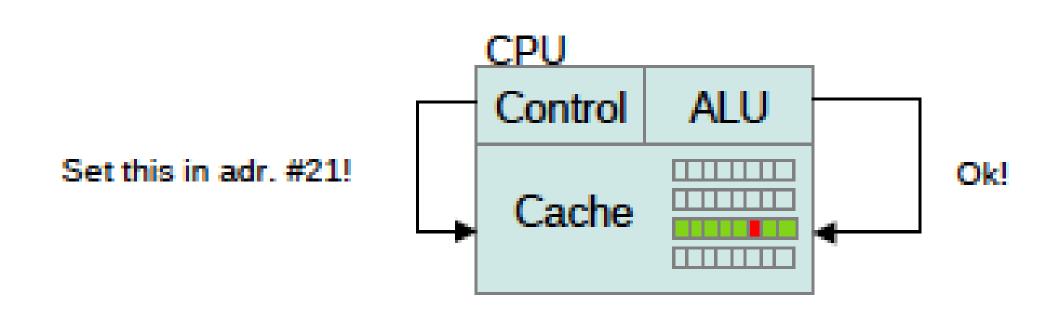
Reading another (now cached) value:



# Caching

(based on JCM slides26)

# Writing a cached value:



Interconnect (silence)

This copy is now out of date, but we don't know it yet

Memory

# Caching – writing cached value

(based on JCM slides26)

We have 2 options:

- 1) Push write operations straight through cache memory, and update main memory ASAP
  - + Simple and inexpensive implementation
  - May create constant memory traffic
- 2) Delay write operations in memory, continue working with the cached copy
  - + Doesn't do unneccessary work
  - Requires more complex circuitry and wiring

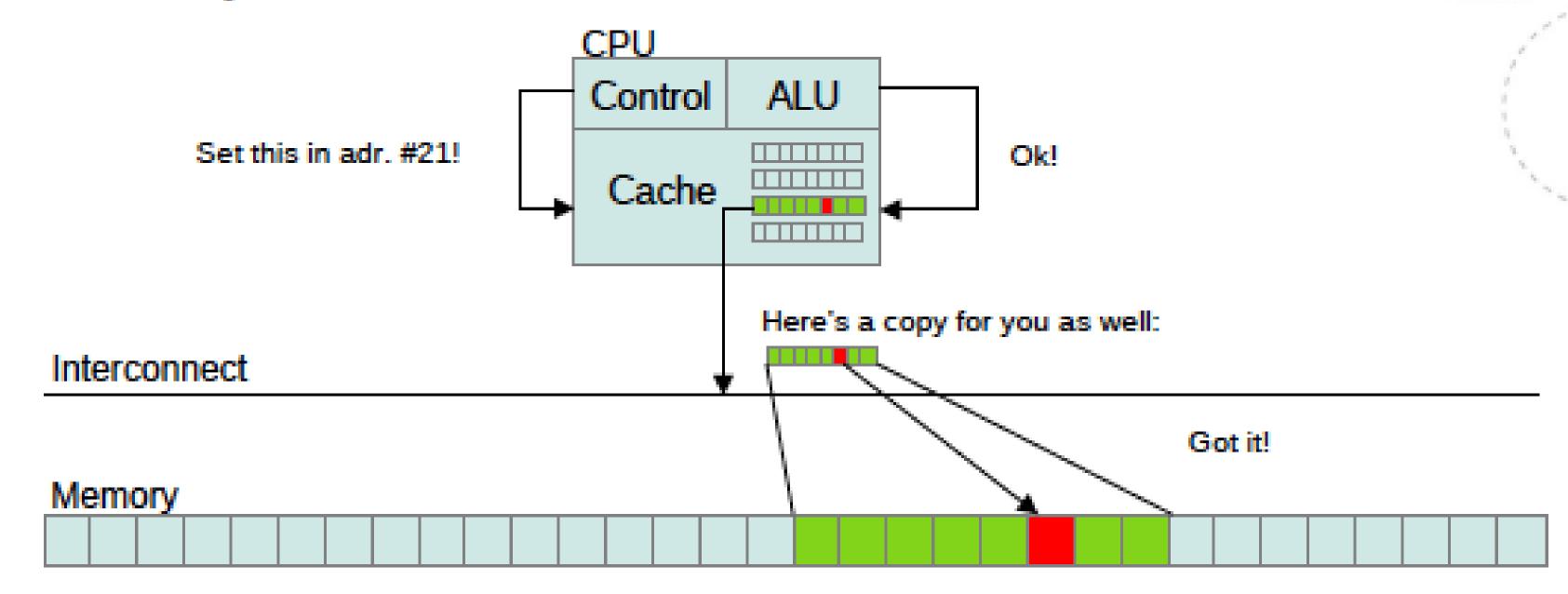
(Similar tradeoff to eager vs. lazy evaluation in programming languages)

# Caching – writing cached value

(JCM slides26 – slide 8)

# Write-through caching

 Write-through caches immediately pass their updates to main memory via the interconnect



# Caching – writing cached value

(based on JCM slides26)

See JCM slides 24, slides 8-24

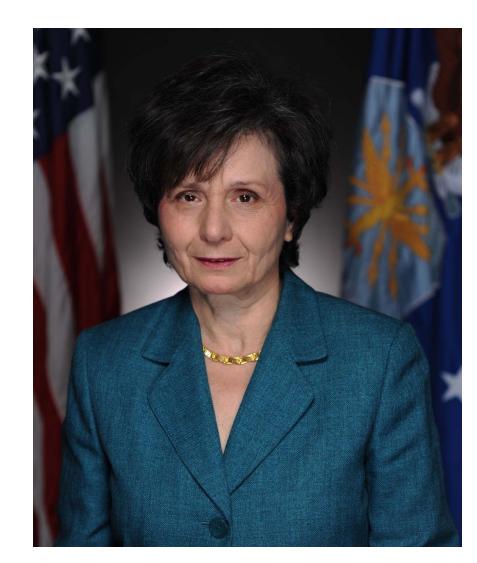
### MPI is SPMD!

- Single Program, Multiple Data
- Model proposed in 1984
  - by Dr. Frederica Darema (IBM)
    - later @ DARPA, NSF and now Director of US AirForce Research.
    - also known for <u>Dynamic Data Driven Application Systems</u> (DDDAS) proposed in 2000.

https://www.af.mil/About-Us/Biographies/Display/Article/3271084/dr-frederica-darema/

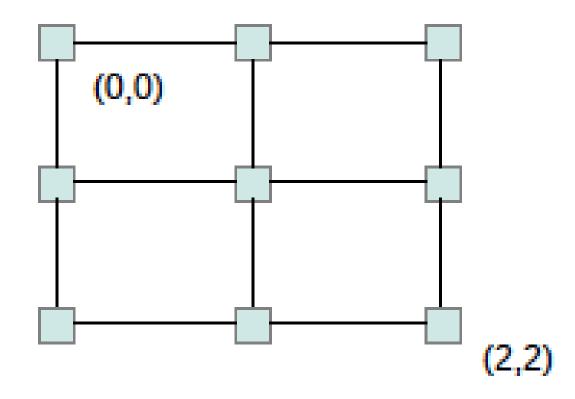
- Can be thought of as subcategory of MIMD
- MPI is SPMD!

Unlike SIMD, can use control statements like if/else to do execute different instructions concurrently



## MPI Virtual Topologies (from JCM Slides 08)

- The world communicator has no internal structure, everyone just gets a number for rank
- MPI lets you declare communicators that have structure, e.g. the Cartesian flavor, where every rank has a set of coordinates:
- This way you can send/receive messages with "rank at (1,1)" instead of having to calculate an indexing scheme yourself



 We can get communicators shaped like arbitrary graphs as well, but this rectangular thing is common

# **MPI Communication modes**

(based on JCM Slides 08)

Point-to-point messages (e.g. MPI\_ Send and MPI\_Recv) can be sent with 4 different guarantees for how they are transmitted:

- Standard (implementation default, more on this later)
- Synchronized (Send-function will not return until reception is acknowledged)
- Buffered (Explicitly manage the memory that's used for sending/receiving)
- Ready (Assume that the receiver has already initiated the receive)

# MPI Non-Blocking Communication

(based on JCM Slides 08)

- Usually, send and receive operations cause the program to stop and wait for the message to come through, and only resume the program afterwards
- This is not 100% true, but close enough for now
- Non-blocking sending and receiving immediately returns a request instead, so that you can continue calculating
- In order to make sure that the message has gone/come through, you must issue a wait-for-completion call for the request later on
  - Whenever you can no longer proceed without the comms being complete

## **More MPI**

#### **MPI Collectives:**

- MPI\_Barrier,
- MPI\_Bcast,
- MPI\_Scatter /MPI\_Gather (+ all-gather /all-scatter)

#### **MPI** Derived datatypes:

- combing several datatypes in a reusable buffer

#### **MPI Parallel I/O**

- Lecture with Jan Chr. Meyer on Sept 11!

# MPI functions -- OpenMPI

# MPI API (section 3 man pages)

<u>MPI</u>	MPI_File_call_errhandler	MPI Ineighbor allgather	MPI T init thread
MPIX_Allgather_init	MPI_File_close	MPI_Ineighbor_allgatherv	MPI_T_pvar_get_info
MPIX_Allgatherv_init	MPI File create errhandler	MPI_Ineighbor_alltoall	MPI T pvar get num
MPIX_Allreduce_init	MPI_File_delete	MPI_Ineighbor_alltoallv	MPI T pvar handle alloc
MPIX_Alltoall_init	MPI_File_f2c	MPI_Ineighbor_alltoallw	MPI_T_pvar_handle_free
MPIX_Alltoallv_init	MPI File get amode	MPI_Info_c2f	MPI_T_pvar_read
MPIX_Alltoallw_init	MPI_File_get_atomicity	MPI_Info_create	MPI_T_pvar_readreset
MPIX_Barrier_init	MPI File get byte offset	MPI_Info_delete	MPI_T_pvar_reset
MPIX_Bcast_init	MPI_File_get_errhandler	MPI_Info_dup	MPI_T_pvar_session_create
MPIX_Exscan_init	MPI_File_get_group	MPI_Info_env	MPI T pvar session free
MPIX Gather init	MPI_File_get_info	MPI_Info_f2c	MPI T pvar start
MPIX Gathery init	MPI_File_get_position	MPI_Info_free	MPI_T_pvar_stop
MPIX Neighbor allgather init	MPI File get position shared	MPI_Info_get	MPI_T_pvar_write
MPIX_Neighbor_allgatherv_init	MPI File get size	MPI Info get nkeys	MPI_Test
MPIX Neighbor alltoall init	MPI File get type extent	MPI Info get nthkey	MPI Test cancelled
MPIX Neighbor alltoally init	MPI_File_get_view	MPI_Info_get_valuelen	MPI_Testall
MPIX Neighbor alltoallw init	MPI File iread	MPI Info set	MPI_Testany
MPIX Query cuda support	MPI File iread all	MPI_Init	MPI_Testsome
MPIX Reduce init	MPI File iread at	MPI Init thread	MPI Topo test
MPIX Reduce scatter block init	MPI File iread at all	MPI_Initialized	MPI_Type_c2f
MPIX Reduce scatter init	MPI File iread shared	MPI Intercomm create	MPI Type commit
MPIX Scan init	MPI File iwrite	MPI_Intercomm_merge	MPI Type contiguous
MPIX Scatter init	MPI File iwrite all	MPI Iprobe	MPI Type create darray
MPIX Scattery init	MPI File iwrite at	MPI_Irecv	MPI Type create f90 complex
MPI Abort	MPI File iwrite at all	MPI Ireduce	MPI Type create f90 integer
MPI_Accumulate	MPI File iwrite shared	MPI Ireduce scatter	MPI Type create f90 real
MPI Add error class	MPI File open	MPI Ireduce scatter block	MPI Type create hindexed
MPI Add error code	MPI_File_preallocate	MPI_Irsend	MPI Type create hindexed block
MPI Add error string	MPI File read	MPI Is thread main	MPI Type create hvector
MPI Address	MPI File read all	MPI_Iscan	MPI Type create indexed block
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# Observing re. MPI

(from JCM slides09)

Every MPI function is called something like

MPI\_Abcd\_efg\_h

- "MPI\_" to begin with
- First letter in the function name is capitalized
- The rest of the name is all in lowercase, with underscore separation
- MPI uses arguments to pass variables in and out of functions
  - For the vast, vast majority of functions, return value is an error code that indicates whether the function completed in style or not
  - In order to obtain the answer from a function, you pass it a pointer to an area you have sized up to contain it, and let the function write it there

# Observing re. MPI

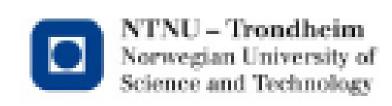
(from JCM slides09)

Why use pointer-arguments instead of C's own return values?

- There is actually a reasonable rationale behind this, you will find that system libraries and many other libraries do it as well
- The purpose is to give the programmer complete control over allocation
- If you're coming from an OO language, it's tempting to build 'constructors' for your structs like this:

```
my_thing * create_thing( int a, int b, int c) { /* malloc in here */ }
void destroy_thing ( my_thing *dead ) { free ( dead ); }
and use them like this
my_thing *newThing = create_thing (1,2,3);
destroy_thing ( newThing );
```

This will force all my\_things into the heap



# Parallel Computing is Fun!

