Assemblaggio di un programma MIPS

Passate

Esempio

Passate

Il processo di assemblaggio procede secondo diversi passaggi logici:

Espansione / sostituzione

- Ogni pseudo-istruzione viene espansa in una sequenza di istruzioni native
- I registri simbolici vengono sostituiti con i registri numerici

Allocazione dati e istruzioni

- Allocazione dei dati specificati nella sezione .data
- Allocazione delle istruzioni nella sezione .text

Costruzione della tabella dei simboli

Ad ogni simbolo viene associato un indirizzo

Assemblaggio

Le istruzioni vengono assemblate

Creazione file binario

- Il risultato dell'assemblaggio viene strutturato secondo lo specifico formato di uscita
- Per esempio il formato elf, hex, axf, ...

Programma

Consideriamo il seguente programma

- Somma i primi sei valori della sequenza di Fibonacci presenti nel vettore V
- Salva il risultato nella variabile SUM.

```
.data
      .byte 1, 1, 2, 3, 5, 8
v:
      .align 2
SUM:
      .word
      .text
      la $t0, V
MAIN:
      li $t1, 6
      li $t2, 0
      1b $t3, ($t0)
LOOP:
      add
                   $t2, $t2, $t3
      addi
                 $t0, $t0, +1
      addi
                $t1, $t1, -1
      bne
                   $t1, $zero, LOOP
      la $t0, SUM
      sw $t2, ($t0)
```

Passo 1: Espansione psudo-istruzioni

- Vengono espanse le pseudo-istruzioni
- I nomi simbolici dei registri sono mantenuti
- Le scritture simboliche high(X) e low(X) indicano le half-word alta e bassa del valore del simbolo X

```
.data
     .byte 1, 1, 2, 3, 5, 8
v:
      .align 2
      word 5
SUM:
      .text
             $t0, V
MAIN:
      la
             $t1, 6
            $t2, 0
            $t3, ($t0)
LOOP:
                                 # 5
      add
            $t2, $t2, $t3
            $t0, $t0, +1
      addi
      addi
            $t1, $t1, -1
            $t1, $zero, LOOP
      bne
            $t0, SUM
      la
            $t2, ($t0)
                                 # 10
```

```
.data
      .byte 1, 1, 2, 3, 5, 8
      .align 2
      .word 5
SUM:
      .text
             $at, high(V)
MAIN:
      lui
             $t0, $at, low(V)
      addiu $t1, $zero, 6
      addiu $t2, $zero, 0
LOOP:
             $t3, ($t0)
            $t2, $t2, $t3
      add
            $t0, $t0, +1
      addi
      addi
             $t1, $t1, -1
            $t1, $zero, LOOP
      bne
      lui $at, high(SUM)
      ori $t0, $at, low(SUM)
            $t2, ($t0)
                                  # 10
      SW
```

Passo 2: Sostituzione dei registri

I nomi simbolici dei registri sono sostituiti con i valori numerici

```
.data
      .byte 1, 1, 2, 3, 5, 8
v:
      .align 2
SUM:
     .word 5
      .text
MAIN:
     lui
            $at, high(V)
            $t0, $at, low(V)
      ori
      addiu $t1, $zero, 6
      addiu $t2, $zero, 0
            $t3, ($t0)
LOOP: 1b
            $t2, $t2, $t3
      add
      addi
            $t0, $t0, +1
                                 # 7
            $t1, $t1, -1
      addi
            $t1, $zero, LOOP
      bne
      lui $at, high(SUM)
      ori $t0, $at, low(SUM)
      sw $t2, ($t0)
                                 # 10
```

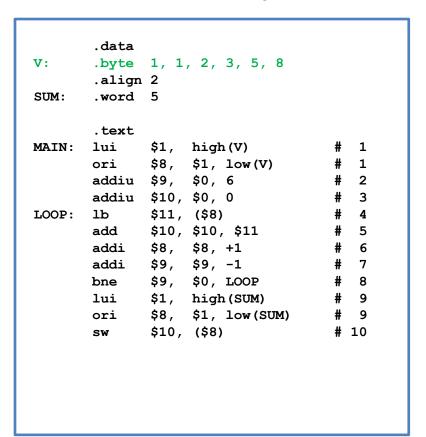
```
.data
      .byte 1, 1, 2, 3, 5, 8
      .align 2
SUM:
      .word 5
      .text
MAIN:
      lui
            $1, high(V)
            $8, $1, low(V)
                                # 1
      ori
      addiu $9, $0, 6
      addiu $10, $0, 0
LOOP:
      lb
            $11, ($8)
            $10, $10, $11
      add
      addi
            $8, $8, +1
            $9, $9, -1
      addi
            $9, $0, LOOP
      bne
      lui
            $1, high(SUM)
      ori $8, $1, low(SUM)
            $10, ($8)
                                # 10
```

■ La sezione .data inizia all'indirizzo 0x10010000

```
.data
     .byte 1, 1, 2, 3, 5, 8
v:
      .align 2
    .word 5
SUM:
      .text
MAIN: lui
            $1, high(V)
            $8, $1, low(V)
      ori
      addiu $9, $0, 6
      addiu $10, $0, 0
            $11, ($8)
LOOP: 1b
            $10, $10, $11
      add
      addi
            $8, $8, +1
      addi
            $9, $9, -1
            $9, $0, LOOP
      bne
      lui $1, high(SUM)
      ori $8, $1, low(SUM)
                                # 9
      sw $10, ($8)
                                # 10
```

| 0x10010000 | |
|------------|--|
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| | |

Alloca e inizializza 6 byte del vettore V



| | | 1 |
|------------|---|------------|
| 0x10010000 | V | 0x03020101 |
| 0x10010004 | | 0x00000805 |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |

Allinea l'indirizzo alla prossima parola

```
.data
      .byte 1, 1, 2, 3, 5, 8
v:
      .align 2
     .word 5
SUM:
      .text
            $1, high(V)
MAIN: lui
            $8, $1, low(V)
      ori
      addiu $9, $0, 6
      addiu
            $10, $0, 0
            $11, ($8)
LOOP:
      lb
            $10, $10, $11
      add
            $8, $8, +1
      addi
      addi
            $9, $9, -1
            $9, $0, LOOP
      bne
            $1, high(SUM)
      lui
      ori $8, $1, low(SUM)
                                 # 9
      sw $10, ($8)
                                 # 10
```

| 0x10010000 | V | 0x03020101 |
|------------|---|------------|
| 0x10010004 | | 0x00000805 |
| 0x10010008 | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |

Alloca ed inizializza la parola SUM

```
.data
      .byte 1, 1, 2, 3, 5, 8
v:
      .align 2
     .word 5
SUM:
      .text
MAIN: lui
            $1, high(V)
            $8, $1, low(V)
      ori
      addiu $9, $0, 6
      addiu
            $10, $0, 0
            $11, ($8)
      1b
LOOP:
            $10, $10, $11
      add
      addi
            $8, $8, +1
      addi
            $9, $9, -1
            $9, $0, LOOP
      bne
            $1, high(SUM)
      lui
      ori $8, $1, low(SUM)
                                 # 9
      sw $10, ($8)
                                 # 10
```

| 0x10010000 | V | 0x03020101 |
|------------|-----|------------|
| 0x10010004 | | 0x00000805 |
| 0x10010008 | SUM | 0x0000000 |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |

Passo 4: Allocazione istruzioni

■ La sezione .text inizia all'indirizzo 0x00400000

```
.data
     .byte 1, 1, 2, 3, 5, 8
v:
      .align 2
    .word 5
SUM:
      .text
            $1, high(V)
MAIN: lui
            $8, $1, low(V)
      ori
      addiu $9, $0, 6
      addiu $10, $0, 0
            $11, ($8)
LOOP:
      lb
            $10, $10, $11
      add
      addi
            $8, $8, +1
      addi
            $9, $9, -1
            $9, $0, LOOP
      bne
      lui $1, high(SUM)
      ori $8, $1, low(SUM)
                                # 9
      sw $10, ($8)
                                # 10
```

| | _ | |
|------------|-----|------------|
| 0x10010000 | v | 0x03020101 |
| 0x10010004 | | 0x00000805 |
| 0x10010008 | SUM | 0x0000000 |
| | | |
| | | |
| 0x00400000 | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |

Passo 4: Allocazione istruzioni

■ Le istruzioni e le relative etichette vengono ordinatamete allocate in memoria

```
.data
      .byte 1, 1, 2, 3, 5, 8
v:
      .align 2
     .word 5
SUM:
      .text
             $1, high(V)
MAIN: lui
             $8, $1, low(V)
      ori
      addiu $9, $0, 6
             $10, $0, 0
      addiu
             $11, ($8)
LOOP:
      1b
             $10, $10, $11
      add
      addi
             $8, $8, +1
             $9, $9, -1
      addi
             $9, $0, LOOP
      bne
             $1, high(SUM)
      lui
      ori
             $8, $1, low(SUM)
             $10, ($8)
                                  # 10
```

| 0x10010000 | v | | 0x03020101 |
|------------|------|-------|--------------------|
| 0x10010004 | | | 0x00000805 |
| 0x10010008 | SUM | | 0x00000000 |
| | | | |
| | | | |
| 0x00400000 | MAIN | lui | \$1, high(V) |
| 0x00400004 | | ori | \$8, \$1, low(V) |
| 0x00400008 | | addiu | \$9, \$0, 6 |
| 0x0040000C | | addiu | \$10, \$0, 0 |
| 0x00400010 | LOOP | 1b | \$11, (\$8) |
| 0x00400014 | | add | \$10, \$10, \$11 |
| 0x00400018 | | addi | \$8, \$8 +1 |
| 0x0040001C | | addi | \$9, \$9, -1 |
| 0x00400020 | | bne | \$9, \$0, LOOP |
| 0x00400024 | | lui | \$1, high(SUM) |
| 0x00400028 | | ori | \$8, \$1, low(SUM) |
| 0x0040002c | | sw | \$10, (\$8) |

Passo 5: Costruzione tablla dei simboli

Si costruisce la tabella dei simboli completa

| 0x10010000 | V | | 0x03020101 |
|------------|------|-------|--------------------|
| 0x10010004 | | | 0x00000805 |
| 0x10010008 | SUM | | 0x00000000 |
| | | | |
| | | | |
| 0x00400000 | MAIN | lui | \$1, high(V) |
| 0x00400004 | | ori | \$8, \$1, low(V) |
| 0x00400008 | | addiu | \$9, \$0, 6 |
| 0x0040000C | | addiu | \$10, \$0, 0 |
| 0x00400010 | LOOP | 1b | \$11, (\$8) |
| 0x00400014 | | add | \$10, \$10, \$11 |
| 0x00400018 | | addi | \$8, \$8 +1 |
| 0x0040001C | | addi | \$9, \$9, -1 |
| 0x00400020 | | bne | \$9, \$0, LOOP |
| 0x00400024 | | lui | \$1, high(SUM) |
| 0x00400028 | | ori | \$8, \$1, low(SUM) |
| 0x0040002c | | sw | \$10, (\$8) |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x0040000 |
| LOOP | 0x00400010 |

Istruzione: lui

Tipo: I: Immediate

- Opcode: 001111

- Registri: rs = N/A,00000 rt = 1,00001

- Immediato: V = 0x10010000 high(V) = 0x1001

| | opcode rs | | | | | | | | | rs rt | | | | | | | imm | | | | | | | | | | | | | | | |
|---|---------------------------------|--|--|--|--|---|---|---|---|-------|---|---|---|---|---|---|-----|---|---|---|---|---|---|--|--|---|---|--|--|---|---|--|
| Γ | 0 0 1 1 1 1 0 0 0 0 0 0 0 0 0 1 | | | | | | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | | | | | | | | | | |
| | 3 | | | | | (| 2 | | | (|) | | | 1 | L | | | 1 | L | | | (|) | | | (|) | | | : | L | |

| 0x10010000 | v | | 0x03020101 |
|------------|------|-------|--------------------|
| 0x10010004 | | | 0x00000805 |
| 0x10010008 | SUM | | 0x00000000 |
| | | | |
| | | | |
| 0x00400000 | MAIN | lui | \$1, high(V) |
| 0x00400004 | | ori | \$8, \$1, low(V) |
| 0x00400008 | | addiu | \$9, \$0, 6 |
| 0x0040000C | | addiu | \$10, \$0, 0 |
| 0x00400010 | LOOP | 1b | \$11, (\$8) |
| 0x00400014 | | add | \$10, \$10, \$11 |
| 0x00400018 | | addi | \$8, \$8 +1 |
| 0x0040001C | | addi | \$9, \$9, -1 |
| 0x00400020 | | bne | \$9, \$0, LOOP |
| 0x00400024 | | lui | \$1, high(SUM) |
| 0x00400028 | | ori | \$8, \$1, low(SUM) |
| 0x0040002c | | sw | \$10, (\$8) |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x00400000 |
| LOOP | 0x00400010 |

Istruzione: ori

Tipo: I: Immediate

- Opcode: 001101

- Registri: rs = 1,00001 rt = 8,01000

- Immediato: V = 0x10010000 low(V) = 0x0000

| opcode rs | | | | | | | | | | | | | rt | | | imm | | | | | | | | | | | | | | | |
|-----------|---|---|---|---|---|---|---|---|---|---|---|---|----|---|---|-----|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 0 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | 3 | 3 | | | 4 | 4 | | | 2 | 2 | | | 8 | 3 | | | (|) | | | (|) | | | (|) | | | (|) | |

| v | | 0x03020101 |
|------|-------|---|
| | | 0x00000805 |
| SUM | | 0x00000000 |
| | | |
| | | |
| MAIN | | 0x3C011001 |
| | ori | \$8, \$1, low(V) |
| | addiu | \$9, \$0, 6 |
| | addiu | \$10, \$0, 0 |
| LOOP | 1b | \$11, (\$8) |
| | add | \$10, \$10, \$11 |
| | addi | \$8, \$8 +1 |
| | addi | \$9, \$9, -1 |
| | bne | \$9, \$0, LOOP |
| | lui | \$1, high(SUM) |
| | ori | \$8, \$1, low(SUM) |
| | sw | \$10, (\$8) |
| | SUM | SUM MAIN ori addiu addiu LOOP lb add addi addi bne lui ori |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x00400000 |
| LOOP | 0x00400010 |

Istruzione: addiu

Tipo: I: Immediate

- Opcode: 001001

- Registri: rs = 0,00000 rt = 9,01001

| | | opc | ode | } | | | | rs | | | | | rt | | | | | | | | | | ir | nm | | | | | | | |
|---|---|-----|-----|---|---|---|---|----|---|---|---|---|----|---|---|---|---|---|---|---|---|---|----|----|---|---|---|---|---|---|---|
| 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 |
| | 2 | | | | 4 | 4 | | | (|) | | | 9 | 9 | | | (|) | | | (|) | | | (|) | | | (| 5 | |

| v | | 0x03020101 |
|------|-------|--|
| | | 0x00000805 |
| SUM | | 0x00000000 |
| | | |
| | | |
| MAIN | | 0x3C011001 |
| | | 0x34280000 |
| | addiu | \$9, \$0, 6 |
| | addiu | \$10, \$0, 0 |
| LOOP | 1b | \$11, (\$8) |
| | add | \$10, \$10, \$11 |
| | addi | \$8, \$8 +1 |
| | addi | \$9, \$9, -1 |
| | bne | \$9, \$0, LOOP |
| | lui | \$1, high(SUM) |
| | ori | \$8, \$1, low(SUM) |
| | sw | \$10, (\$8) |
| | SUM | SUM MAIN addiu addiu LOOP lb add addi addi bne lui ori |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x00400000 |
| LOOP | 0x00400010 |

Istruzione: addiu

Tipo: I: Immediate

- Opcode: 001001

- Registri: rs = 0,00000 rt = 10,01010

| | opcode 0 0 1 0 0 1 0 | | | | | | | | rs | | | | | rt | | | | | | | | | | ir | nm | | | | | | | |
|---|-------------------------|---|---|---|---|---|--|---|----|---------------|--|--|---|----|--|--|---|---|---|---|---|---|---|----|----|---|---|---|---|---|---|---|
| Γ | 0 | 0 | 1 | 0 | 0 | 1 | | 0 | 0 | 0 0 0 0 1 0 1 | | | | | | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | 2 | | | | 4 | 4 | | | (|) | | | 1 | A | | | (|) | | | (|) | | | (|) | | | (|) | | |

| V | | 0x03020101 |
|------|-------|---|
| | | 0x00000805 |
| SUM | | 0x0000000 |
| | | |
| | | |
| MAIN | | 0x3C011001 |
| | | 0x34280000 |
| | | 0x24090006 |
| | addiu | \$10, \$0, 0 |
| LOOP | 1b | \$11, (\$8) |
| | add | \$10, \$10, \$11 |
| | addi | \$8, \$8 +1 |
| | addi | \$9, \$9, -1 |
| | bne | \$9, \$0, LOOP |
| | lui | \$1, high(SUM) |
| | ori | \$8, \$1, low(SUM) |
| | sw | \$10, (\$8) |
| | SUM | SUM MAIN addiu LOOP 1b add addi addi bne lui ori |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x00400000 |
| LOOP | 0x00400010 |

Istruzione: lb

Tipo: I: Immediate

- Opcode: 001001

- Registri: rs = 8,01000 rt = 11,01011

| | | opc | ode |) | | | | rs | | | | | rt | | | | | | | | | | ir | nm | | | | | | | |
|---|---|-----|-----|---|---|---|---|----|---|---|---|---|----|---|---|---|---|---|---|---|---|---|----|----|---|---|---|---|---|---|---|
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | 8 | | | | : | L | | | (|) | | | 1 | 3 | | | (|) | | | (|) | | | (|) | | | (|) | |

| v | | 0x03020101 |
|------|------|--|
| | | 0x00000805 |
| SUM | | 0x00000000 |
| | | |
| | | |
| MAIN | | 0x3C011001 |
| | | 0x34280000 |
| | | 0x24090006 |
| | | 0x240A0000 |
| LOOP | 1b | \$11, (\$8) |
| | add | \$10, \$10, \$11 |
| | addi | \$8, \$8 +1 |
| | addi | \$9, \$9, -1 |
| | bne | \$9, \$0, LOOP |
| | lui | \$1, high(SUM) |
| | ori | \$8, \$1, low(SUM) |
| | sw | \$10, (\$8) |
| | SUM | SUM MAIN LOOP 1b add addi addi bne lui ori |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x00400000 |
| LOOP | 0x00400010 |

Istruzione: add

Tipo: R: Register

- Opcode: 000000 funct = 100000 shamt = 0000

- Registri: rs = 10,01010 rt = 11,01011 rd = 10,01010

| | | opc | | | | rs | | | | | rt | | | | | rd | | | | s | han | nt | | | | fur | nct | | | | |
|---|---|-----|---|---|---|----|---|---|---|---|----|-----------|---|---|--|----|---|---|---|---|-----|----|---|---|---|-----|-----|---|---|---|---|
| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 0 1 1 0 | | | | | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
| | 0 | | | | 1 | L | | | 4 | 1 | | | 1 | 3 | | | į | 5 | | | (|) | | | 2 | 2 | | | (|) | |

| 0x10010000 | v | | 0x03020101 |
|------------|------|------|--------------------|
| 0x10010004 | | | 0x00000805 |
| 0x10010008 | SUM | | 0x00000000 |
| | | | |
| | | | |
| 0x00400000 | MAIN | | 0x3C011001 |
| 0x00400004 | | | 0x34280000 |
| 0x00400008 | | | 0x24090006 |
| 0x0040000C | | | 0x240A0000 |
| 0x00400010 | LOOP | | 0x810B0000 |
| 0x00400014 | | add | \$10, \$10, \$11 |
| 0x00400018 | | addi | \$8, \$8 +1 |
| 0x0040001C | | addi | \$9, \$9, -1 |
| 0x00400020 | | bne | \$9, \$0, LOOP |
| 0x00400024 | | lui | \$1, high(SUM) |
| 0x00400028 | | ori | \$8, \$1, low(SUM) |
| 0x0040002c | | sw | \$10, (\$8) |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x0040000 |
| LOOP | 0x00400010 |

Istruzione: addi

Tipo:I: Immediate

- Opcode: 001000

- Registri: rs = 8,01000 rt = 8,01000

| | | opo | ode |) | | | rs | | | | | rt | | | | | | | | | | ir | nm | | | | | | | |
|---|---------------------|-----|-----|---|---|---|----|---|---|---|---|----|---|---|---|---|---|---|---|---|---|----|----|---|---|---|---|---|---|--|
| [| 0 0 1 0 0 0 0 1 0 0 | | | | | | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | |
| | 2 | | | | 1 | L | | (|) | | | 8 | 3 | | | (|) | | | (|) | | | (|) | | | 1 | L | |

| 0x10010000 | V | | 0x03020101 |
|------------|------|------|--------------------|
| 0x10010004 | | | 0x00000805 |
| 0x10010008 | SUM | | 0x0000000 |
| | | | |
| | | | |
| 0x00400000 | MAIN | | 0x3C011001 |
| 0x00400004 | | | 0x34280000 |
| 0x00400008 | | | 0x24090006 |
| 0x0040000C | | | 0x240A0000 |
| 0x00400010 | LOOP | | 0x810B0000 |
| 0x00400014 | | | 0x014B5020 |
| 0x00400018 | | addi | \$8, \$8 +1 |
| 0x0040001C | | addi | \$9, \$9, -1 |
| 0x00400020 | | bne | \$9, \$0, LOOP |
| 0x00400024 | | lui | \$1, high(SUM) |
| 0x00400028 | | ori | \$8, \$1, low(SUM) |
| 0x0040002c | | sw | \$10, (\$8) |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x00400000 |
| LOOP | 0x00400010 |

Istruzione: addi

Tipo: I: Immediate

- Opcode: 001000

- Registri: rs = 9,01001 rt = 9,01001

- Immediato: -1 = 0xFFFFFFF

| | | op | cod | de | | | | rs | | | | rt | | | | | | | | | | ir | nm | | | | | | | |
|---|---------------------------|----|-----|----|--|---|---|----|---|---|---|----|---|---|---|---|---|---|---|---|---|----|----|---|---|---|--|---|--|--|
| 0 | 0 0 1 0 0 0 0 1 0 0 1 0 1 | | | | | | | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | | | | |
| | 2 | | | | | 1 | L | | 2 | 2 | | 9 | 9 | | | 1 | • | | | I | • | | | 1 | • | | | 1 | | |

| 0x10010000 | V | | 0x03020101 |
|------------|------|------|--------------------|
| 0x10010004 | | | 0x00000805 |
| 0x10010008 | SUM | | 0x0000000 |
| | | | |
| | | | |
| 0x00400000 | MAIN | | 0x3C011001 |
| 0x00400004 | | | 0x34280000 |
| 0x00400008 | | | 0x24090006 |
| 0x0040000C | | | 0x240A0000 |
| 0x00400010 | LOOP | | 0x810B0000 |
| 0x00400014 | | | 0x014B5020 |
| 0x00400018 | | | 0x21080001 |
| 0x0040001C | | addi | \$9, \$9, -1 |
| 0x00400020 | | bne | \$9, \$0, LOOP |
| 0x00400024 | | lui | \$1, high(SUM) |
| 0x00400028 | | ori | \$8, \$1, low(SUM) |
| 0x0040002c | | sw | \$10, (\$8) |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x00400000 |
| LOOP | 0x00400010 |

Istruzione: bne

Tipo: I: Immediate

- Opcode: 000101

- Registri: rs = 9,01001 rt = 0,00000

- Immediato: (LOOP - PC - 4) >> 2 = (0x00400010 - 0x00400020 - 4) >> 2 = 0xFFFB

| | | (| opc | ode | • | | | rs | | | | rt | | | | | | | | | | ir | nm | | | | | | | |
|---|-----------------------------|---|-----|-----|---|---|---|----|---|---|---|----|---|---|---|---|---|---|---|---|---|----|----|---|---|--|--|---|---|--|
| Γ | 0 0 0 1 0 1 0 1 0 0 1 0 0 0 | | | | | | | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | | | | | | |
| | 1 | | | | | ţ | 5 | | 2 | 2 | | (|) | | | I | ? | | | I | 7 | | | 1 | • | | | I | 3 | |

| v | | 0x03020101 |
|------|-----|------------------------------|
| | | 0x00000805 |
| SUM | | 0x00000000 |
| | | |
| | | |
| MAIN | | 0x3C011001 |
| | | 0x34280000 |
| | | 0x24090006 |
| | | 0x240A0000 |
| LOOP | | 0x810B0000 |
| | | 0x014B5020 |
| | | 0x21080001 |
| | | 0x2129FFFF |
| | bne | \$9, \$0, LOOP |
| | lui | \$1, high(SUM) |
| | ori | \$8, \$1, low(SUM) |
| | sw | \$10, (\$8) |
| | SUM | SUM MAIN LOOP bne lui ori |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x0040000 |
| LOOP | 0x00400010 |

Istruzione: lui

Tipo: I: Immediate

- Opcode: 001111

- Registri: rs = N/A,00000 rt = 1,00001

- Immediato: SUM=0x10010008 high(SUM) = 0x1001

| | | (| opc | ode | } | | | rs | | | | rt | | | | | | | | | | ir | nm | | | | | | | |
|---|-------------------------------|---|-----|-----|---|---|---|----|---|---|---|----|---|---|---|---|---|---|---|---|---|----|----|---|---|--|--|---|---|--|
| Γ | 0 0 1 1 1 1 0 0 0 0 0 0 0 0 0 | | | | | | | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | | | | | | |
| | 3 | | | | | (| 2 | | (|) | | 1 | L | | | 1 | L | | | (|) | | | (|) | | | : | L | |

| 0x10010000 | v | | 0x03020101 |
|------------|------|-----|--------------------|
| 0x10010004 | | | 0x00000805 |
| 0x10010008 | SUM | | 0x00000000 |
| | | | |
| | | | |
| 0x00400000 | MAIN | | 0x3C011001 |
| 0x00400004 | | | 0x34280000 |
| 0x00400008 | | | 0x24090006 |
| 0x0040000C | | | 0x240A0000 |
| 0x00400010 | LOOP | | 0x810B0000 |
| 0x00400014 | | | 0x014B5020 |
| 0x00400018 | | | 0x21080001 |
| 0x0040001C | | | 0x2129FFFF |
| 0x00400020 | | | 0x1520FFFF |
| 0x00400024 | | lui | \$1, high(SUM) |
| 0x00400028 | | ori | \$8, \$1, low(SUM) |
| 0x0040002c | | sw | \$10, (\$8) |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x00400000 |
| LOOP | 0x00400010 |

Istruzione: ori

Tipo: I: Immediate

- Opcode: 001101

- Registri: rs = 1,00001 rt = 8,01000

- Immediato: SUM=0x10010008 low(SUM) = 0x0008

| opcode rs | | | | | | rt imm | | | | | | | | | | | | | | | | | |
|-----------|-------|---|---|---|---|--------|---|---|---|---|---|---|---|---|---|---|-------------------------------|-----|--|--|--|---|--|
| 0 | T | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 | | | | | 0 | |
| | 3 4 2 | | | | 8 | | | | | 0 | | | | 0 | | | | 0 8 | | | | | |

| v | | 0x03020101 |
|------|-----|--------------------|
| | | 0x00000805 |
| SUM | | 0x00000000 |
| | | |
| | | |
| MAIN | | 0x3C011001 |
| | | 0x34280000 |
| | | 0x24090006 |
| | | 0x240A0000 |
| LOOP | | 0x810B0000 |
| | | 0x014B5020 |
| | | 0x21080001 |
| | | 0x2129FFFF |
| | | 0x1520FFFF |
| | | 0x3c011001 |
| | ori | \$8, \$1, low(SUM) |
| | sw | \$10, (\$8) |
| | SUM | SUM MAIN LOOP ori |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x00400000 |
| LOOP | 0x00400010 |

Istruzione: sw

Tipo: I: Immediate

- Opcode: 101011

- Registri: rs = 8,01000 rt = 10,01010

| | opcode rs rt | | | | | | | imm | | | | | | | | | | | | | |
|---|--------------|---------|---|---|---|-------|---|-----|---|---|---|---|---|---|---|---|-------------------------------|--|--|--|---|
| Γ | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 | | | | 0 |
| | | A D 0 A | | | | 0 0 0 | | | | | (|) | | | | | | | | | |

| 0x10010000 | v | 0x03020101 |
|------------|------|----------------|
| 0x10010004 | | 0x00000805 |
| 0x10010008 | SUM | 0x0000000 |
| | | |
| | | |
| 0x00400000 | MAIN | 0x3C011001 |
| 0x00400004 | | 0x34280000 |
| 0x00400008 | | 0x24090006 |
| 0x0040000C | | 0x240A0000 |
| 0x00400010 | LOOP | 0x810B0000 |
| 0x00400014 | | 0x014B5020 |
| 0x00400018 | | 0x21080001 |
| 0x0040001C | | 0x2129FFFF |
| 0x00400020 | | 0x1520FFFF |
| 0x00400024 | | 0x3c011001 |
| 0x00400028 | | 0x34280008 |
| 0x0040002c | | sw \$10, (\$8) |

| Symbol | Address |
|--------|------------|
| v | 0x10010000 |
| SUM | 0x10010008 |
| MAIN | 0x00400000 |
| LOOP | 0x00400010 |

Finalizzazione

La mappa binaria della memorie viene infine strutturata secondo il formato di uscita

| v | 0x03020101 |
|------|------------|
| | 0x00000805 |
| SUM | 0x0000000 |
| | |
| | |
| MAIN | 0x3C011001 |
| | 0x34280000 |
| | 0x24090006 |
| | 0x240A0000 |
| LOOP | 0x810B0000 |
| | 0x014B5020 |
| | 0x21080001 |
| | 0x2129FFFF |
| | 0x1520FFFF |
| | 0x3c011001 |
| | 0x34280008 |
| | 0xAD0A0000 |
| | SUM |