

Diagrama de flujo

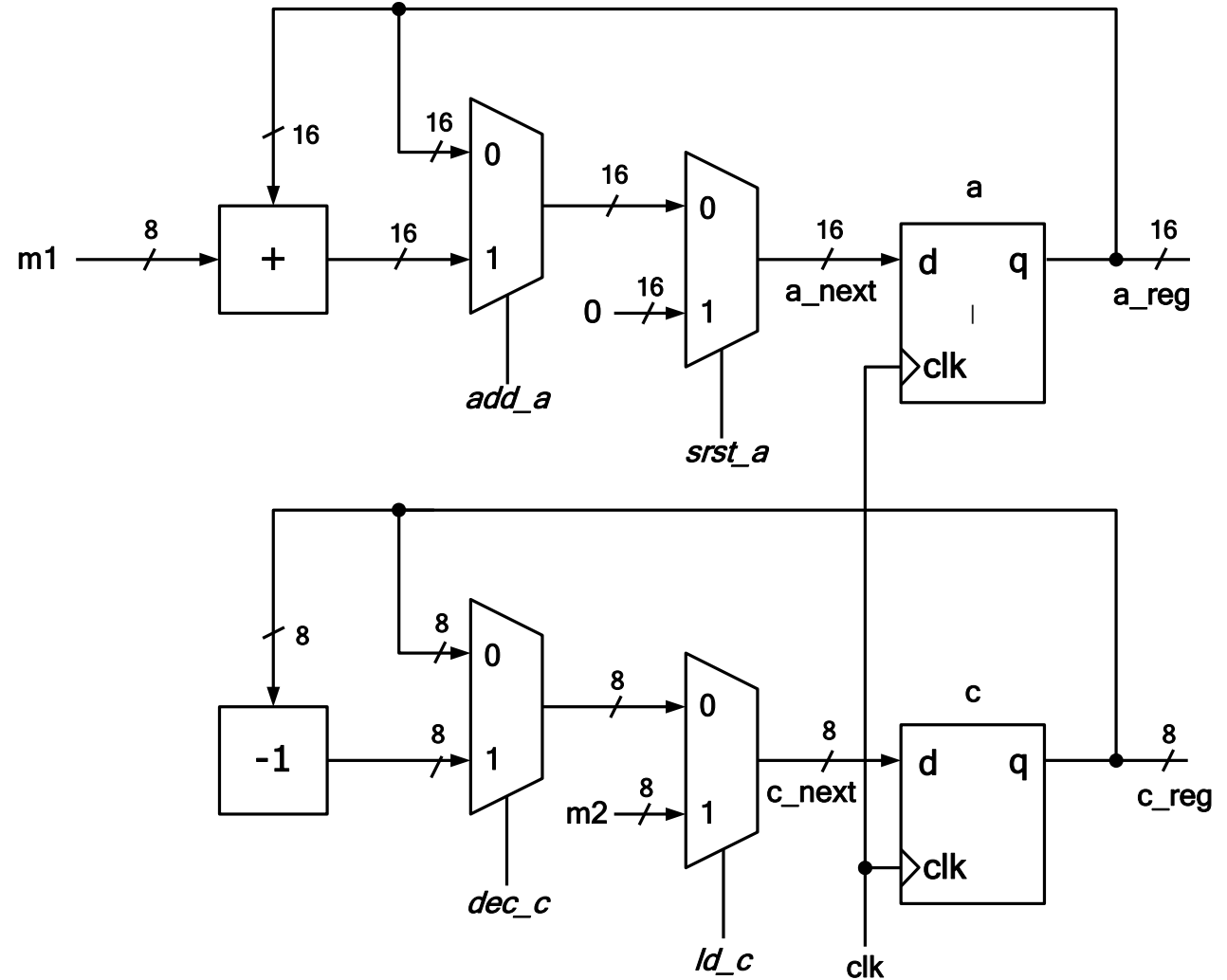
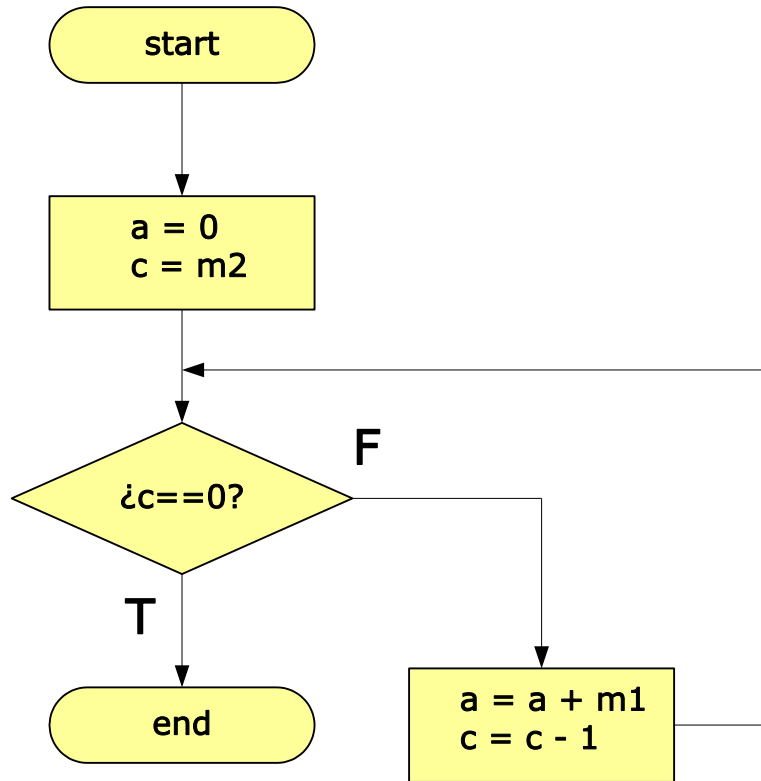


Diagrama temporal

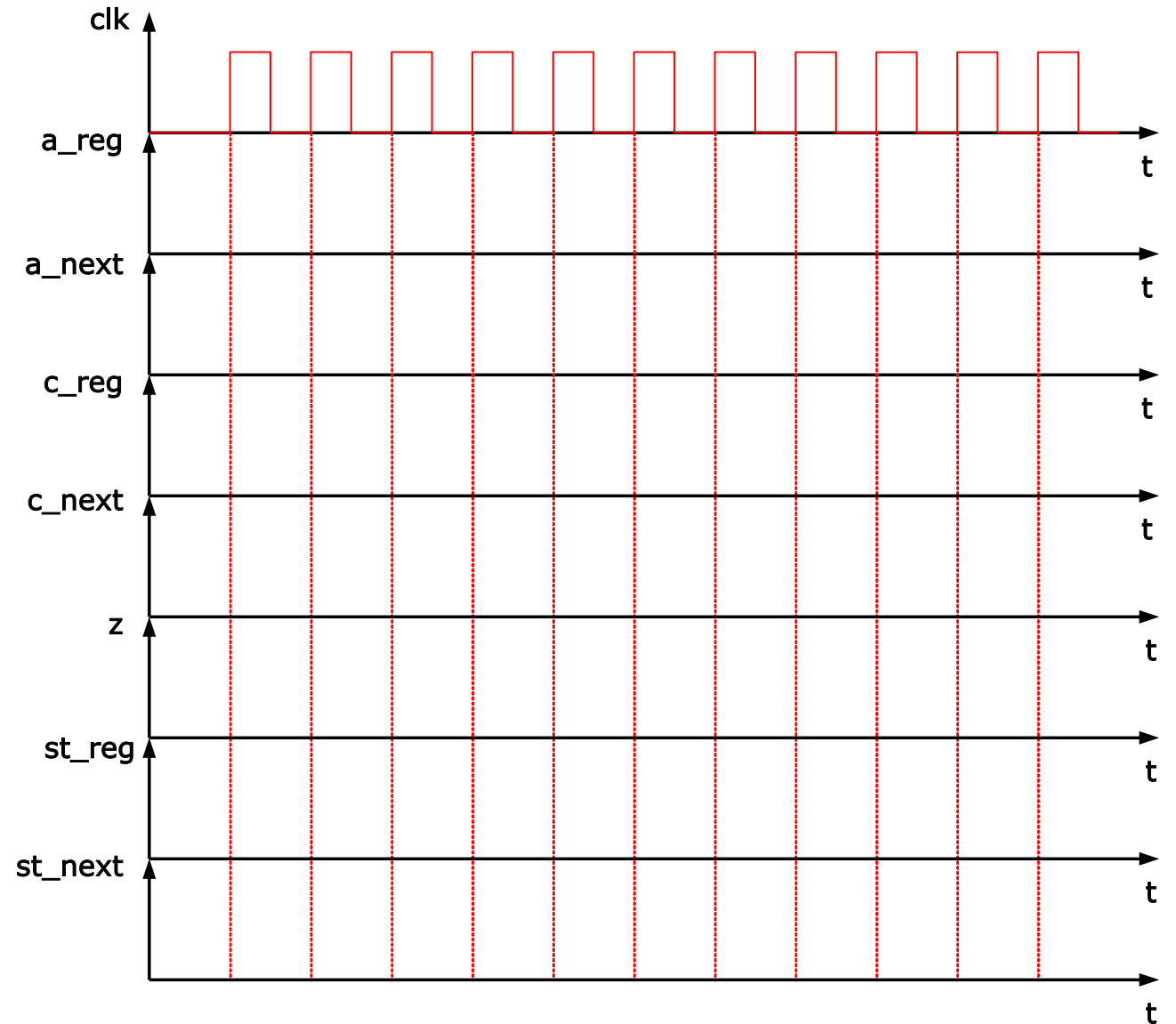
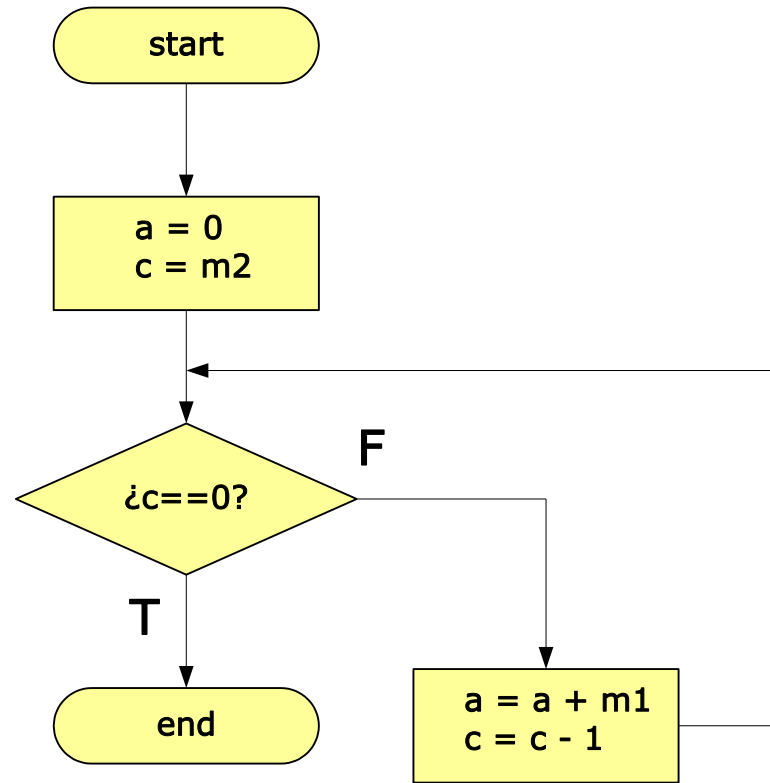


Tabla de transiciones

st_reg	st_next	
	Z = 0	Z = 1
0 0		
0 1		
1 0		
1 1		
0 0		

st_reg(1), st_reg(0)		00	01	11	10
z	0				
	1				

st_reg(1), st_reg(0)		00	01	11	10
z	0				
	1				

Tabla de microoperaciones

st_reg	srst_a	inc_a	ld_c	dec_c
0 0				
0 1				
1 0				
1 1				
0 0				

Circuito completo

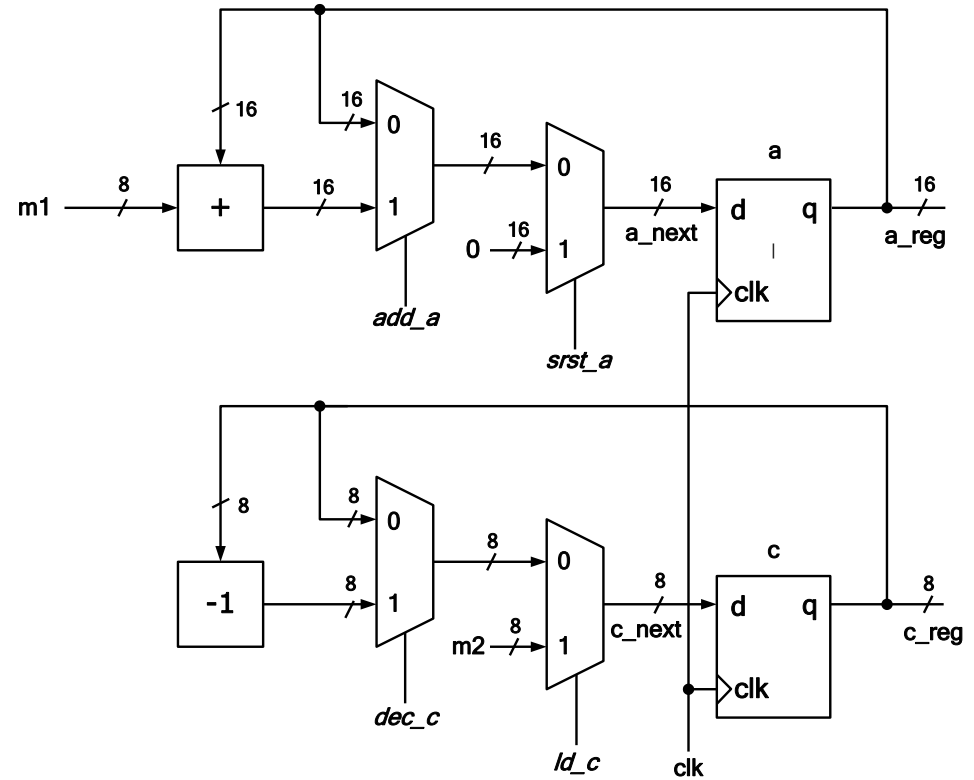


Diagrama temporal

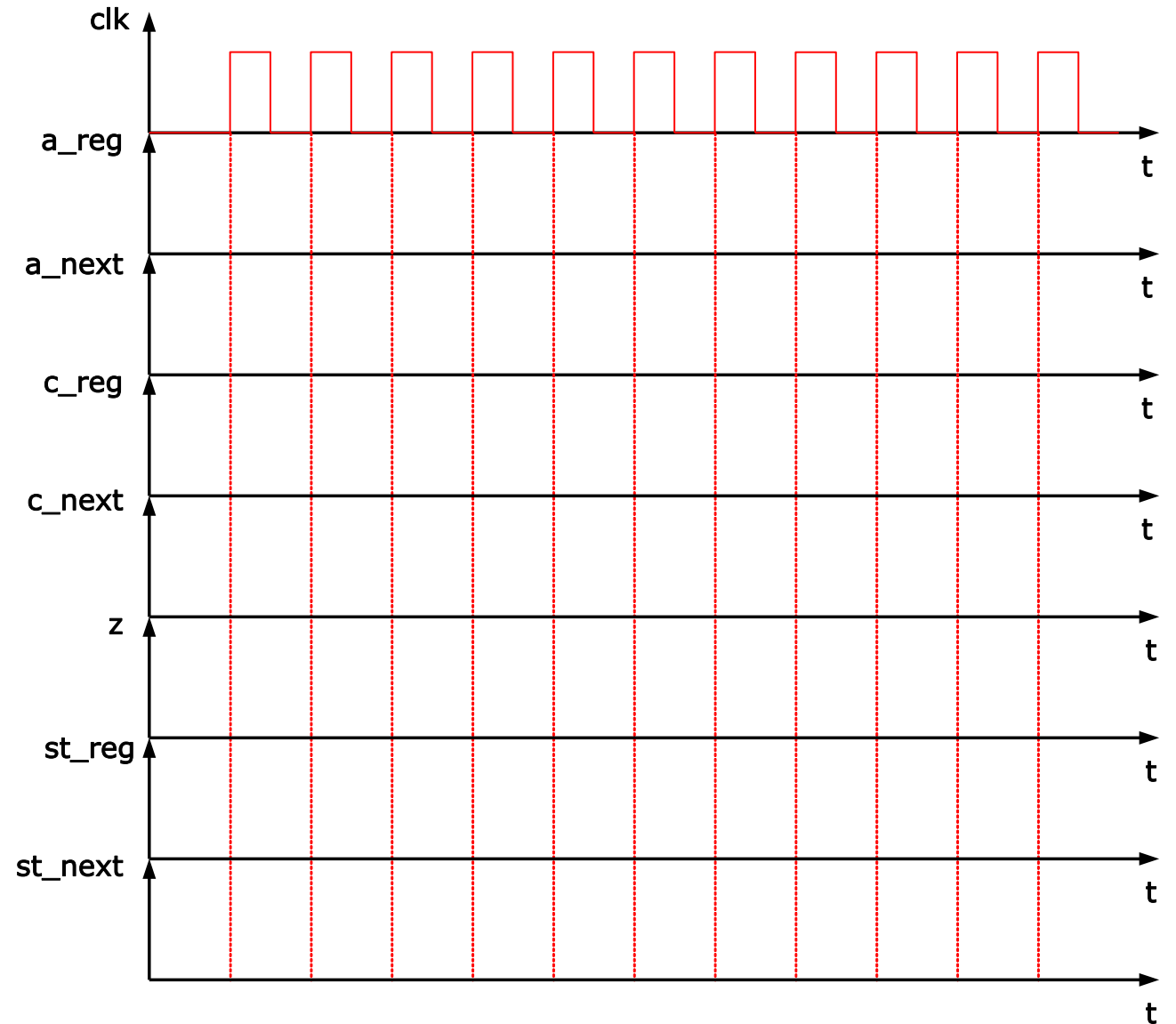
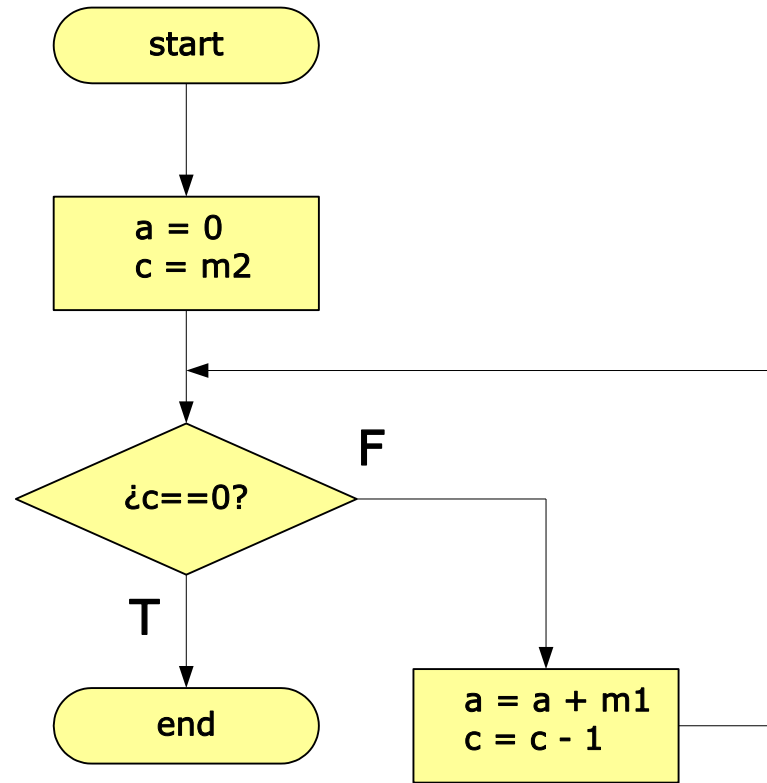


Tabla de transiciones

st_reg	st_next	
	Z = 0	Z = 1
0 0		
0 1		
1 0		
1 1		
0 0		

st_reg(1), st_reg(0)

z	00	01	11	10
0				
1				

st_reg(1), st_reg(0)

z	00	01	11	10
0				
1				

Tabla de microoperaciones

st_reg	srst_a	inc_a	ld_c	dec_c
0 0				
0 1				
1 0				
1 1				
0 0				

Circuito completo

