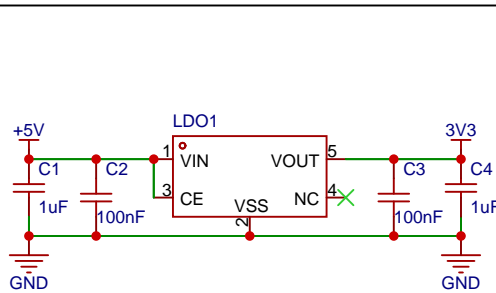
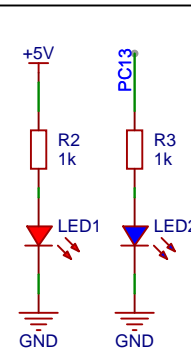


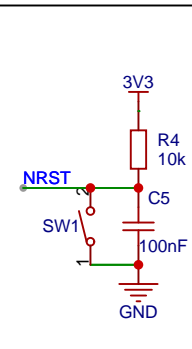
Type-C供电/下载



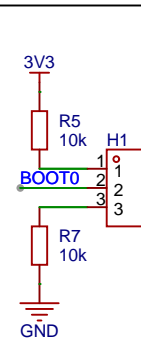
LDO-5V转3.3V电路



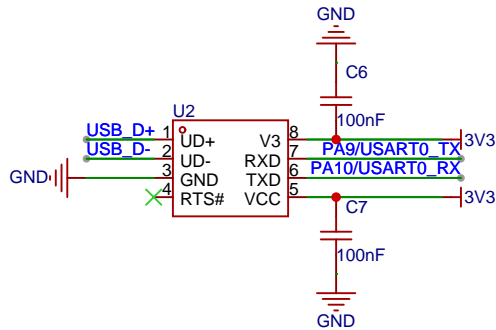
LED指示电路



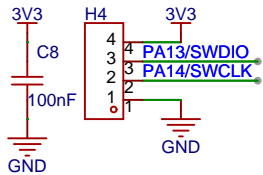
复位电路



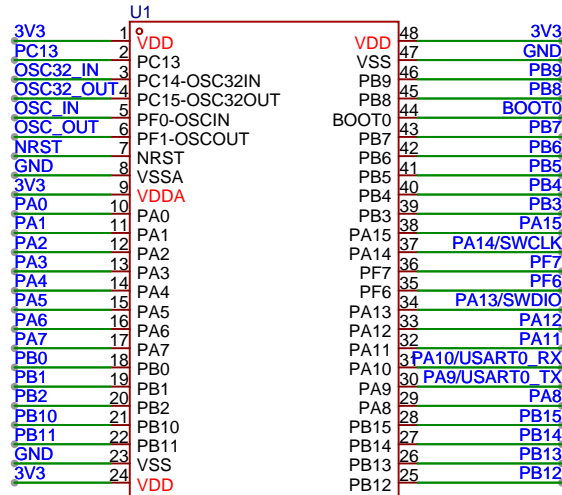
BOOT模式选择



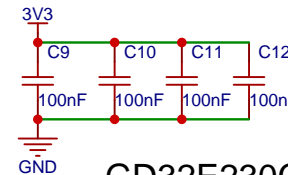
串口通信&下载



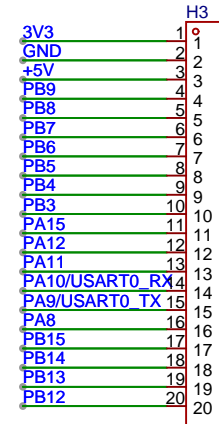
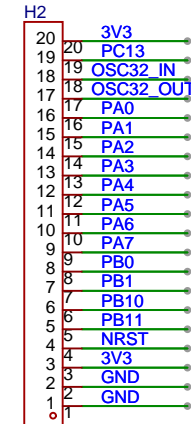
SWD下载接口



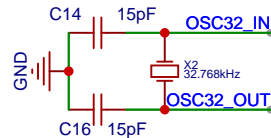
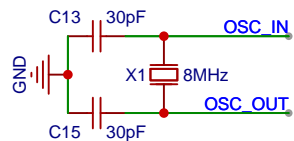
GD32E230C8T6



GD32E230C8T6



拓展排针接口



晶振电路

原理图

SCH_GD32E230

更新日期

2023-10-08

图页

P1

创建日期

2023-10-08

绘制

物料编码

审阅

GD32E230最小系统板

版本

尺寸

页

1

共

1

V1.0

A4

嘉立创EDA

