1. Description

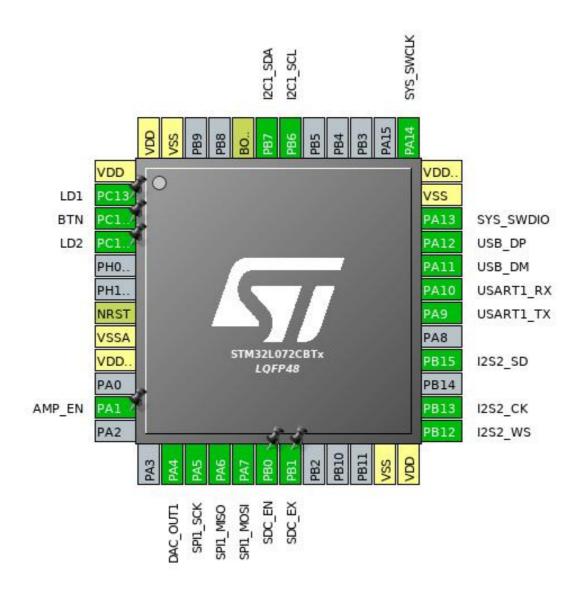
1.1. Project

| Project Name | sdcard |
|-----------------|-------------------|
| Board Name | custom |
| Generated with: | STM32CubeMX 5.0.0 |
| Date | 11/26/2018 |

1.2. MCU

| MCU Series | STM32L0 |
|----------------|---------------|
| MCU Line | STM32L0x2 |
| MCU name | STM32L072CBTx |
| MCU Package | LQFP48 |
| MCU Pin number | 48 |

2. Pinout Configuration

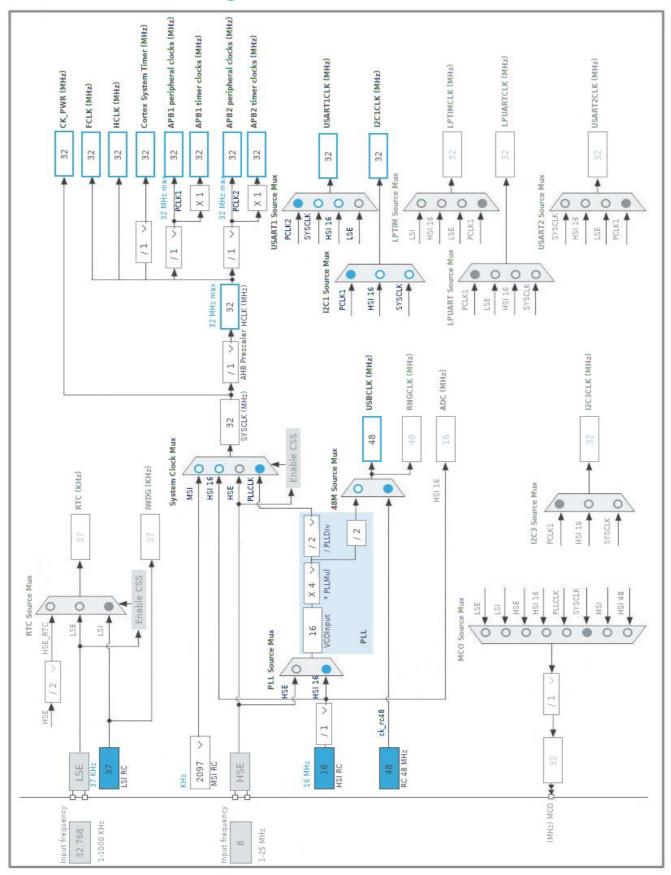


3. Pins Configuration

| Pin Number LQFP48 | Pin Name (function after | Pin Type | Alternate Function(s) | Label |
|----------------------|-----------------------------|----------|--------------------------|--------|
| | reset) | _ | | |
| 1 | VDD | Power | | |
| 2 | PC13 * | I/O | GPIO_Output | LD1 |
| 3 | PC14-OSC32_IN | I/O | GPIO_EXTI14 | BTN |
| 4 | PC15-OSC32_OUT * | I/O | GPIO_Output | LD2 |
| 7 | NRST | Reset | | |
| 8 | VSSA | Power | | |
| 9 | VDDA | Power | | |
| 11 | PA1 * | I/O | GPIO_Output | AMP_EN |
| 14 | PA4 | I/O | DAC_OUT1 | |
| 15 | PA5 | I/O | SPI1_SCK | |
| 16 | PA6 | I/O | SPI1_MISO | |
| 17 | PA7 | I/O | SPI1_MOSI | |
| 18 | PB0 * | I/O | GPIO_Output | SDC_EN |
| 19 | PB1 | I/O | GPIO_EXTI1 | SDC_EX |
| 23 | VSS | Power | | |
| 24 | VDD | Power | | |
| 25 | PB12 | I/O | 12S2_WS | |
| 26 | PB13 | I/O | 12S2_CK | |
| 28 | PB15 | I/O | 12S2_SD | |
| 30 | PA9 | I/O | USART1_TX | |
| 31 | PA10 | I/O | USART1_RX | |
| 32 | PA11 | I/O | USB_DM | |
| 33 | PA12 | I/O | USB_DP | |
| 34 | PA13 | I/O | SYS_SWDIO | |
| 35 | VSS | Power | _ | |
| 36 | VDD_USB | Power | | |
| 37 | PA14 | I/O | SYS_SWCLK | |
| 42 | PB6 | I/O | I2C1_SCL | |
| 43 | PB7 | I/O | I2C1_SDA | |
| 44 | воото | Boot | - _ = | |
| 47 | VSS | Power | | |
| 48 | VDD | Power | | |

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



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5. Software Project

5.1. Project Settings

| Name | Value | |
|-----------------------------------|---------------------------------------|--|
| Project Name | sdcard | |
| Project Folder | /home/sungjune/Projects/public/sdcard | |
| Toolchain / IDE | Makefile | |
| Firmware Package Name and Version | STM32Cube FW_L0 V1.11.0 | |

5.2. Code Generation Settings

| Name | Value |
|---|---------------------------------------|
| STM32Cube Firmware Library Package | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files | No |
| Backup previously generated files when re-generating | No |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power | No |
| consumption) | |

6. Power Consumption Calculator report

6.1. Microcontroller Selection

| Series | STM32L0 |
|-----------|---------------|
| Line | STM32L0x2 |
| мси | STM32L072CBTx |
| Datasheet | 027100_Rev3 |

6.2. Parameter Selection

| Temperature | 25 |
|-------------|-----|
| Vdd | 3.0 |

7. IPs and Middleware Configuration 7.1. DAC

mode: OUT1 Configuration 7.1.1. Parameter Settings:

DAC Out1 Settings:

Output Buffer Enable

Trigger Out event *

Wave generation mode Disabled

7.2. I2C1

12C: 12C

7.2.1. Parameter Settings:

Timing configuration:

I2C Speed Mode Standard Mode

I2C Speed Frequency (KHz)100Rise Time (ns)0Fall Time (ns)0Coefficient of Digital Filter0

Analog Filter Enabled
Timing 0x00000708

Slave Features:

Clock No Stretch Mode Disabled
General Call Address Detection Disabled
Primary Address Length selection 7-bit
Dual Address Acknowledged Disabled
Primary slave address 0

7.3. I2S2

Mode: Half-Duplex Master 7.3.1. Parameter Settings:

Generic Parameters:

Transmission Mode Mode Master Transmit

Communication Standard I2S Philips

Data and Frame Format 16 Bits Data on 16 Bits Frame

Selected Audio Frequency 8 KHz

Real Audio Frequency 8.0 KHz *

Error between Selected and Real 0.0 % *

Clock Parameters:

Clock Polarity Low

7.4. SPI1

Mode: Full-Duplex Master 7.4.1. Parameter Settings:

Basic Parameters:

Frame Format Motorola

Data Size 8 Bits

First Bit MSB First

Clock Parameters:

Prescaler (for Baud Rate) 2

Baud Rate 16.0 MBits/s *

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

Advanced Parameters:

CRC Calculation Disabled
NSS Signal Type Software

7.5. SYS

mode: Debug Serial Wire Timebase Source: SysTick

7.6. TIM6

mode: Activated

7.6.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value) 0
Counter Mode Up

Counter Period (AutoReload Register - 16 bits value) 1999 *
auto-reload preload Disable

luio-reioau preioau Disc

Trigger Output (TRGO) Parameters:

Trigger Event Selection Update Event *

7.7. **USART1**

Mode: Asynchronous

7.7.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples
Single Sample Disable

Advanced Features:

Auto Baudrate Disable TX Pin Active Level Inversion Disable RX Pin Active Level Inversion Disable **Data Inversion** Disable TX and RX Pins Swapping Disable Enable Overrun DMA on RX Error Enable MSB First Disable

7.8. USB

mode: Device (FS)

7.8.1. Parameter Settings:

Basic Parameters:

Speed Full Speed 12MBit/s

Endpoint 0 Max Packet size 64 Bytes
Physical interface Internal Phy

Power Parameters:

Low Power Disabled
Link Power Management Disabled

7.9. FATFS

mode: User-defined 7.9.1. Set Defines:

Version:

FATFS version R0.12c

Function Parameters:

FS_READONLY (Read-only mode) Disabled
FS_MINIMIZE (Minimization level) Disabled

USE_STRFUNC (String functions) Enabled with LF -> CRLF conversion

USE_FIND (Find functions) Enabled *

USE_MKFS (Make filesystem function)

USE_FASTSEEK (Fast seek function)

USE_EXPAND (Use f_expand function)

USE_CHMOD (Change attributes function)

USE_LABEL (Volume label functions)

Disabled

USE_FORWARD (Forward function)

Disabled

Locale and Namespace Parameters:

CODE_PAGE (Code page on target)

USE_LFN (Use Long Filename)

Disabled

MAX_LFN (Max Long Filename)

255

LFN_UNICODE (Enable Unicode)

STRF_ENCODE (Character encoding)

UTF-8

FS_RPATH (Relative Path)

Disabled

Physical Drive Parameters:

VOLUMES (Logical drives) 1

MAX_SS (Maximum Sector Size) 512

MIN_SS (Minimum Sector Size) 512

MULTI_PARTITION (Volume partitions feature) Disabled

USE_TRIM (Erase feature) Disabled

FS_NOFSINFO (Force full FAT scan) 0

System Parameters:

FS_TINY (Tiny mode) Disabled
FS_EXFAT (Support of exFAT file system) Disabled

FS_NORTC (Timestamp feature) Dynamic timestamp

NORTC_YEAR (Year for timestamp) 2015

NORTC_MON (Month for timestamp) 6
NORTC_MDAY (Day for timestamp) 4

FS_REENTRANT (Re-Entrancy) Disabled FS_TIMEOUT (Timeout ticks) 1000

SYNC_t (O/S sync object) osSemaphoreId

FS_LOCK (Number of files opened simultaneously) 2

7.10. USB_DEVICE

Class For FS IP: Mass Storage Class

7.10.1. Parameter Settings:

Basic Parameters:

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces)

1
USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration)

1
USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors)

512
USBD_SUPPORT_USER_STRING (Enable user string descriptor)

Disabled
USBD_SELF_POWERED (Enabled self power)

Enabled

USBD_DEBUG_LEVEL (USBD Debug Level) 0: No debug message

Class Parameters:

MSC_MEDIA_PACKET (Media I/O buffer Size) 512

7.10.2. Device Descriptor:

Device Descriptor:

VID (Vendor IDentifier) 1155

LANGID_STRING (Language Identifier) English(United States)

MANUFACTURER_STRING (Manufacturer Identifier) STMicroelectronics

Device Descriptor FS:

PID (Product IDentifier) 22314

PRODUCT_STRING (Product Identifier)

STM32 Mass Storage
SERIALNUMBER_STRING (Serial number)

CONFIGURATION_STRING (Configuration Identifier)

MSC Config

INTERFACE_STRING (Interface Identifier)

MSC Interface

* User modified value

8. System Configuration

8.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull | Max | User Label |
|--------|------------------------|-------------|-------------------------------|-----------------------------|-------------|------------|
| | | | | down | Speed | |
| DAC | PA4 | DAC_OUT1 | Analog mode | No pull-up and no pull-down | n/a | |
| I2C1 | PB6 | I2C1_SCL | Alternate Function Open Drain | Pull-up | Very High * | |
| | PB7 | I2C1_SDA | Alternate Function Open Drain | Pull-up | Very High | |
| 12S2 | PB12 | 12S2_WS | Alternate Function Push Pull | No pull-up and no pull-down | Low | |
| | PB13 | 12S2_CK | Alternate Function Push Pull | No pull-up and no pull-down | Low | |
| | PB15 | I2S2_SD | Alternate Function Push Pull | No pull-up and no pull-down | Low | |
| SPI1 | PA5 | SPI1_SCK | Alternate Function Push Pull | No pull-up and no pull-down | Very High | |
| | PA6 | SPI1_MISO | Alternate Function Push Pull | Pull-up * | Very High | |
| | PA7 | SPI1_MOSI | Alternate Function Push Pull | No pull-up and no pull-down | Very High | |
| SYS | PA13 | SYS_SWDIO | n/a | n/a | n/a | |
| | PA14 | SYS_SWCLK | n/a | n/a | n/a | |
| USART1 | PA9 | USART1_TX | Alternate Function Push Pull | No pull-up and no pull-down | Very High | |
| | PA10 | USART1_RX | Alternate Function Push Pull | No pull-up and no pull-down | Very High | |
| USB | PA11 | USB_DM | n/a | n/a | n/a | |
| | PA12 | USB_DP | n/a | n/a | n/a | |
| GPIO | PC13 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | LD1 |
| | PC14- | GPIO_EXTI14 | External Interrupt | Pull-up * | n/a | BTN |
| | OSC32_IN | | Mode with Falling | i un up | | |
| | | | edge trigger detection | | | |
| | PC15- OSC32_OU T | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | LD2 |
| | PA1 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | AMP_EN |
| | PB0 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | SDC_EN |
| | PB1 | GPIO_EXTI1 | External Interrupt | Pull-up * | n/a | SDC_EX |
| | | | Mode with | | | |
| | | | Rising/Falling edge | | | |

| sdcard Project |
|----------------------|
| Configuration Report |

8.2. DMA configuration

| DMA request | Stream | Direction | Priority |
|-------------|---------------|----------------------|----------|
| DAC_CH1 | DMA1_Channel2 | Memory To Peripheral | Low |

DAC_CH1: DMA1_Channel2 DMA request Settings:

Mode: Circular *

Peripheral Increment: Disable

Memory Increment: Enable *

Peripheral Data Width: Half Word

Memory Data Width: Half Word

8.3. NVIC configuration

| Interrupt Table | Enable | Preenmption Priority | SubPriority |
|---|--------|----------------------|-------------|
| Non maskable Interrupt | true | 0 | 0 |
| Hard fault interrupt | true | 0 | 0 |
| System service call via SWI instruction | true | 0 | 0 |
| Pendable request for system service | true | 0 | 0 |
| System tick timer | true | 0 | 0 |
| EXTI line 0 and line 1 interrupts | true | 0 | 0 |
| EXTI line 4 to 15 interrupts | true | 0 | 0 |
| DMA1 channel 2 and channel 3 interrupts | true | 0 | 0 |
| USB event interrupt / USB wake-up interrupt through EXTI line 18 | true | 0 | 0 |
| PVD interrupt through EXTI line 16 | unused | | |
| Flash and EEPROM global interrupt | unused | | |
| RCC and CRS global interrupt | unused | | |
| TIM6 global interrupt and DAC1/DAC2 underrun error interrupts | unused | | |
| I2C1 event global interrupt / I2C1 wake-up interrupt through EXTI line 23 | unused | | |
| SPI1 global interrupt | unused | | |
| SPI2 global interrupt | unused | | |
| USART1 global interrupt / USART1 wake-up interrupt through EXTI line 25 | unused | | |

* User modified value

9. Software Pack Report