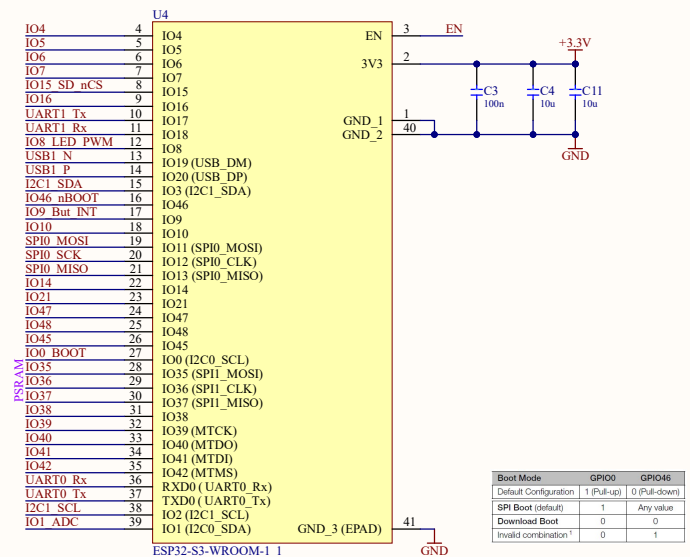


Пример схемы
https://dl.espressif.com/dl/SCH_ESP32-S3-DEVKITC-1_V1_20210312C.pdf



- Chip boot mode – GPIO0 and GPIO46
- VDD_SPI voltage – GPIO45
- ROM messages printing – GPIO46
- JTAG signal source – GPIO3

Strapping Pin	Default Configuration	Bit Value
GPIO0	Pull-up	1
GPIO3	Floating	-
GPIO45	Pull-down	0
GPIO46	Pull-down	0

EFUSE_VDD_SPI_FORCE	GPIO45	eFuse ¹	Voltage	VDD_SPI power source ²
0	0	Ignored	3.3 V	VDD3P3_RTC via R _{SP1}
	1		1.8 V	Flash Voltage Regulator
1	Ignored	0	1.8 V	Flash Voltage Regulator
		1	3.3 V	VDD3P3_RTC via R _{SP1}

eFuse 1 ^a	eFuse 2 ^b	eFuse 3 ^c	GPIO3	JTAG Signal Source
0	0	0	Ignored	USB Serial/JTAG Controller
		1	0	JTAG pins MTDI, MTCK, MTMS, and MTDO
			1	USB Serial/JTAG Controller
0	1	Ignored	Ignored	JTAG pins MTDI, MTCK, MTMS, and MTDO
1	0	Ignored	Ignored	USB Serial/JTAG Controller
1	1	Ignored	Ignored	JTAG pins MTDI, MTCK, MTMS, and MTDO