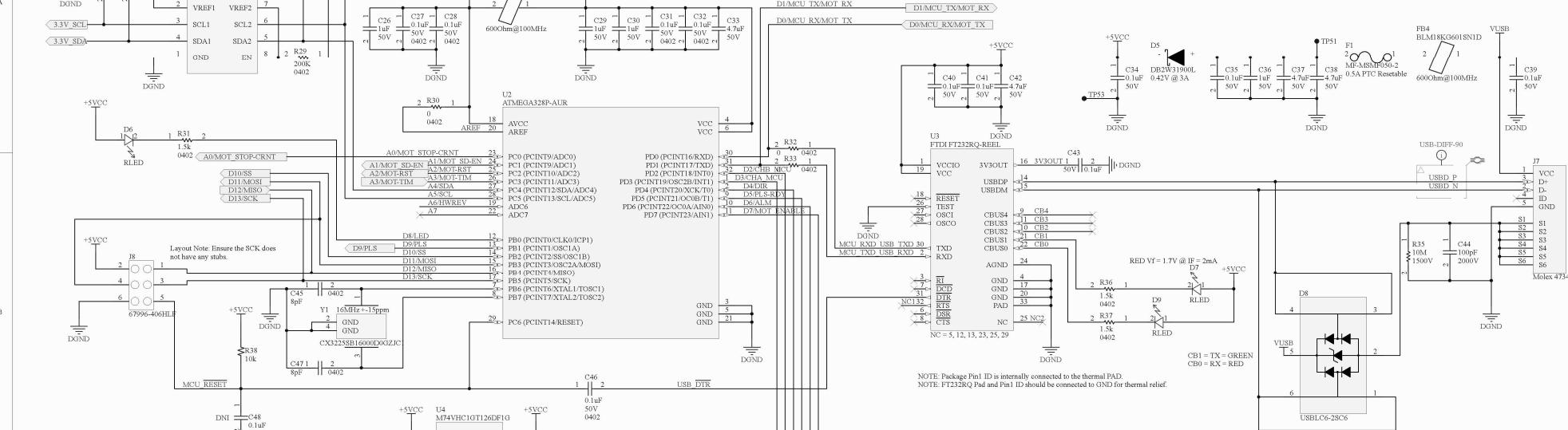


A



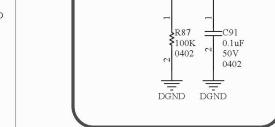
NOTE: Package Pin1 ID is internally connected to the thermal PAD.
NOTE: FT232RQ Pad and Pin1 ID should be connected to GND for thermal relief.

B



Layout Note: Ensure the SCL does not have any stubs.

C

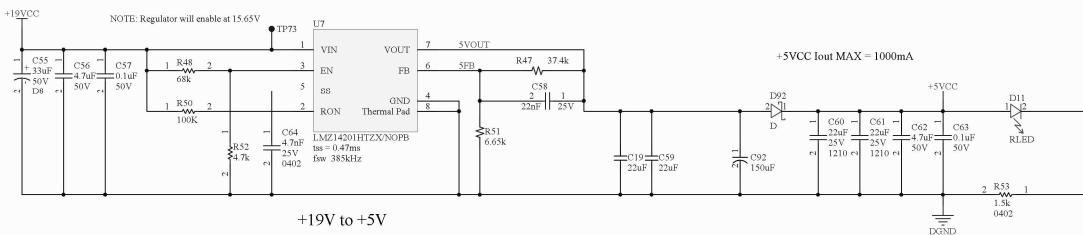


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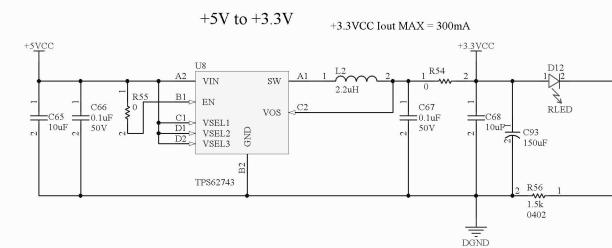
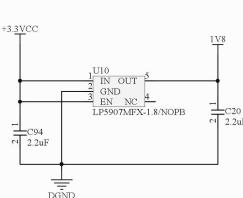
SIZE	DESIGN	REV
C		Rev

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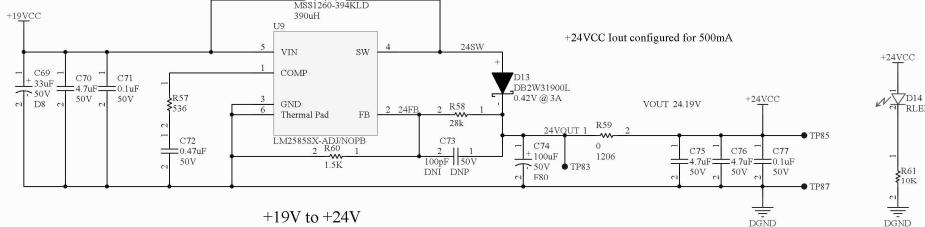
A



B



C



D

A

A

B

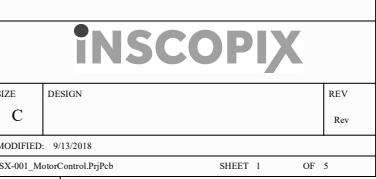
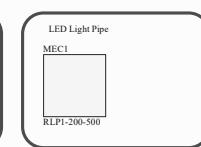
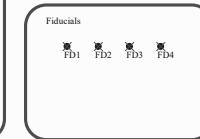
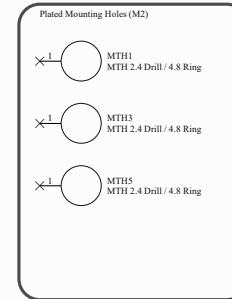
B

C

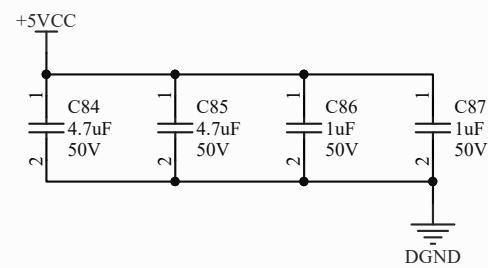
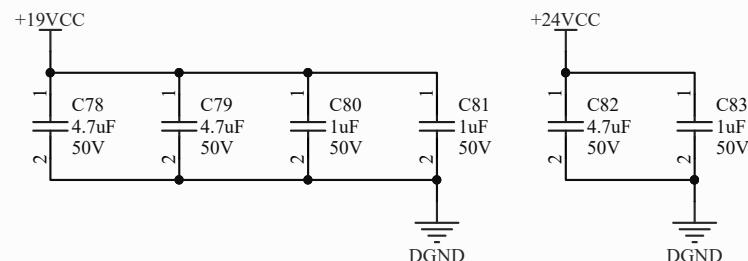
C

D

D

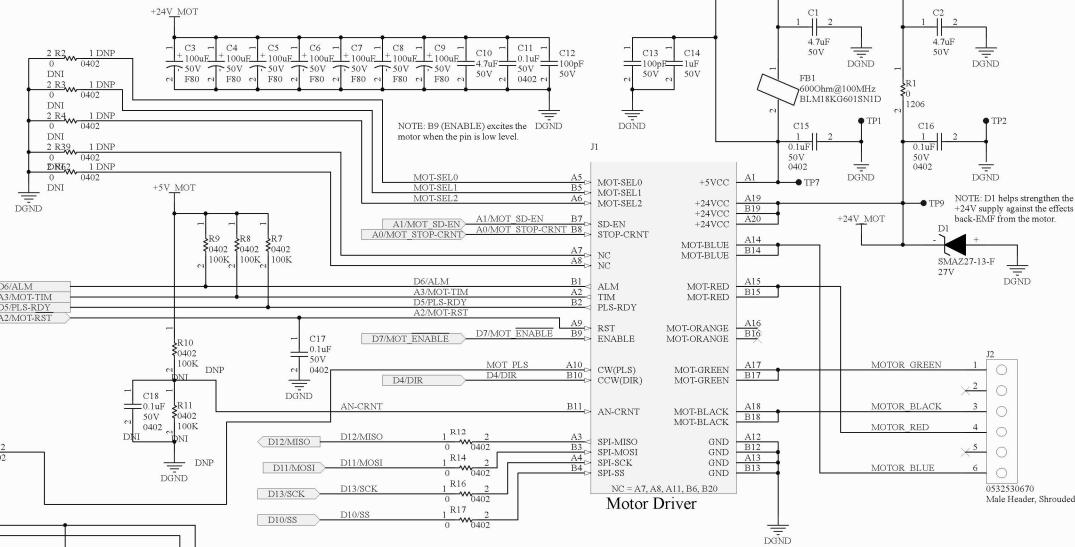


Supply Bulk Capacitors

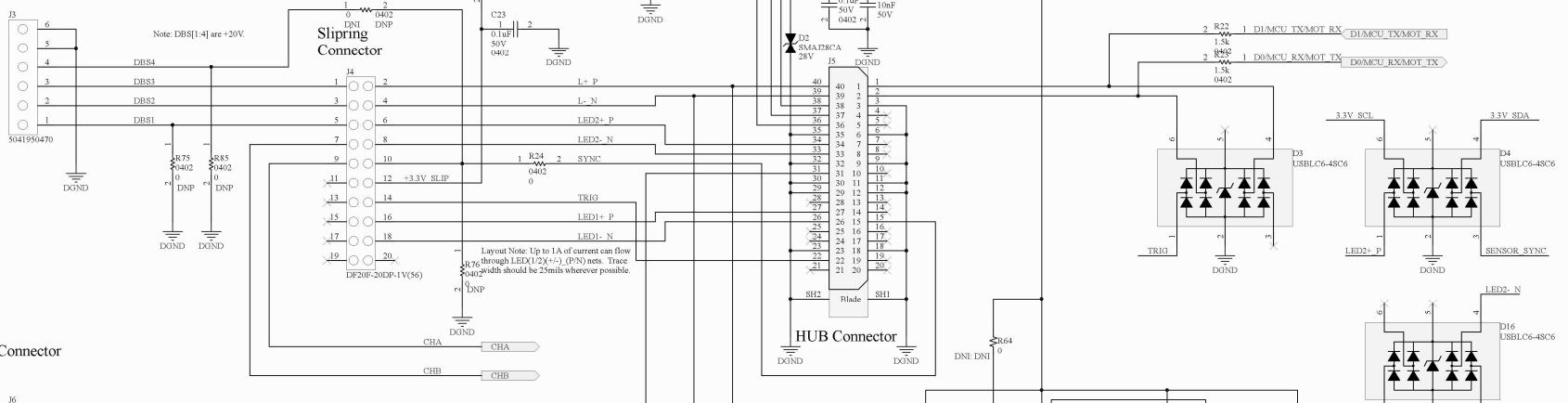


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Date: 9/13/2018	Sheet of	
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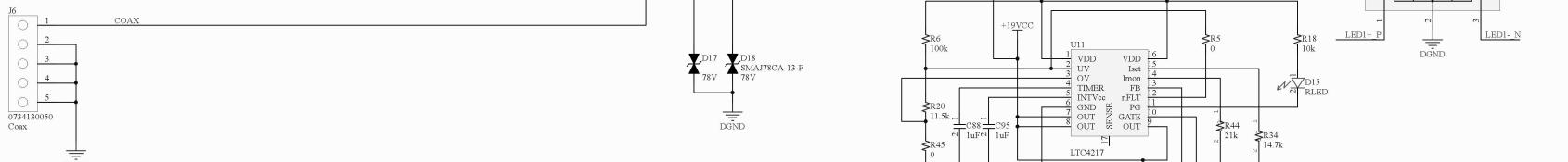
NOTE: If R71-R74 are used, the corresponding resistor R12, R14, R16, R17 should be DNP.
NOTE: Ensure R62 and R39 are DNP when using the SPI model.
NOTE: Resistors are set for SPI model.



DBS Connector



Coax Connector



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SIZE C	DESIGN	REV Rev
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