Contact

nirmal.ks@gmail.com

www.linkedin.com/in/
nirmalkshetrapalan (LinkedIn)

Top Skills

SoC

VLSI

ASIC

Nirmal Kshetrapalan

Director - ASIC Design at SiFive

India

Experience

SiFive
Director - ASIC Design
February 2019 - Present (1 year 7 months)
Bengaluru, Karnataka, India

Qualcomm
Senior Staff Manager
January 2018 - January 2019 (1 year 1 month)
Bengaluru, Karnataka, India

Arm
Senior Engineering Manager
August 2014 - January 2018 (3 years 6 months)
Bangalore

Broadcom
Manager, IC Design Engineering
February 2012 - May 2014 (2 years 4 months)
Bangalore

NetLogic Microsystems
Sr. Design Manager
2005 - February 2012 (7 years)

Texas Instruments
Lead Design Engineer
2004 - 2005 (1 year)

Alliance Semiconductor
Lead Design Engineer
1999 - 2004 (5 years)

ITI
Engineer
1998 - 1999 (1 year)

Education

Indian Institute of Technology, Delhi
Master of Technology (M.Tech.), Integrated Circuit Design · (1996 - 1997)

Cochin University of Science and Technology
Bachelor of Technology (B.Tech.), Electronics Engineering · (1991 - 1995)

maharaja's college