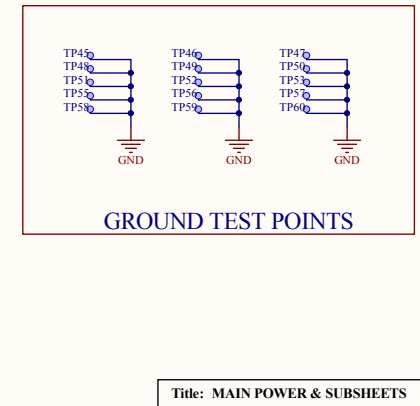
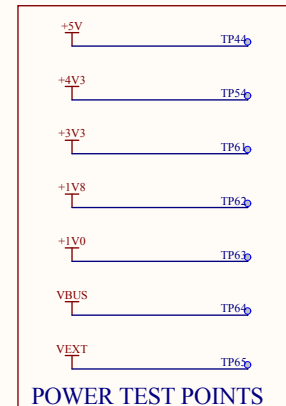
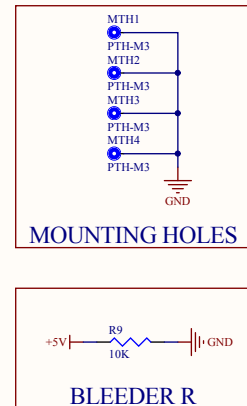
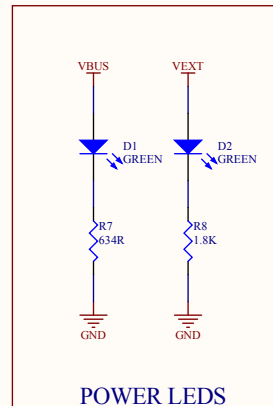
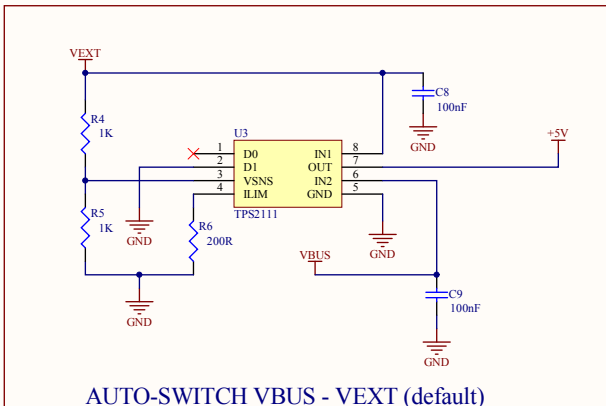
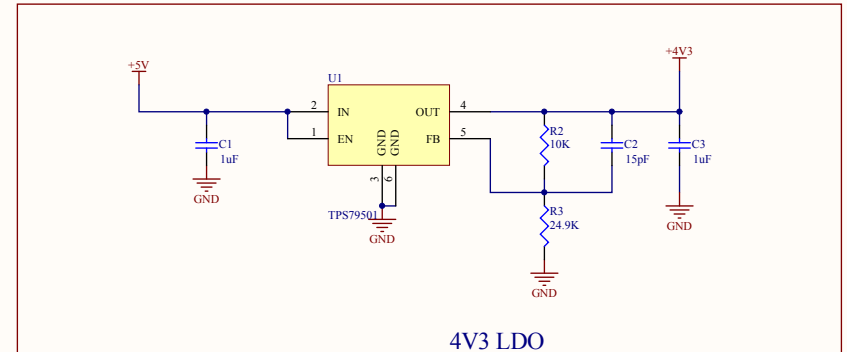
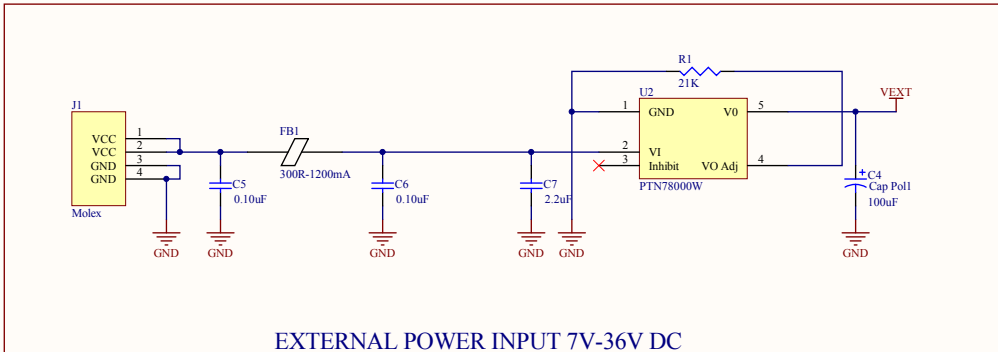


SUBSHEETS & TEST POINTS



3V3 POWER SUPPLY & VOLTAGE SUPERVISOR

1V0 POWER SUPPLY & VOLTAGE SUPERVISOR

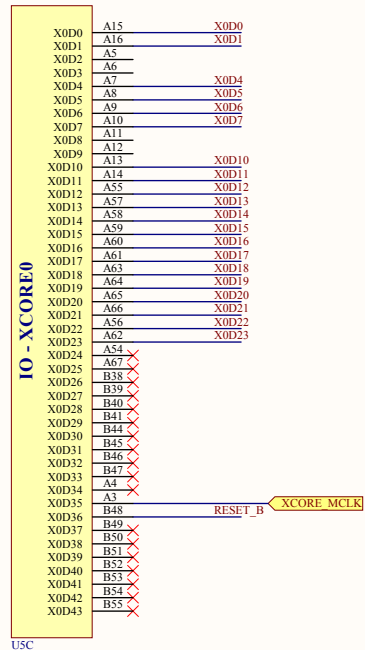
XS1-L2 SERVICE & POWERING

WARNING: Whenever 5 wire or 2 wire XSYS is used by daughter board, the main code shall be properly configured

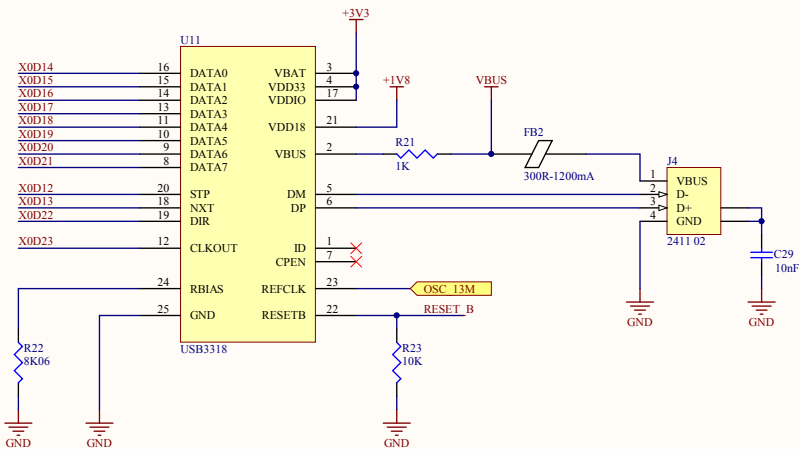
XTAG2 INPUT & XLINK OUTPUT

1V0 DECOUPLING

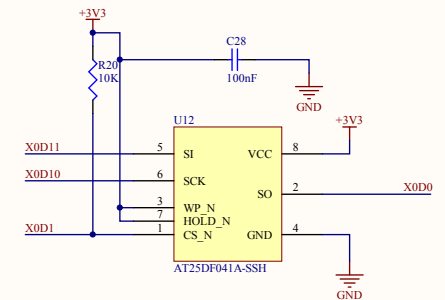
3V3 DECOUPLING



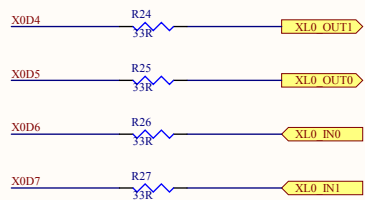
XCORE0 I/O



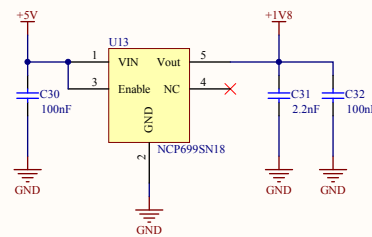
USB PHY (VBUS)



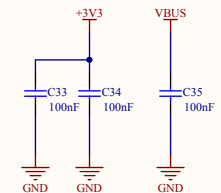
SPI BOOT FLASH



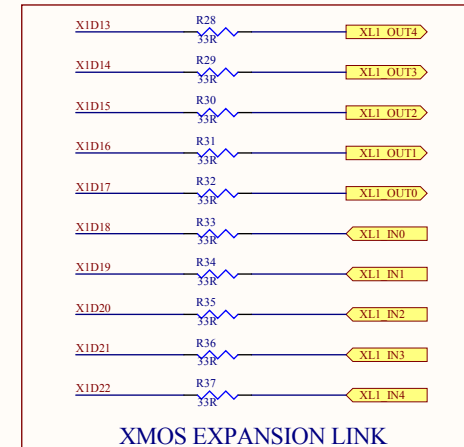
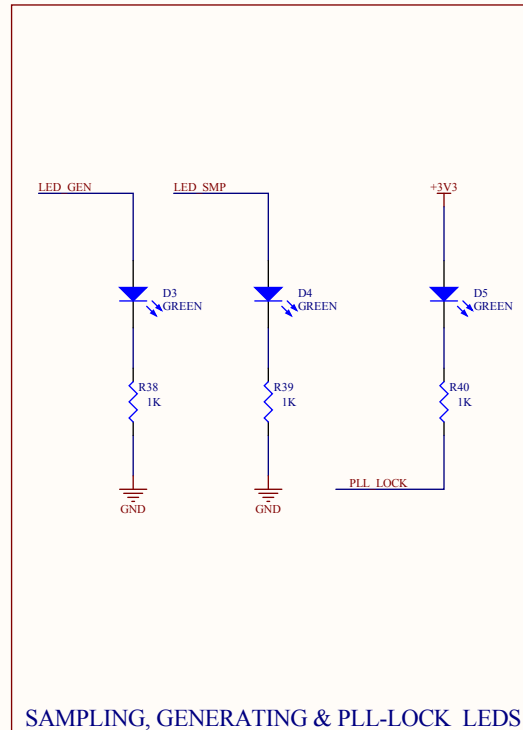
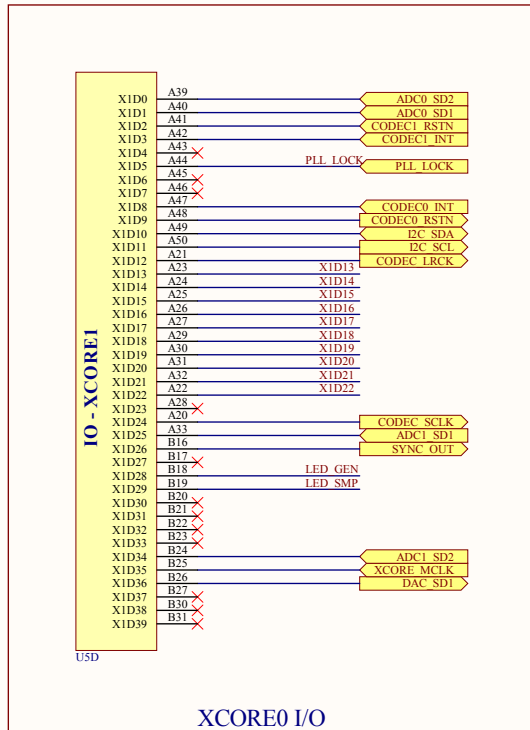
XMOS LINK IN

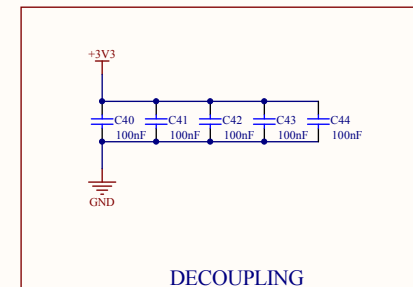
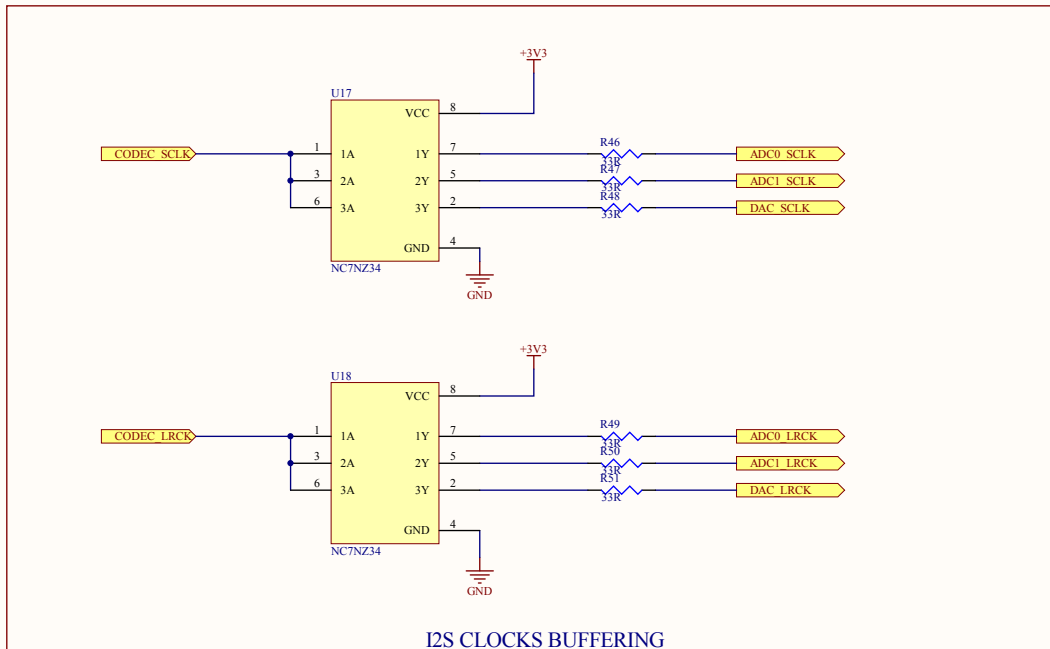
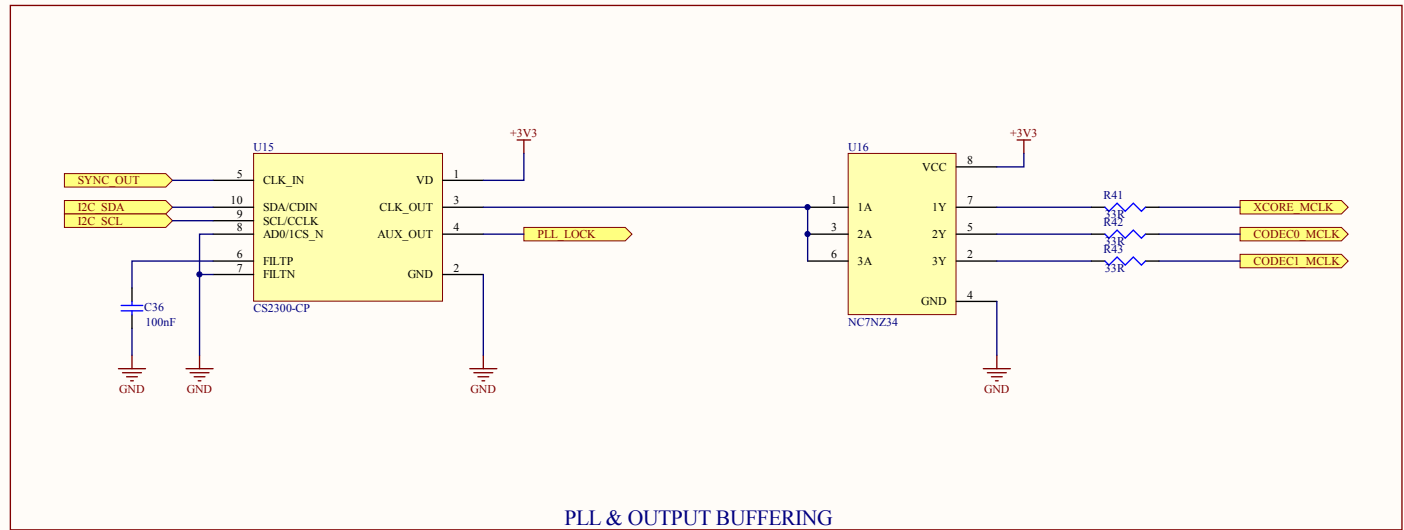
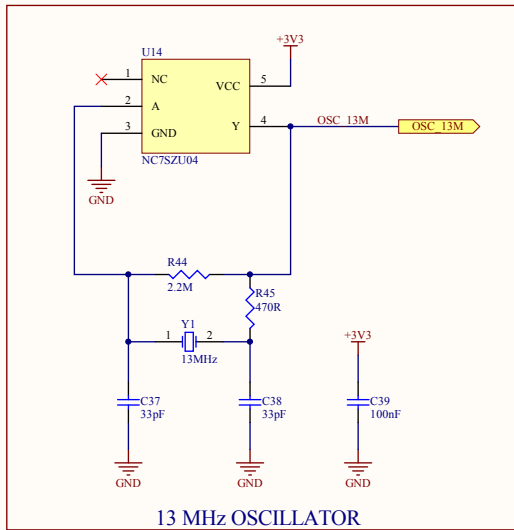


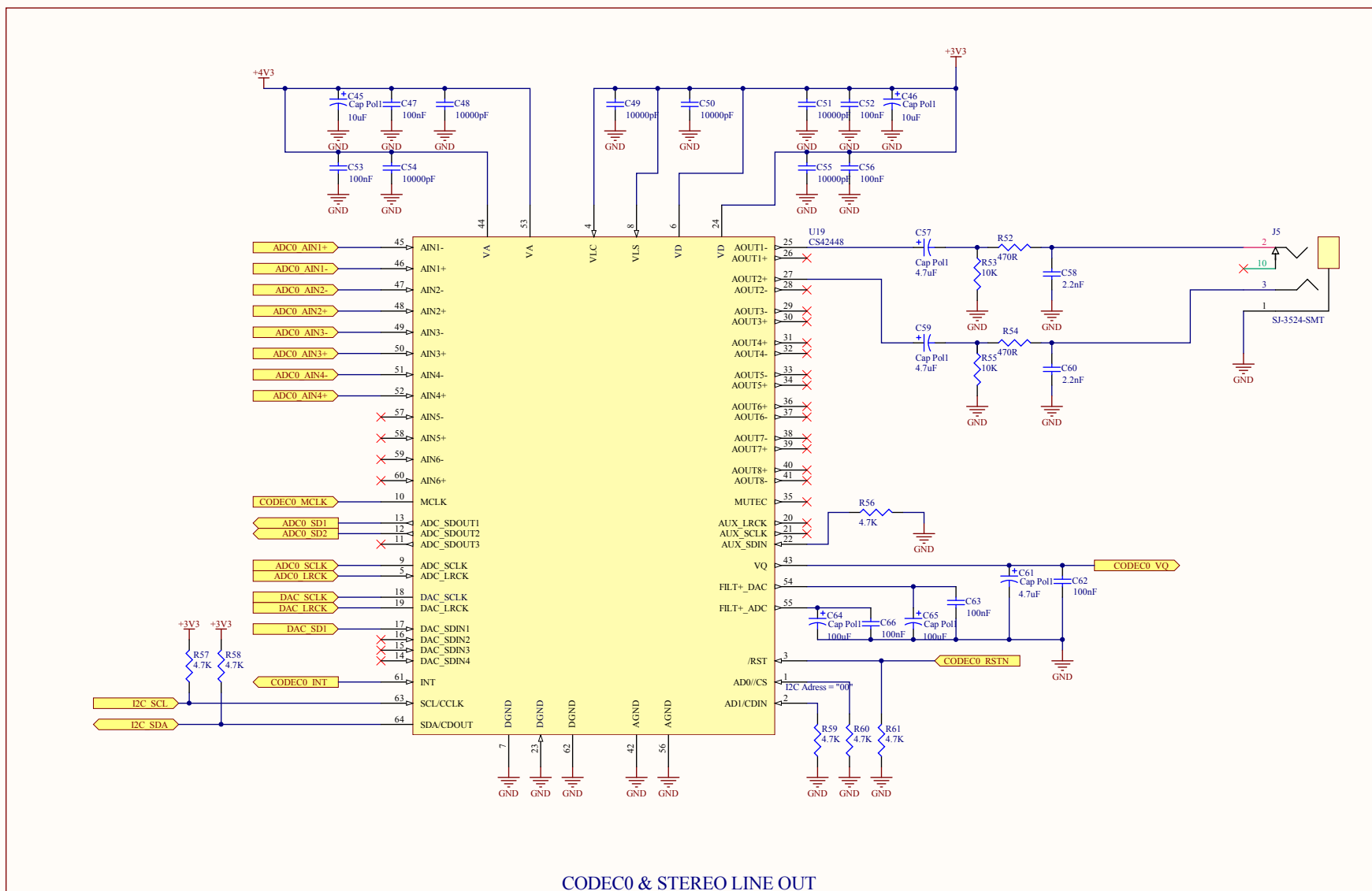
1V8 LDO POWER SUPPLY

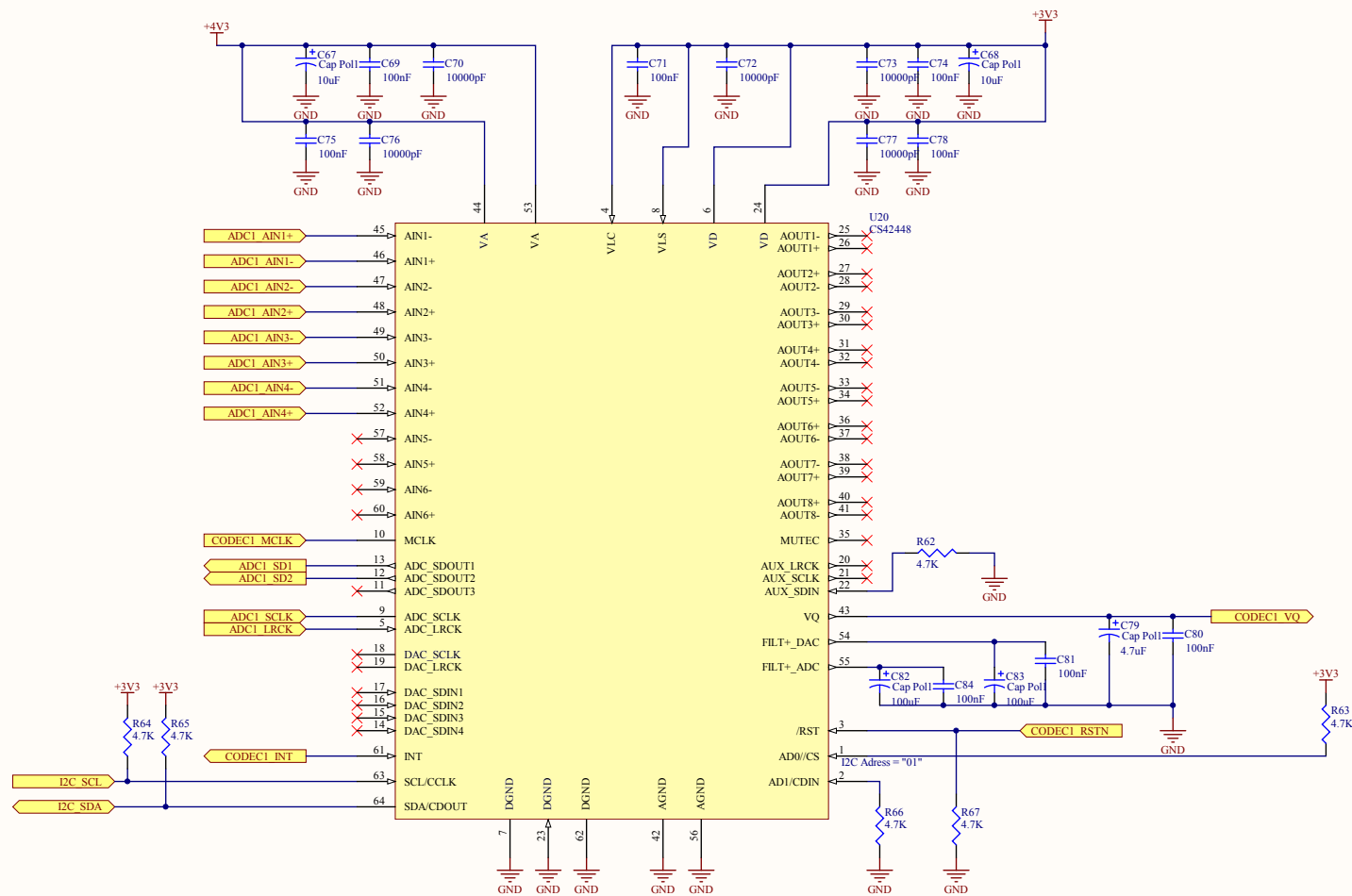


DECOUPLING

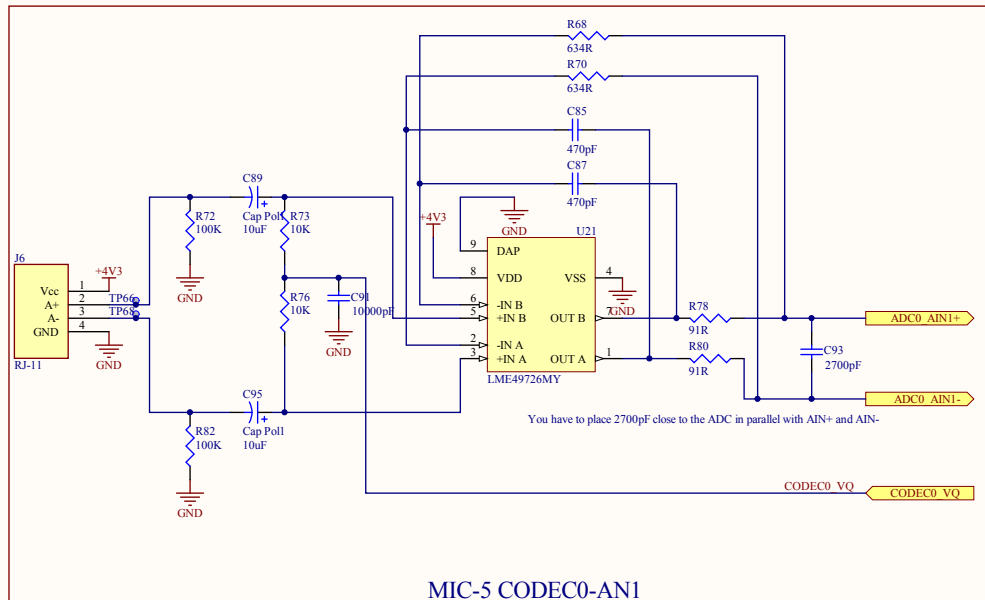




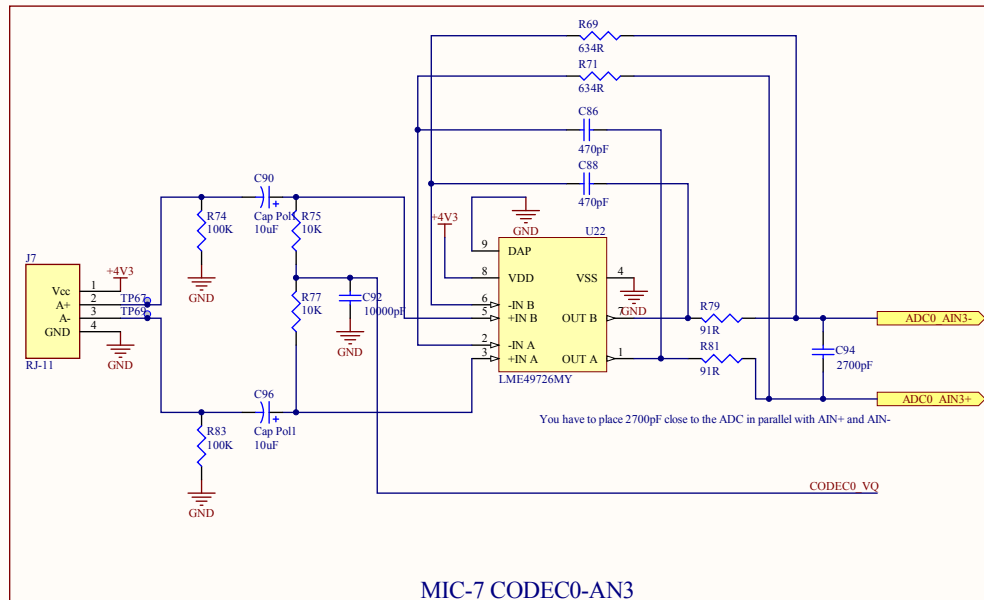




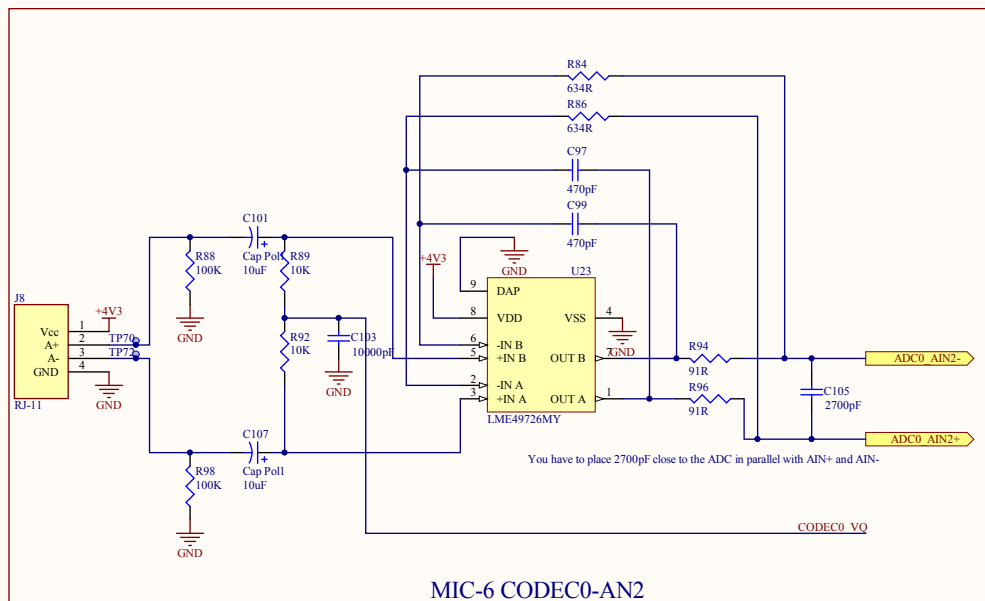
CODEC1



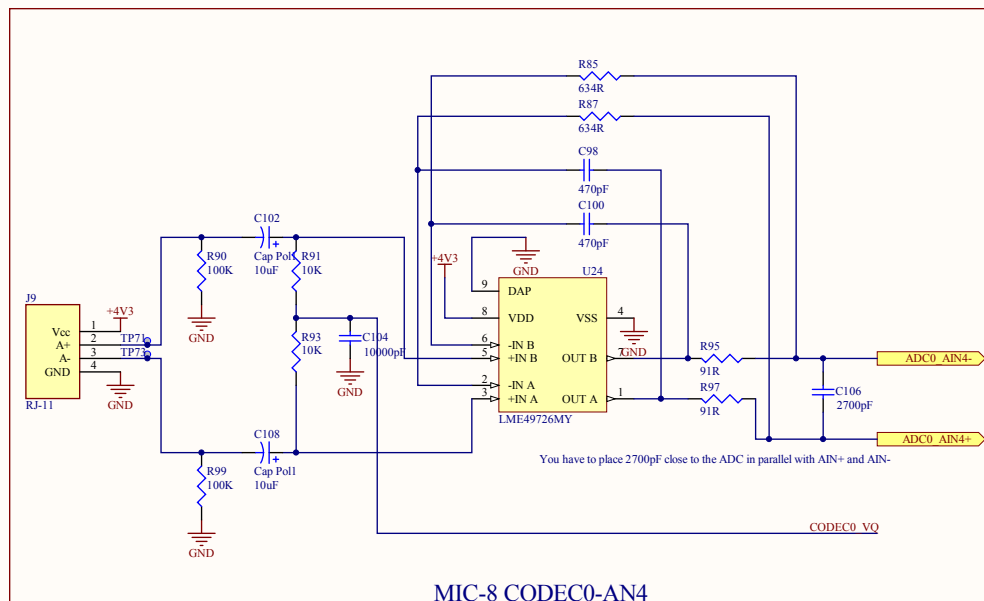
MIC-5 CODEC0-AN1



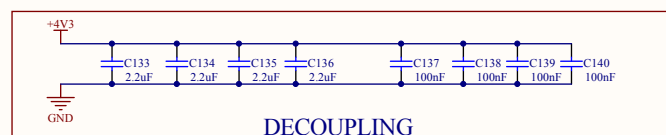
MIC-7 CODEC0-AN3

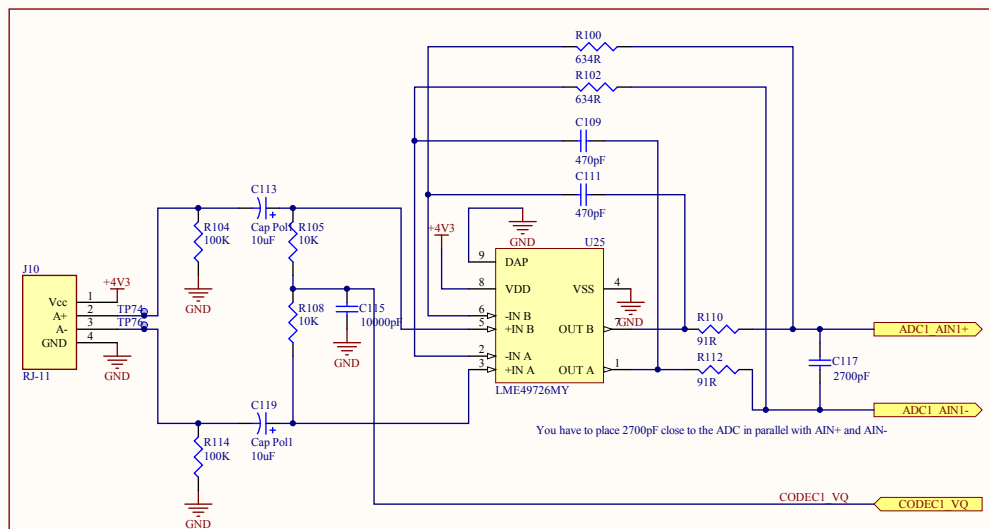


MIC-6 CODEC0-AN2

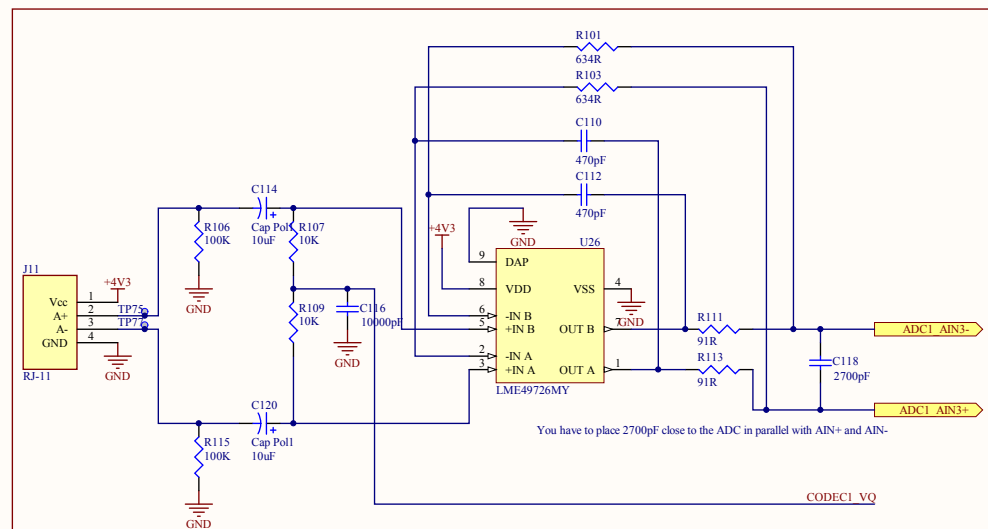


MIC-8 CODEC0-AN4

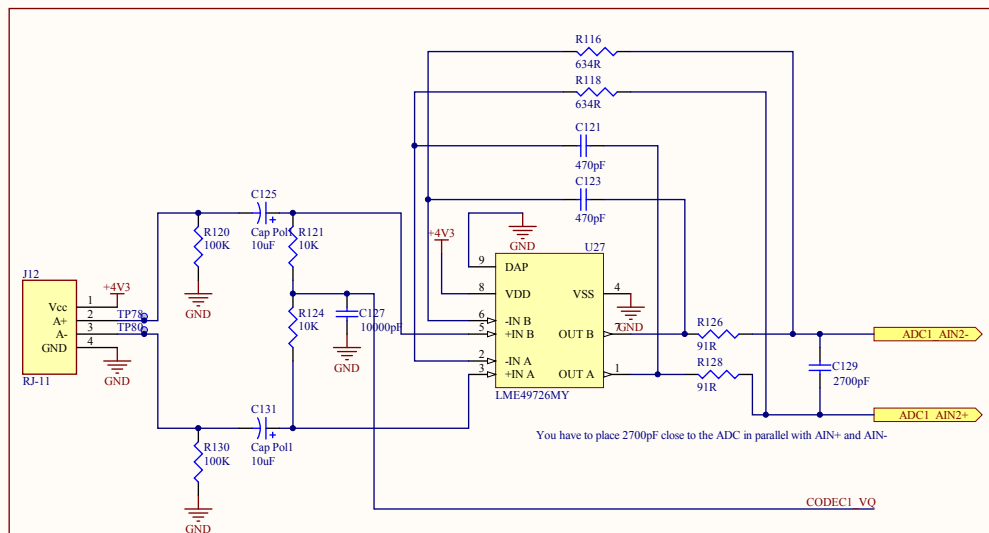




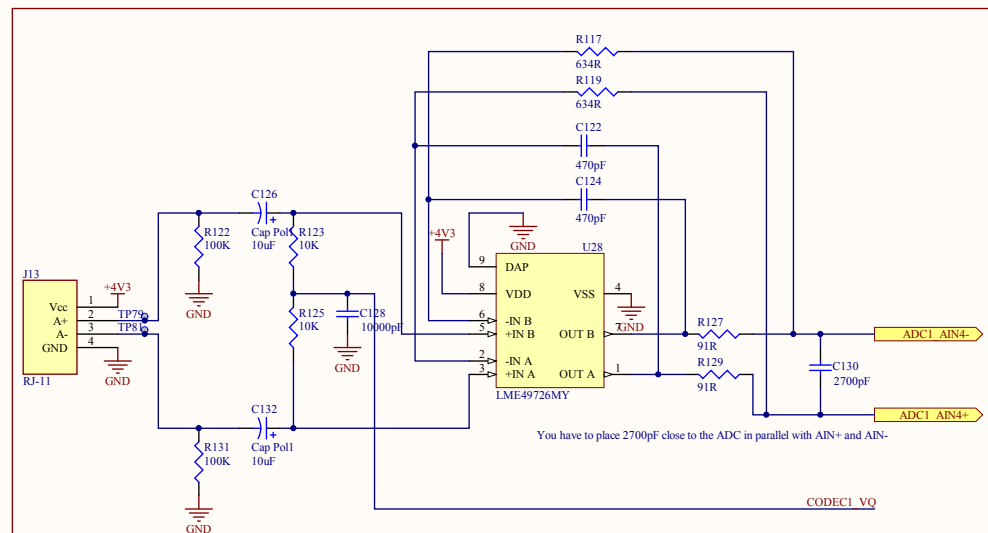
MIC-1 CODEC1-AN1



MIC-3 CODEC1-AN3



MIC-2 CODEC1-AN2



MIC-4 CODEC1-AN4

