```
1 /*********************
2
   Target MCU & clock speed: ATtiny13A @ 1.2Mhz internal
   Name : main.c
4 C modules of this project, ISR:
5 main.c
6 Custom Headers:
7
      Nothina
  Author : Insoo Kim (insoo@hotmail.com)
8
   Created : May 15, 2015
10 Updated: Sep 03, 2018 (On Atmel Studio 7)
11
12 Description:
13
      ATtiny13A controls 220V power relay for multiple purposes using three
        slide switches.
14
       In this particular application for managing submersed water pump in fish >
        tank,
15
       I don't care slide switches, but only works in a single mode of operation. >
16
17 HEX size[Byte]: 218 out of 1024
18
19 How to upload to the target MCU
20 <For Windows Atmel Studio>
21 Slect Tool ? USBtiny (USBtiny memu should be configured in the external tool
     memu)
22
23 <For CMD window or DOS prompt>
24 cd " C:₩Users₩insoo₩Documents₩GitHub₩ATmelStudio₩ATtiny13A₩ClockGen₩ISR₩Debug >
25 avrdude -c usbtiny -P usb -p attiny13 -U flash:w:ISR.hex:i
26
27
  Ref:
29
30 #include <avr/interrupt.h>
31 #include <avr/sleep.h>
32 #include <avr/io.h>
33 #include <util/delay.h>
35 //#define TEST_MODE
36 #define PRODUCTION_MODE
37
38 #ifdef TEST MODE
       #define UNIT_DELAY_WDT 4 //<=== TEST VALUE in development; 4 sec WDT
39
       #define INITPUMP_DURATION 5000 // 5 sec for initial pump period
40
41 #endif
42 #ifdef PRODUCTION_MODE
       #define UNIT_DELAY_WDT 8 //<=== SELECTED VALUE in production; WDT period →
         in seconds; 8 sec WDT
44
       #define INITPUMP_DURATION 10000 // 60 sec for initial pump period
45 #endif
46
47 //select accumulated WDT period as DELAY UNIT = UNIT DELAY WDT *
     SET_DELAY_UNIT
48 #ifdef TEST_MODE
       #define SET_DELAY_UNIT 2 //<=== TEST VALUE in development; 4*3=12 [sec]</pre>
```

```
50 #endif
51 #ifdef PRODUCTION MODE
52
       #define SET DELAY UNIT 225 //<=== SELECTED VALUE in production; 8*225=1800 →
         [sec]=30 minutes
53 #endif
54 // # of UNIT_DELAY_WDT, Max 253
55 //#define SET_DELAY_UNIT 2 // <=== TEST VALUE in development
56 //#define SET_DELAY_UNIT 15 // 2 min (15*8=120[sec]) when UNIT_DELAY_WDT is 8
57 //#define SET_DELAY_UNIT 150 // 20 min when UNIT_DELAY_WDT is 8
58 //#define SET_DELAY_UNIT 225 //<=== SELECTED VALUE in production; 30 min when >
     UNIT_DELAY_WDT is 8
59 //#define SET_DELAY_UNIT 253 // (34 min - 8 sec) when UNIT_DELAY_WDT is 8
60
   //select accumulated DELAY_UNIT as WAKEUP period = SET_DELAY_UNIT *
     WAKEUP_PERIOD
62 #ifdef TEST_MODE
63
       //4*3*1=12 [sec], every 12 sec system will wakeup from sleep and do ISR
       //#define WAKEUP_PERIOD 1 //<=== TEST VALUE in development;
64
65
       uint8_t wakeupPeriod = 1;
66 #endif
67 #ifdef PRODUCTION_MODE
       //8*225*2=3600[sec]=1 [hour], every hour system will wakeup from sleep and ➤
          do ISR
       //#define WAKEUP_PERIOD 2 //<=== SELECTED VALUE in production;
69
70
                                   // 2:one hour, 4:two hours, etc
71
                                   // when SET_DELAY_UNIT is 225 of ATtiny13a at >
                       1.2Mhz
72
       uint8_t wakeupPeriod = 2;
73 #endif
74
75 //select WAKEUP duration, i.e. how long ISR function will persist.
76
77 #ifdef TEST MODE
       //#define wakeupDuration 75 // 10 min operation, Refer the comments of
78
         SET_DELAY_UNIT
       //#define wakeupDuration 1 // 4 sec operation, Refer the comments of
79
         SET_DELAY_UNIT
       uint8_t wakeupDuration = 1; // 4 sec operation, Refer the comments of
80
         SET_DELAY_UNIT
81 #endif
82 #ifdef PRODUCTION_MODE
83
       //#define wakeupDuration 75 // 10 min operation, Refer the comments of
         SET DELAY UNIT
       //#define wakeupDuration 150 // 20 min operation, Refer the comments of
84
         SET_DELAY_UNIT
85
       uint8_t wakeupDuration = 150; // 4 sec operation, Refer the comments of
         SET_DELAY_UNIT
86 #endif
87
88 //port for the transistor to control relay on/off
89 #define NPN_TR_PORT PBO // when using NPN TR
90
91 uint8 t WDTtick;
92 uint8 t WDTtick30min;
93 //uint8_t readBit;
94 ISR(WDT_vect)
```

```
95 {
 96
        //readBit = PINB & PB3 ;
 97
        //if (readBit == 1)
 98
        //if(bit_it_set(PINB, PB3))
 99
        if (PINB & (1<<PB3))
100
            //wakeupDuration = 2;
            wakeupPeriod = 1; // every 30min in production mode
101
102
        else
103
            //wakeupDuration = 1;
104
            wakeupPeriod = 2; // every 60min in production mode
105
        // ----- INCREASE WDT TICK COUNT -----
106
107
        // increase WDTtick every UNIT_DELAY_WDT sec
108
        ++WDTtick;
109
        // On every SET_DELAY_UNIT (half-hour) except last half-hour
110
111
        // reset WDTtick count and increase half-hour count
        if ((WDTtick >= SET_DELAY_UNIT) && (WDTtick30min < wakeupPeriod))</pre>
112
113
114
            // Reset WDT counter value of the designated address in the EEPROM of >
            //eeprom_update_byte((uint8_t*)WDTTICK_CTR_ADDR, 0);
115
            WDTtick = 0;
116
117
118
            //Increase WDT Half-hour counter value of the designated address in
              the EEPROM of ATtiny13A
            //eeprom_update_byte((uint8_t*)WDTTICK_30MIN_ADDR, ++WDTtick30min);
119
120
            ++WDTtick30min;
        }//if (WDTtick >= SET_DELAY_UNIT) && (WDTtick30min < WAKEUP_PERIOD)</pre>
121
122
123
        // On every wakeupPeriod (i.e. one hour in production mode)
124
        if (WDTtick30min >= wakeupPeriod)
125
        {
            // ----- DO TO PROPER ACTION TO WDT TICK COUNT
126
              _____
            // When the accumulated WDT reaches every SET_DELAY_UNIT, turn on
127
              FSP-01
            if (WDTtick == 0)
128
129
130
                // Give logic HIGH to port 4 to turn ON NPN transistor(2n2222),
                // so let the GND of ESP-01 module CONNECT to system GND.
131
132
                // This will power ON ESP-01 and measure temperature & humidity
                  via DHT22
                PORTB = 1<<NPN_TR_PORT; //turn on GND of MOSFET or ESP-01
133
134
            }//if (WDTtick == 0)
135
            else if (WDTtick >= wakeupDuration)
136
                // Give logic LOW to port 4 to turn OFF NPN transistor(2n2222),
137
138
                // so let the GND of ESP-01 module DISCONNECT to system GND.
139
                // This will power OFF ESP-01 and don't measure temperature &
                  humidity via DHT22
                PORTB = (0<<NPN TR PORT); //turn off GND of MOSFET or ESP-01
140
141
142
                // Reset WDT counter value of the designated address in the EEPROM >
                   of ATtiny13A
                //eeprom_update_byte((uint8_t*)WDTTICK_CTR_ADDR, 0);
143
```

```
144
                 WDTtick = 0;
145
                 //Reset WDT Half-hour counter value of the designated address in
                   the EEPROM of ATtinv13A
146
                 //eeprom_update_byte((uint8_t*)WDTTICK_30MIN_ADDR, 0);
147
                 WDTtick30min=0;
148
             }//else if (WDTtick == 1)
         }//if (WDTtick30min >= wakeupPeriod)
149
150
151
152 }//ISR(WDT_vect)
153
154 int main(void) {
155
156
         WDTtick=0;
157
         WDTtick30min=0;
158
         // Set NPN_TR_PORT mode to output
159
         DDRB = (1 << NPN_TR_PORT);
160
         // Set PB1-3 mode to input
         DDRB &= \sim(1<<PB1);
161
162
         DDRB &= \sim(1<<PB2);
163
         DDRB &= \sim(1<<PB3);
164
         PORTB = 1<<NPN_TR_PORT; //turn on MOSFET
165
166
167
         _de/ay_ms(INITPUMP_DURATION); // initial measurement of DHT22 via ESP-01
           for 10 sec
168
         PORTB = 0<<NPN_TR_PORT; //turn off MOSFET
169
170
171
         // temporarily prescale timer to UNIT_DELAY_WDT seconds so we can measure >
           current
172
         switch (UNIT_DELAY_WDT)
173
         {
             case 4:
174
175
                 WDTCR |= (1<<WDP3); // 4s
176
                 break;
177
             case 8:
                 WDTCR |= (1<<WDP3) | (1<<WDP0); // 8s
178
179
                 break;
180
             default:
                 WDTCR |= (1<<WDP3) | (1<<WDP0); // 8s
181
         }
182
183
184
         // Enable watchdog timer interrupts
185
         WDTCR |= (1<<WDTIE);</pre>
186
         sei(); // Enable global interrupts
187
188
189
         // Use the Power Down sleep mode
190
         set_sleep_mode(SLEEP_MODE_PWR_DOWN);
191
         //set_sleep_mode(SLEEP_MODE_IDLE);
192
         for (;;) {
193
194
             sleep mode();
                             // go to sleep and wait for interrupt...
195
196 }//main
```

198