



UI_LED

LED_DATA_IN

LED_DATA_OUT

File: UI_LED.kicad_sch

MCU

LED_DATA_OUT

File: MCU.kicad_sch

Sheet5D7C8BFD

ANA_0

ANA_1

ANA_4

ANA_5

ANA_8

ANA_9

ANA_12

ANA_13

IN_0

IN_1

IN_2

IN_3

IN_4

IN_5

IN_6

IN_7

OUTD

ADDRESS_A

ADDRESS_B

ADDRESS_C

ESP_GPIO_1

ESP_GPIO_18

ESP_GPIO_37

ESP_GPIO_36

ESP_GPIO_35

File: UI_MUX.kicad_sch

sheet5D8763D6

ANA_2

ANA_3

ANA_6

ANA_7

ANA_10

ANA_11

ANA_14

ANA_15

IN_0

IN_1

IN_2

IN_3

IN_4

IN_5

IN_6

IN_7

OUTD

ADDRESS_A

ADDRESS_B

ADDRESS_C

ESP_GPIO_2

ESP_GPIO_5

ESP_GPIO_4

ESP_GPIO_3

File: UI_MUX.kicad_sch

HWCFG

HWCFG_SHIFT

HWCFG_CLOCK

HWCFG_DATA

File: HWCFG.kicad_sch

MCU

HWCFG_SHIFT

HWCFG_CLOCK

HWCFG_DATA

File: MCU.kicad_sch

Common Sheets:

500 GRID

600 USB_POWER

700 MCU

Module Specific:

800 HWCFG

900 LED

1000 UI

Sheet: /

File: PCBA-PBF4.kicad_sch

Title:

Size: A3

KiCad E.D.A. 8.0.6

Date:

Rev:

Id: 1/10

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Simulation:
<http://tinyurl.com/y229mt4>



Sheet: /UI_BUTTON/ File: UI_BUTTON.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.6	Id: 3/10	

900



Sheet: /UI_LED/
File: UI_LED.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.6

Rev:

Id: 4/10

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Sheet: /Sheet5D7C8BFD/ File: UI_MUX.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.6	Id: 5/10	

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Sheet: /sheet5D8763D6/ File: UI_MUX.kicad_sch		
Title:		
Size: A4	Date:	Rev:
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6

600

ESD Diodes

ESD protection for all of the externally accessible nets.



+3V3 LDO Regulators

Regulators for generating independent power rails for the microcontroller and the user interface.



Sheet: /MCU/Sheet60F06FE1/
File: USB_POWER.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.6

Rev:

Id: 9/10

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The schematic diagram illustrates a board identification circuit using a 74HC165 shift register (U801). The circuit is powered by +3V3 and grounded (GND).

Connections:

- HWCFG_LOW connects to pins 10 (SI), 11 (D0), 12 (D1), 13 (D2), 14 (D3), 3 (D4), 4 (D5), 5 (D6), and 6 (D7).
- HWCFG_HIGH connects to pin 9 (Q).
- HWCFG_CLOCKD connects to pin 2 (CLK).
- HWCFG_SHIFTD connects to pin 1 (SH/LD).
- +3V3 connects to pin 16 (VCC).
- GND connects to pins 8 and 15.

Board Identification

Grid firmware can identify the hardware and the board revision through a 3-wire serial interface using one or more shift registers as read-only memory. The content of the memory is defined by pulling the inputs high or low through PCB traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Model Codes (D3–D0):

Po16 0000
Bo16 0001
PBF4 0010
EN16 0011
...

Revision Codes (D7–D4):

RevA 0000
RevB 0001
RevC 0010
RevD 0011
...

Sheet: /HWCFG/ File: HWCFG.kicad_sch		
Title:		
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KiCad E.D.A. 8.0.6		Id: 10/10

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Po16	0000
Bo16	0001
PBF4	0010
EN16	0011
...	

```
RevA 0000
RevB 0001
RevC 0010
RevD 0011
...
```

Sheet: /HWCFG/ File: HWCFG.kicad_sch	
Title:	
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KiCad E.D.A. 8.0.6	Id: 10/10