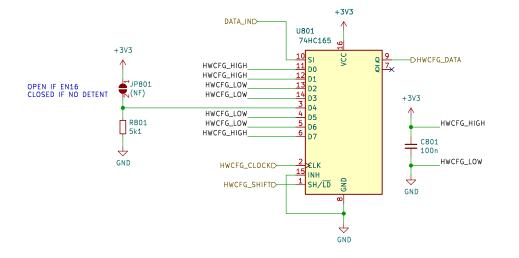


600 ESD Diodes ESD protection for all of the externally accessible nets.  $\Diamond$  TP601 ♦ TP602 ♦ TP603 ♦ TP604 GND VB VBUS U601 C5451661 VBUS → USB\_DATA\_P -
USB\_DATA\_P USB\_DATA\_N♦ ->USB\_DATA\_N GND GND +3V3 LDO Regulators Regulators for generating independent power rails for the microcontroller and the user interface. PWR\_FLAG +3V3 PWR\_FLAG VBUS VBUS +5V U602 LN1134A332MR-G GZ2012D601TF L601 J601 TYPE-C-32-M-12 OUT GND C601 VBUS TP605 A4B9 VBUS GND CC1 GND GND ->USB\_DATA\_N -◇USB\_DATA\_P **₩**GND GND SBU1 5k1 5k1 PWR\_FLAG B8 X SBU2 R601 R602 GND A1B12 GND 412B1 GND + C603 R603 GND GND Sheet: /MCU/Sheet60F06FE1/ File: USB\_POWER.kicad\_sch Title: Size: A4 Date: Rev: KiCad E.D.A. kicad-cli 7.0.11+1 ld: 9/23 800



## Board Identification

Grid firmware can identify the hardware and the board revision thorugh a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

```
DO: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D7: REVISION (MSB)
```

## Model Codes (D3-D0):

Po16 0000 Bo16 0001 PBF4 0010 EN16 0011

## Revision Codes (D7-D4):

RevA 0000 RevB 0001 RevC 0010 RevD 0011

> Sheet: /HWCFG/ File: HWCFG.kicad\_sch

Title:

 Size: A4
 Date:
 Rev:

 KiCad E.D.A. kicad-cli 7.0.11+1
 Id: 10/23

