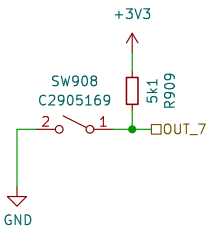
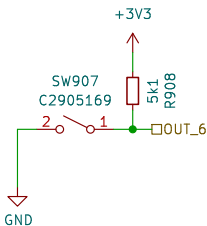
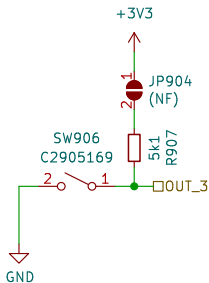
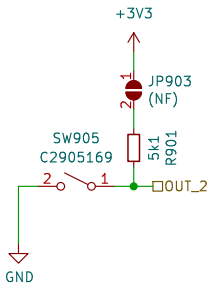
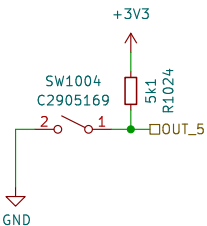
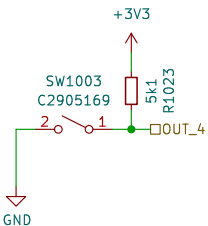
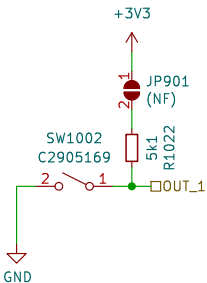
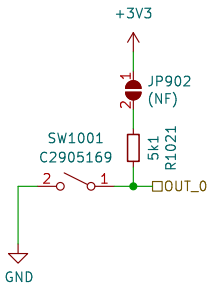


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Sheet: /UI\_POT\_BTN/  
File: UI\_POT\_BTN.kicad\_sch

Title:

Size: A4

Date:

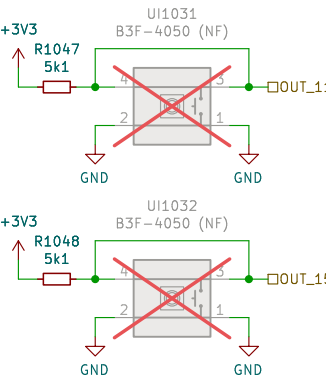
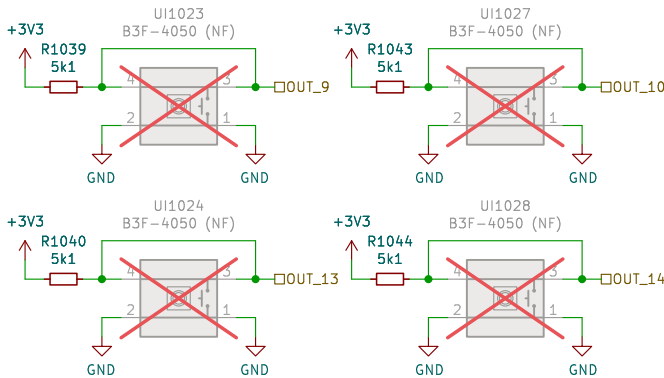
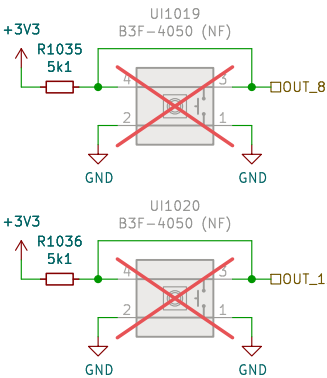
KiCad E.D.A. 8.0.3

Rev:

Id: 2/11

1000

Simulation:  
<http://tinyurl.com/y229mty4>



Sheet: /UI_BUTTON/ File: UI_BUTTON.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.3	Id: 3/11	

# 900



Sheet: /UI\_LED/  
File: UI\_LED.kicad\_sch

**Title:**

Size: A4

Date:

KiCad E.D.A. 8.0.3

**Rev:**

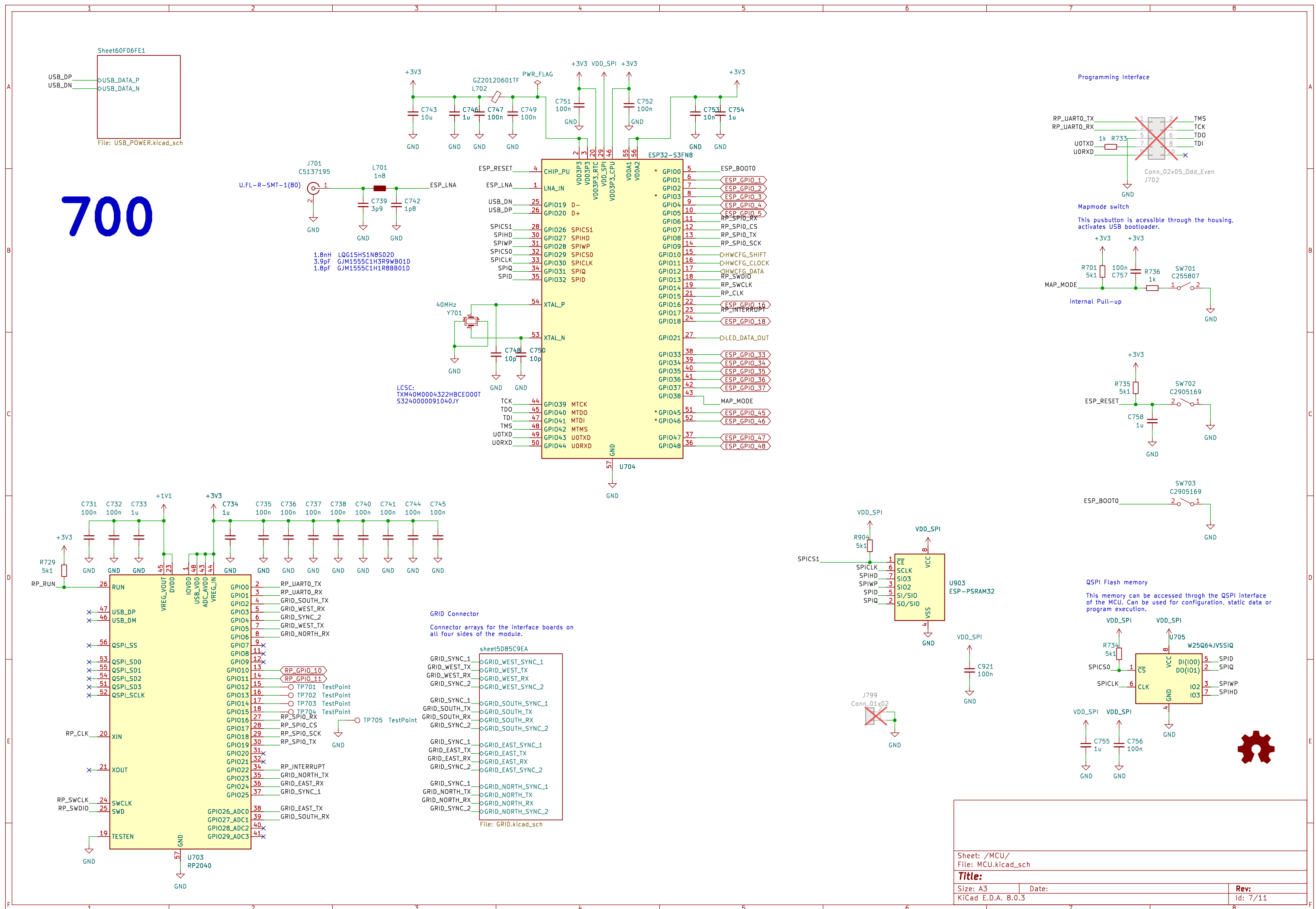
Id: 4/11

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500

GRID Connector  
Bi-Directional Data  
2x SYNC

Board Mounting Pattern



Sheet: /MCU/sheet5D85C9EA/ File: GRID.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.3		Id: 8/11



[illegible]

<b>Title:</b>		
Size: A4	Date:	<b>Rev:</b>
KiCad E.D.A. 8.0.3		Id: 9/11

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RIGHT SCREEN

LEFT SCREEN

U801 74HC165

HWCFG\_LOW

HWCFG\_HIGH

HWCFG\_CLOCKD

HWCFG\_SHIFTD

HWCFG\_DATA

HWCFG\_HIGH

HWCFG\_LOW

Model Codes (D3-D0):

Revision Codes (D7-D4):

Sheet: /HWCFG/  
File: HWCFG.kicad\_sch

Title:

Size: A4 Date: Rev:

KiCad E.D.A. 8.0.3 Id: 10/11

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

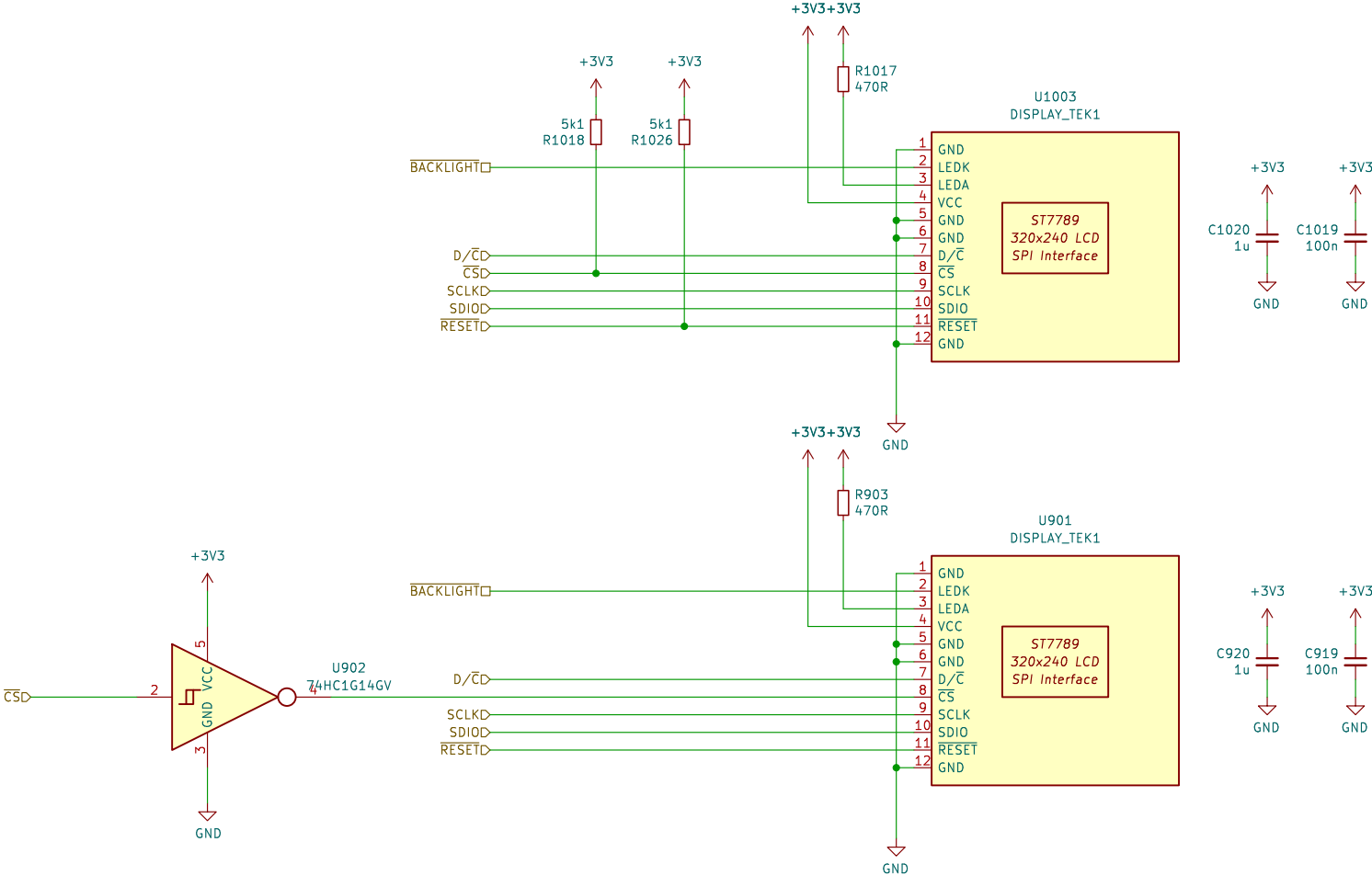
D0: MODEL (LSB)  
D1: MODEL  
D2: MODEL  
D3: MODEL (MSB)  
D4: REVISION (LSB)  
D5: REVISION  
D6: REVISION  
D7: REVISION (MSB)

Po16	0000
Bo16	0001
PBF4	0010
EN16	0011
...	

```
RevA 0000
RevB 0001
RevC 0010
RevD 0011
...
```

Sheet: /HWCFG/ File: HWCFG.kicad_sch			D
Title:			
Size: A4	Date:	Rev:	
KiCad E.D.A. 8.0.3		Id: 10/11	

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Sheet: /UI_DISPLAY/ File: UI_DISPLAY.kicad_sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.3	Id: 11/11	