USER INTERFACE TOP LEVEL SCHEMATIC **PROCESSOR** CONTROL_2 LED 2 MUX_A ADRS_A MUX_B ADRS_B MUX_C ADRS_C +5V +3V3_UC VSS < LED_3 CONTROL_3 **ANALOG SUPPLY** ADC1/4 ADC1/5 ADC0/0 DAC/0 ADC0/1 AREFA ADC1/6 ADC1/7 ✓ MUX_OUT_1 LED_4 CONTROL 4 ADC1/8 ADC1/9 ADC0/2 ADC1/0 ADC0/3 ADC1/1 ADC0/4 AREFB ADC0/5 DAC/1 ADC0/6 AREFC ADC0/7 C12 SC4/0 SC4/1 SC0/0 SC0/1 SC0/2 SC0/3 VSS < AREFB DAC1 ADC0/8 ADC1/2 SC0/0 ADC0/9 ADC1/3 SC0/1 ADC0/10 SC0/2 ADC0/11 SC0/3 USB + PWR SDHC0/SDDAT3 SDHC0/SDCK SDHC0/SDCD SC4/0 SC4/1 SC4/2 SC4/3 SC6/2 SC6/3 SC7/0 SC7/1 SC7/2 SC7/3 SC2/0 SC2/1 SC2/2 SC2/3 C16 C9 1u 100n SC7/2 SC7/3 SC6/1 SC6/0 SC6/2 SC6/3 SC4/1 SC4/0 SC4/2 SC4/3 UI_EN UI_PWR_EN SC1/0 SC1/1 SC1/2 SC1/3 SC6/0 SC6/1 SC6/2 SC6/3 CCD1 LCD1 O-LCD0 SEE CD03 O-LCD2 LCD3 LCD3 O-VSS < SC5/0 SC5/1 SC5/2 SC5/3 SC3/0 SC3/1 SC5/2 SC5/3 SC3/0 SC3/1 USBDM SC3/2 USBDP SC3/3 SERCOM7 39 51 63 77 VDDIO1 VDDIO2 VDDIO2 VDDIO3 SERCOM I2C SYSTEM_SCL SYSTEM_SDA PIN SUPPORT ON 100PIN PACKAGE SC1/2 SC1/3 SC0/0 SC0/1 SC0/2 SC0/3 VDDIO: A12, PA13, PA16, PA17, PA22, **○** GRID_AUX_RX GRID_AUX_TX GRID_AUX_TX SYNC_1 SYNC_2 SYS_DOCK_DETECT TRACEDATA3 TRACEDATA2 TRACEDATA1 TRACECLK PWR < VDD < VSS < LQH3NPN100MJ0 or Equvival 12Mhz SWDIO SWO USB_INT **MAPMODE** SC1 | UART | PA00, PA01 | GRID NORTH SC2 | UART | PB24, PB25 | GRID AUX SC3 | **** | PA16->PA19 | SYS_*** (LCD) ADC0/12 ADC0/13 ADC0/14 ADC0/15 SC4 | UART | PB08, PB09 | GRID_WEST SC5 | I2C | PB24, PB15 | SYS_I2C T 4.7u 100n HIGH-SPEED INTERFACES: USB | PA24, PA25 QSPI | PA08, PA09, PA10, PA11, PB10, PB11 | FLASH SWD | PA30, PA31, PB30, RESET TRACE | PC24, PC25, PC26, PC27, PC28

































