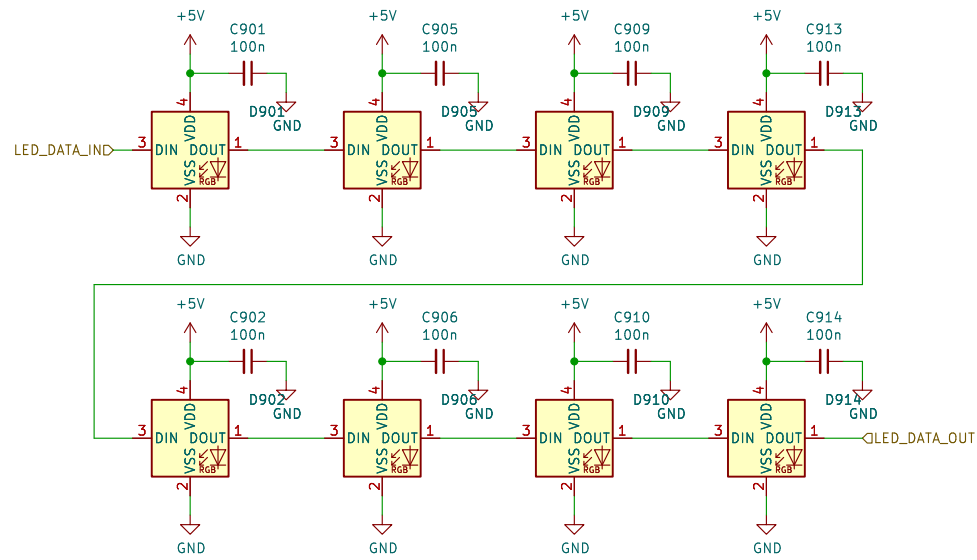




900



Sheet: /UI_LED/
File: UI_LED.kicad_sch

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500

GRID Connector
Bi-Directional Data
2x SYNC

Board Mounting Pattern



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600

ESD Diodes
ESD protection for all of the externally accessible nets.

+3V3 LDO Regulators
Regulators for generating independent power rails for the microcontroller and the user interface.

Components and Connections:

- J601 TYPE-C-32-M-12:** USB Type-C connector with pins A9B4, A4B9, CC1, CC2, D-, D+, D-, D+, SBU1, SBU2, SHIELD, SHIELD, SHIELD, SHIELD, GND, GND.
- U601 C5451661:** ESD diode for USB data lines.
- TP601 GND, TP602 VB, TP603 D-, TP604 D+:** Test points for USB data lines.
- U602 LN1134A332MR-G:** LDO regulator for +3V3.
- Capacitors:** C601 (1u), C602 (1u), C603 (4n7), C604 (1M).
- Resistors:** R601 (5k1), R602 (5k1), R603 (1M).
- Other:** PWR_FLAG, VBUS, USB_DATA_N, USB_DATA_P, GND, +3V3.

**Sheet: /MCU/Sheet60F06FE1/
File: USB_POWER.kicad_sch**

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800

The schematic shows a 74HC165 (U801) connected to various signals. The SI pin (10) is connected to DATA_IND. The D0-D7 pins (11-18) are connected to HWCFG_LOW, HWCFG_HIGH, and HWCFG_CLOCKD. The SH/LD pin (1) is connected to HWCFG_SHIFTD. The VCC pin (16) is connected to +3V3. The Q pin (9) is connected to HWCFG_DATA. A jumper JP801 (NF) is connected between pin 2 and +3V3, with a note "OPEN IF EN16 CLOSED IF NO DETENT". A resistor R801 (5k1) is connected between pin 2 and GND. A capacitor C801 (100n) is connected between HWCFG_HIGH and HWCFG_LOW.

Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Model Codes (D3–D0):

Po16 0000
Bo16 0001
PBF4 0010
EN16 0011
...

Revision Codes (D7–D4):

RevA 0000
RevB 0001
RevC 0010
RevD 0011
...

Sheet: /HWCFG/ File: HWCFG.kicad_sch		
Title:		
Size: A4	Date:	Rev:
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Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Po16	0000
Bo16	0001
PBF4	0010
EN16	0011
...	

```
RevA 0000
RevB 0001
RevC 0010
RevD 0011
...
```

Sheet: /HWCFG/ File: HWCFG.kicad_sch			
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Sheet: /UI_ENC/ENCODER_0/
File: UI_ENC_FILTER.kicad_sch

Title:

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Sheet: /UI_ENC/ENCODER_3/
File: UI_ENC_FILTER.kicad_sch

Title:

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Sheet: /UI_ENC/ENCODER_1/
File: UI_ENC_FILTER.kicad_sch

Title:

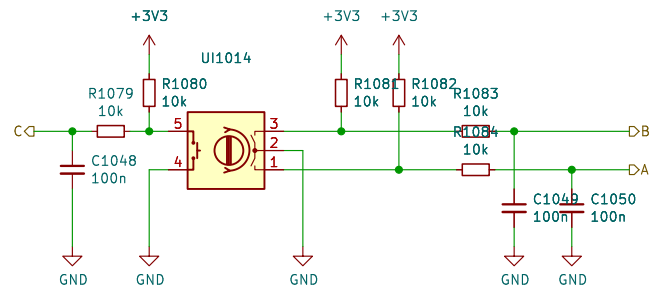
Size: A4

Date:

Rev:

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Id: 15/11



Sheet: /UI_ENC/ENCODER_2/
File: UI_ENC_FILTER.kicad_sch

Title:

Size: A4

Date:

Rev:

KiCad E.D.A. 8.0.6

Id: 21/11