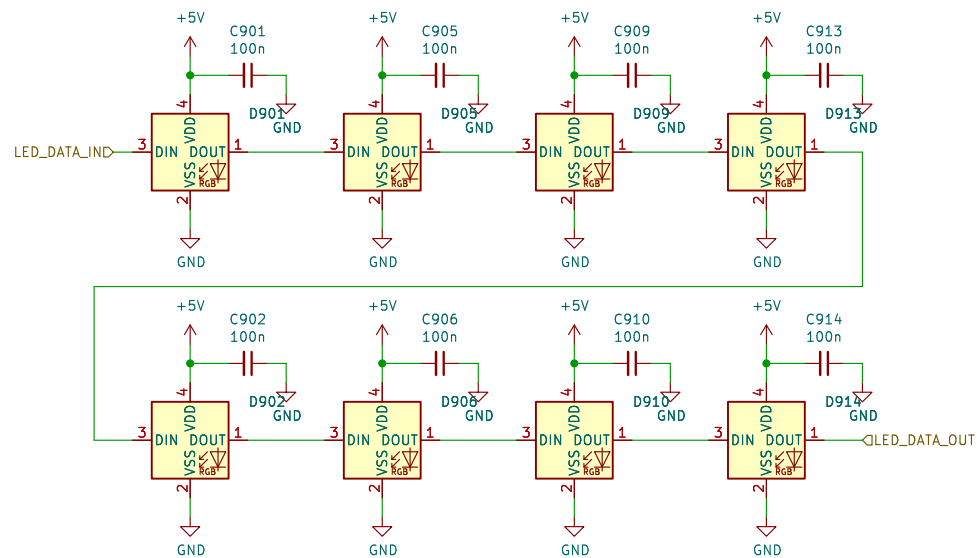


900



Sheet: /UI_LED/
File: UI_LED.kicad_sch

Title:

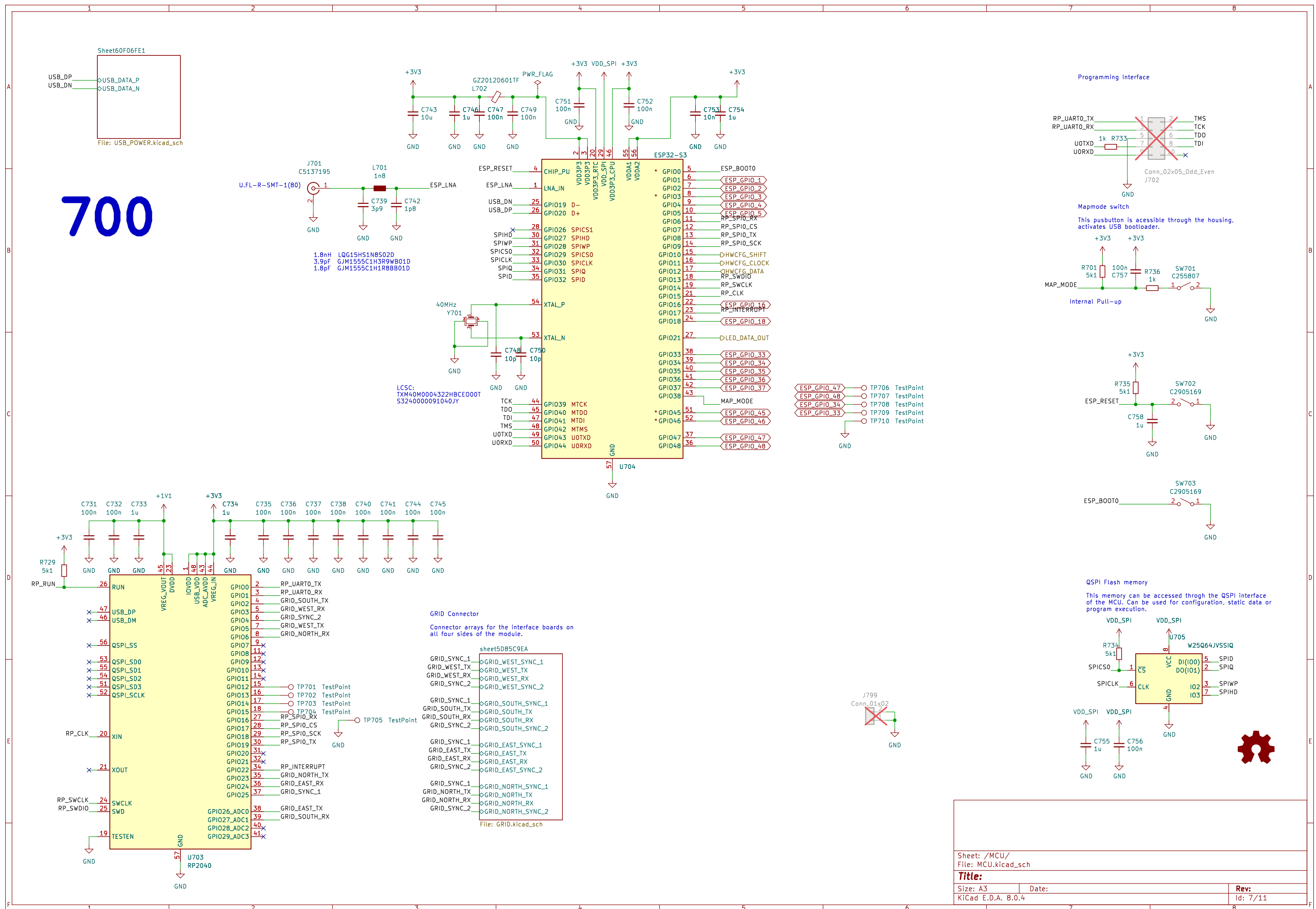
Size: A4

Date:

KiCad E.D.A. 8.0.4

Rev:

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500

GRID Connector
Bi-Directional Data
2x SYNC

Board Mounting Pattern



Sheet: /MCU/sheet5D85C9EA/ File: GRID.kicad_sch		
Title:		
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600

ESD Diodes

ESD protection for all of the externally accessible nets.

TP601 GND

TP602 VB

TP603 D-

TP604 D+

VBUS

USB_DATA_N

USB_DATA_P

USB_DATA_N

VBUS

VBUS

PWR_FLAG

VBUS

J601 TYPE-C-32-M-12

VBUS

VBUS

CC1

CC2

D-

D-

D+

D+

SBU1

SBU2

SHIELD

SHIELD

SHIELD

GND

GND

A9B4

A4B9

A5

B5

A7

B7

A6

B6

A8

BB

A1B12

A12B1

PWR_FLAG

GND

GND

5k1

5k1

R601

R602

C603 4n7

R603 1M

GND

GND

TP605

UI

+3V3

VBUS +5V

GZ2012D601TF L601

PWR_FLAG

C601 1u

U602 LN1134A332MR-G

IN

EN

OUT

NC

GND

GND

GND

GND

1

3

5

4

2

1

2

3

4

5

6

7

8

9

10

11

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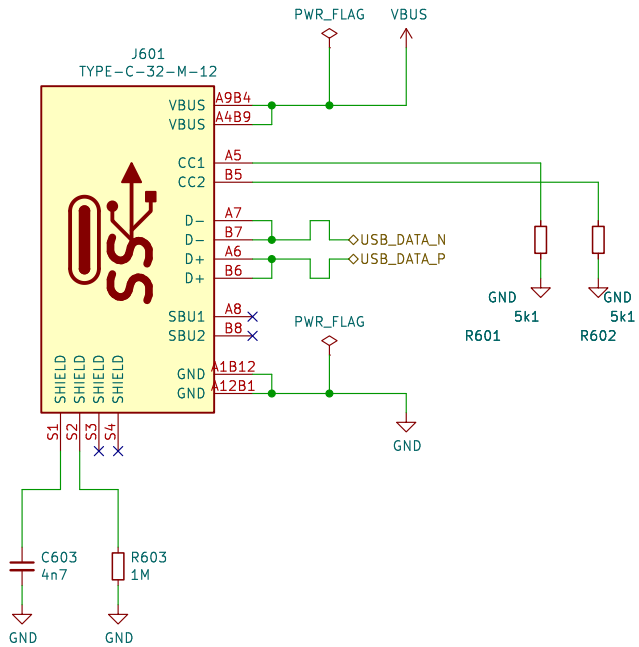
369

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372</

ESD protection for all of the externally accessible nets.



Id: 9/11

800

OPEN IF EN16
CLOSED IF NO DETENT

JP801 (NF)

R801 5k1

GND

+3V3

HWCFG_LOW

HWCFG_LOW

HWCFG_HIGH

HWCFG_LOW

HWCFG_LOW

HWCFG_LOW

HWCFG_HIGH

D0

D1

D2

D3

D4

D5

D6

D7

DATA_IND

U801 74HC165

VCC

HWCFG_CLOCKD

HWCFG_SHIFTD

GND

GND

+3V3

C801 100n

HWCFG_HIGH

HWCFG_LOW

GND

HWCFG_DATA

Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Model Codes (D3–D0):

Po16 0000
Bo16 0001
PBF4 0010
EN16 0011
...

Revision Codes (D7–D4):

RevA 0000
RevB 0001
RevC 0010
RevD 0011
...

Sheet: /HWCFG/ File: HWCFG.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.4		Id: 10/11

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Po16	0000
Bo16	0001
PBF4	0010
EN16	0011
...	

```
RevA 0000
RevB 0001
RevC 0010
RevD 0011
...
```

Sheet: /HWCFG/ File: HWCFG.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.4		Id: 10/11





Sheet: /UI_ENC/ENCODER_3/
File: UI_ENC_FILTER.kicad_sch

Title:

Size: A4

Date:

Rev:

KiCad E.D.A. 8.0.4

Id: 14/11



