

1000



Sheet: /UI\_POT/  
File: UI\_POT.kicad\_sch

**Title:**

Size: A4

Date:

KiCad E.D.A. kicad-cli 7.0.9-7.0.9-ubuntu23.04.1

**Rev:**

Id: 2/10

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Simulation:  
<http://tinyurl.com/y229mty4>



Sheet: /UI\_BUTTON/  
File: UI\_BUTTON.kicad\_sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad-cli 7.0.9-7.0.9-ubuntu23.04.1

Rev:

Id: 3/10

# 900



Sheet: /UI\_LED/  
File: UI\_LED.kicad\_sch

### Title:

Size: A4  
KiCad E.D.A. kicad-cli 7.0.9-7.0.9-ubuntu23.04.1

Date:

Rev:  
Id: 4/10

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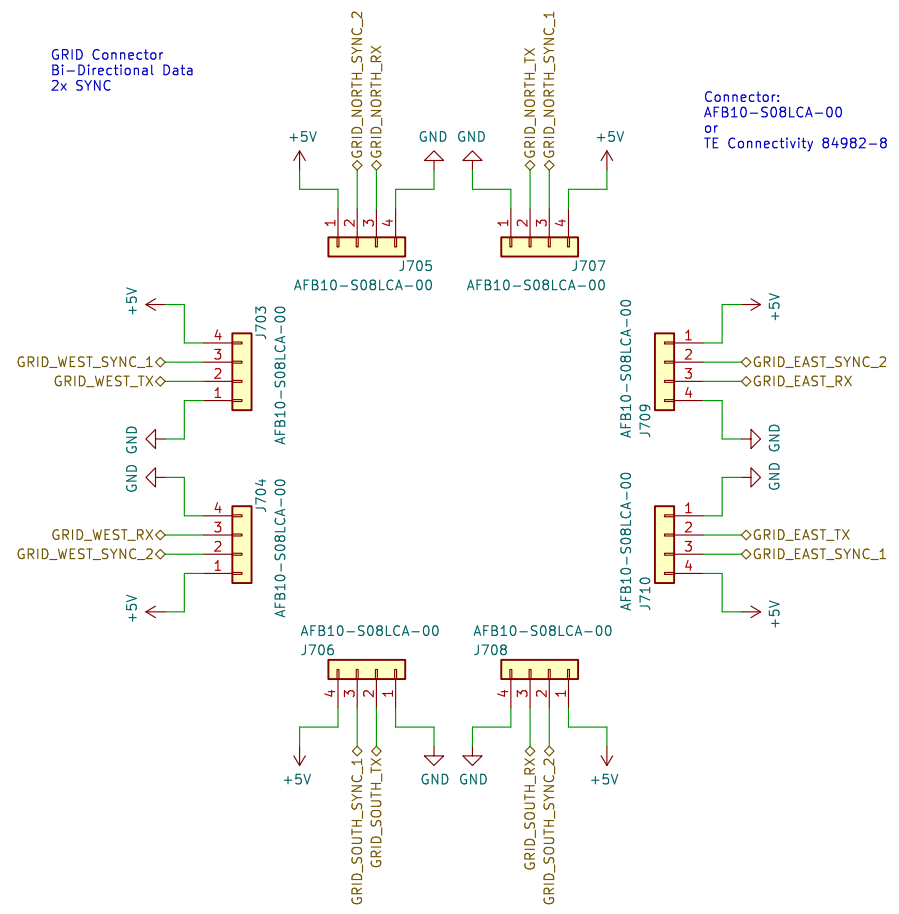


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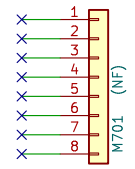




# 500



Board Mounting Pattern





# 600

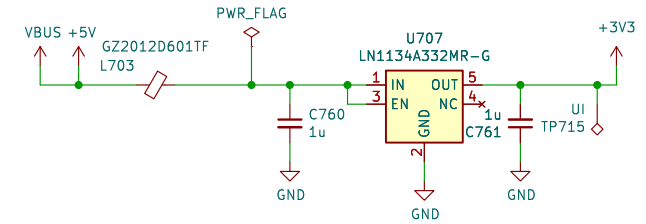
## ESD Diodes

ESD protection for all of the externally accessible nets.



## +3V3 LDO Regulators

Regulators for generating independent power rails for the microcontroller and the user interface.



Sheet: /MCU/Sheet60F06FE1/  
File: USB\_POWER.kicad\_sch

## Title:

Size: A4

Date:

KiCad E.D.A. kicad-cli 7.0.9-7.0.9-ubuntu23.04.1

Rev:

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**800**

The schematic shows the HWCFG component U801 (74HC165) connected as follows:

- VCC (pin 16) is connected to +3V3.
- GND (pins 8 and 1) are connected to ground.
- HWCFG\_CLOCKD (pin 2) is connected to CLK.
- HWCFG\_SHIFTD (pin 1) is connected to SH/LD.
- HWCFG\_HIGH is connected to pin 7.
- HWCFG\_LOW is connected to pin 15.
- HWCFG\_DATA (pin 9) is connected to DQ.
- Jumper JP701 (NF) is connected between +3V3 and pin 3 (D4).
- Jumper JP801 (NF) is connected between +3V3 and pin 11 (D0).
- Resistor R702 (5k1) is connected between +3V3 and pin 3 (D4).
- Resistor R801 (5k1) is connected between +3V3 and pin 11 (D0).

**Board Identification**

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)  
D1: MODEL  
D2: MODEL  
D3: MODEL (MSB)  
D4: REVISION (LSB)  
D5: REVISION  
D6: REVISION  
D7: REVISION (MSB)

**Model Codes (D3-D0):**

Po16 0000  
Bo16 0001  
PBF4 0010  
EN16 0011  
...

**Revision Codes (D7-D4):**

RevA 0000  
RevB 0001  
RevC 0010  
RevD 0011  
...

Sheet: /HWCFG/ File: HWCFG.kicad_sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A.	kicad-cli 7.0.9-7.0.9-ubuntu23.04.1	Id: 10/10

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

D0: MODEL (LSB)  
D1: MODEL  
D2: MODEL  
D3: MODEL (MSB)  
D4: REVISION (LSB)  
D5: REVISION  
D6: REVISION  
D7: REVISION (MSB)

Po16	0000
Bo16	0001
PBF4	0010
EN16	0011
...	

```
RevA 0000
RevB 0001
RevC 0010
RevD 0011
...
```

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