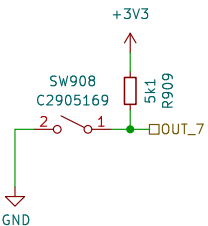
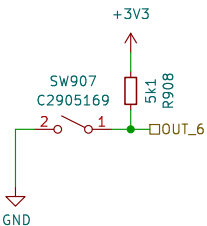
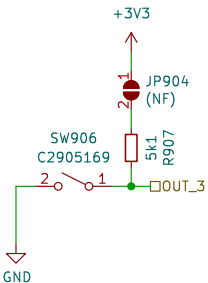
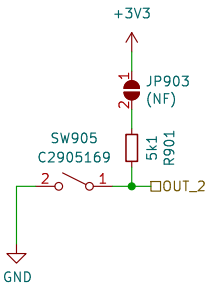
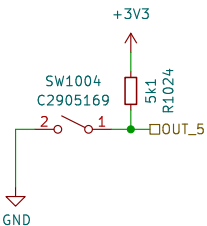
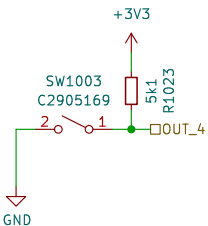
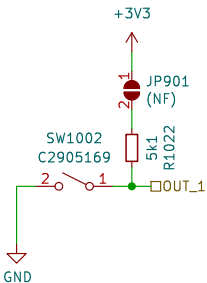
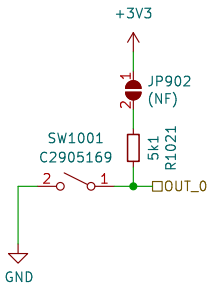


1000



Sheet: /UI_POT_BTN/
File: UI_POT_BTN.kicad_sch

Title:

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Date:

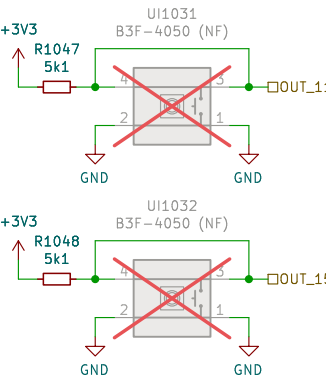
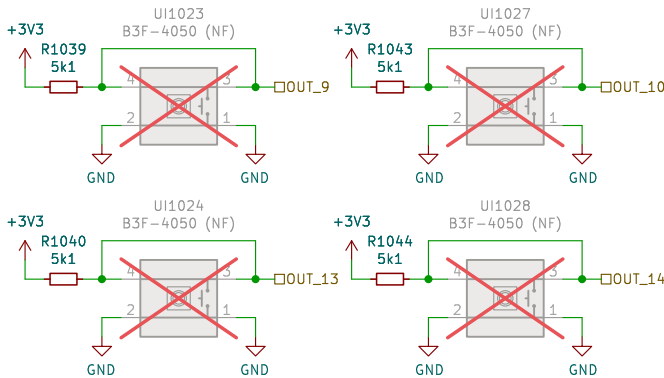
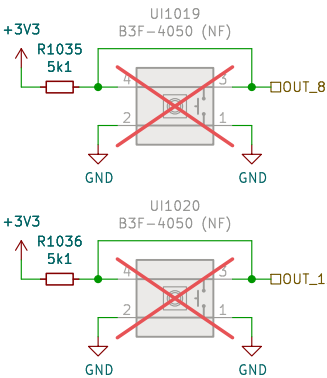
KiCad E.D.A. 8.0.3

Rev:

Id: 2/11

1000

Simulation:
<http://tinyurl.com/y229mty4>



Sheet: /UI_BUTTON/ File: UI_BUTTON.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.3	Id: 3/11	

900



Sheet: /UI_LED/
File: UI_LED.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.3

Rev:

Id: 4/11

1000



1000



700

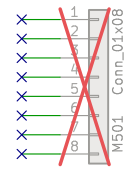


500

GRID Connector
Bi-Directional Data
2x SYNC



Board Mounting Pattern



Sheet: /MCU/sheet5D85C9EA/ File: GRID.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.3		Id: 8/11

600

ESD Diodes

ESD protection for all of the externally accessible nets.



+3V3 LDO Regulators

Regulators for generating independent power rails for the microcontroller and the user interface.



Sheet: /MCU/Sheet60F06FE1/
File: USB_POWER.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.3

Rev:

Id: 9/11

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Po16	0000
Bo16	0001
PBF4	0010
EN16	0011
...	

```
RevA 0000
RevB 0001
RevC 0010
RevD 0011
...
```

Sheet: /HWCFG/ File: HWCFG.kicad_sch		D	
Title:			
Size: A4	Date:		Rev:
KiCad E.D.A. 8.0.3			Id: 10/11

1000

The schematic diagram illustrates the electrical connections for a UI_DISPLAY circuit. It features two ST7789 320x240 LCD modules, labeled U901 and U1003, both using an SPI interface. The LCD modules are connected to a +3V3 power supply and ground. The circuit also includes a 74HC1G14GV inverter (U902) which is connected to the CS pin of U901 and the D/C pin of U901. The inverter's output is connected to the D/C pin of U901. The LCD modules are connected to a +3V3 power supply and ground. The circuit also includes resistors R903, R1017, R1018, and R1026, and capacitors C920, C919, C1020, and C1019. The schematic is labeled '1000' in the top left corner.

Sheet: /UI_DISPLAY/		
File: UI_DISPLAY.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.3	Id: 11/11	