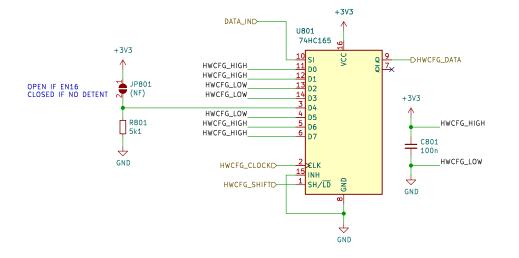


800



Board Identification

Grid firmware can identify the hardware and the board revision thorugh a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

```
DO: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION (MSB)
```

Model Codes (D3-D0):

Po16 0000 Bo16 0001 PBF4 0010 EN16 0011

Revision Codes (D7-D4):

RevA 0000 RevB 0001 RevC 0010 RevD 0011

Sheet: /HWCFG/
File: HWCFG.kicad_sch

Title:

 Size: A4
 Date:
 Rev:

 KiCad E.D.A. 8.0.8
 Id: 10/23

