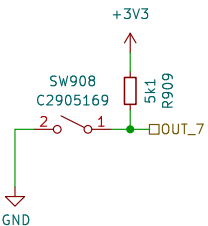
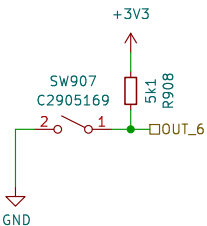
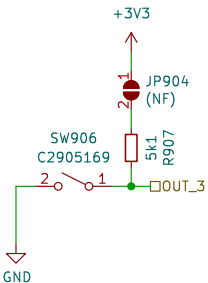
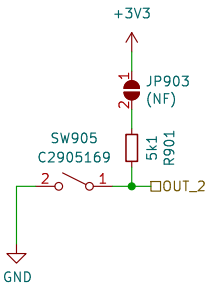
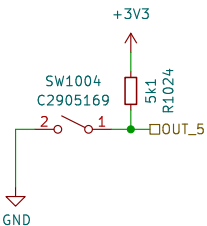
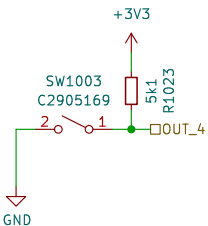
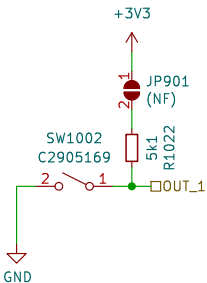
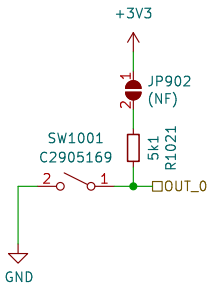


1000



Sheet: /UI_POT_BTN/
File: UI_POT_BTN.kicad_sch

Title:

Size: A4

Date:

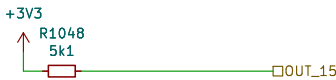
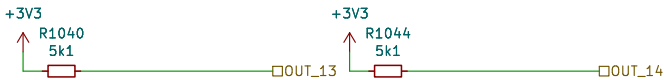
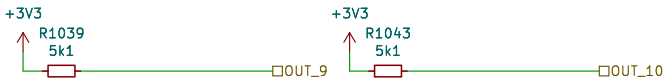
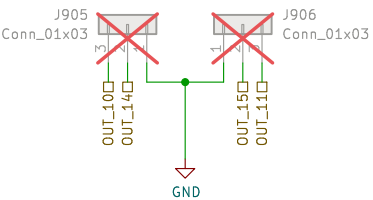
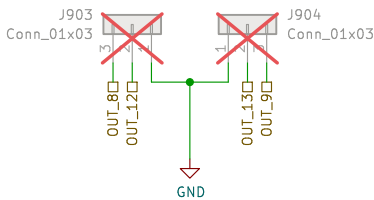
KiCad E.D.A. 8.0.4

Rev:

Id: 2/11

1000

Simulation:
<http://tinyurl.com/y229mty4>



Sheet: /UI_BUTTON/
File: UI_BUTTON.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.4

Rev:

Id: 3/11

900



Sheet: /UI_LED/
File: UI_LED.kicad_sch

Title:

Size: A4
KiCad E.D.A. 8.0.4

Date:

Rev:
Id: 4/11

1000



Sheet: /Sheet5D7C8BFD/ File: UI_MUX.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.4	Id: 5/11	

1000



700



500

GRID Connector
Bi-Directional Data
2x SYNC

Board Mounting Pattern

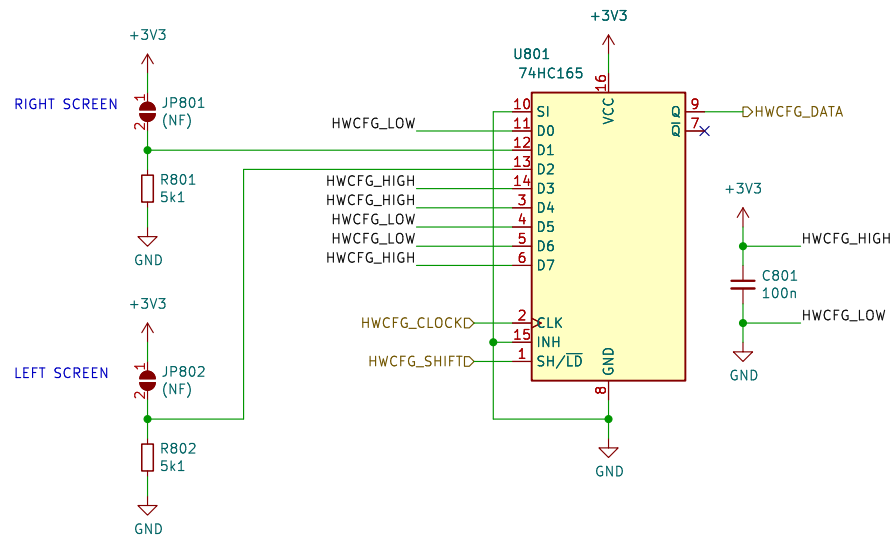


Sheet: /MCU/sheet5D85C9EA/ File: GRID.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.4	Id: 8/11	

[illegible]

Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.4		Id: 9/11
4	5	6

800



Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Model Codes (D3-D0):

Po16 0000
Bo16 0001
PBF4 0010
EN16 0011
...

Revision Codes (D7-D4):

RevA 0000
RevB 0001
RevC 0010
RevD 0011
...

Sheet: /HWCFG/
File: HWCFG.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.4

Rev:

Id: 10/11

1000

BACKLIGHT \square BACKLIGHT_PWM

$\overline{CS1}$ CS1
 $\overline{CS0}$ CS0
D/ \overline{C} D/ \overline{C}
SCLKD SCLK
SDIOD SDIO
 \overline{RESET} RESET



Sheet: /UI_DISPLAY/
File: UI_DISPLAY.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.4

Rev:

Id: 11/11