



HWCFG

HWCFG\_SHIFT

HWCFG\_CLOCK

HWCFG\_DATA

File: HWCFG.kicad\_sch

Common Sheets:

500 GRID

600 USB\_POWER

700 MCU

Module Specific:

800 HWCFG

900 LED

1000 UI

Sheet: /

File: PCBA-TEK2.kicad\_sch

Title:

Size: A3

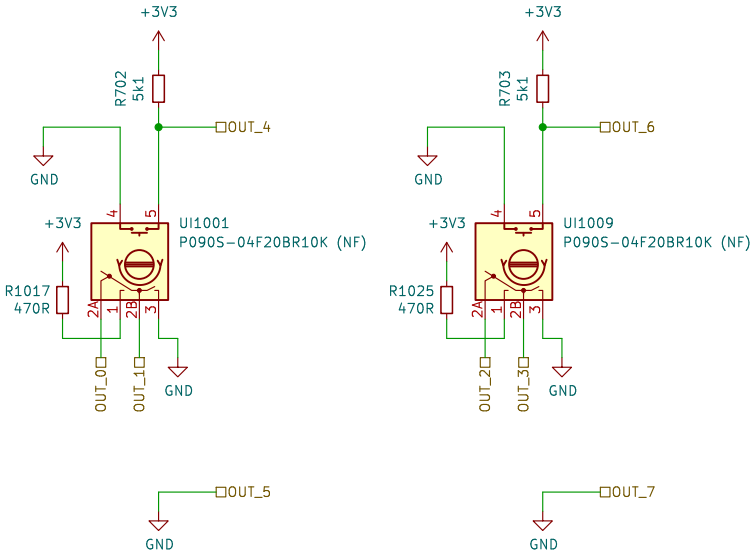
Date:

Rev:

KiCad E.D.A. 8.0.6

Id: 1/10

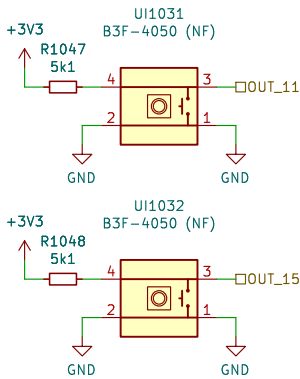
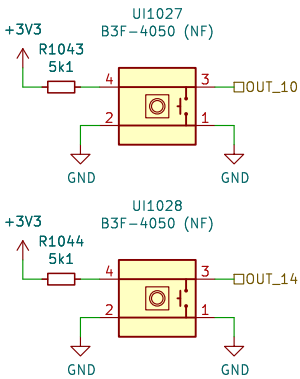
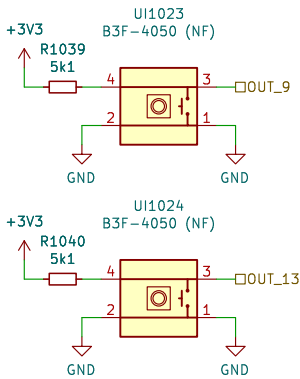
1000



Sheet: /UI_POT/ File: UI_POT.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.6	Id: 2/10	

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Simulation:  
<http://tinyurl.com/y229mt4>



Sheet: /UI_BUTTON/ File: UI_BUTTON.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.6	Id: 3/10	

# 900



Sheet: /UI\_LED/  
File: UI\_LED.kicad\_sch

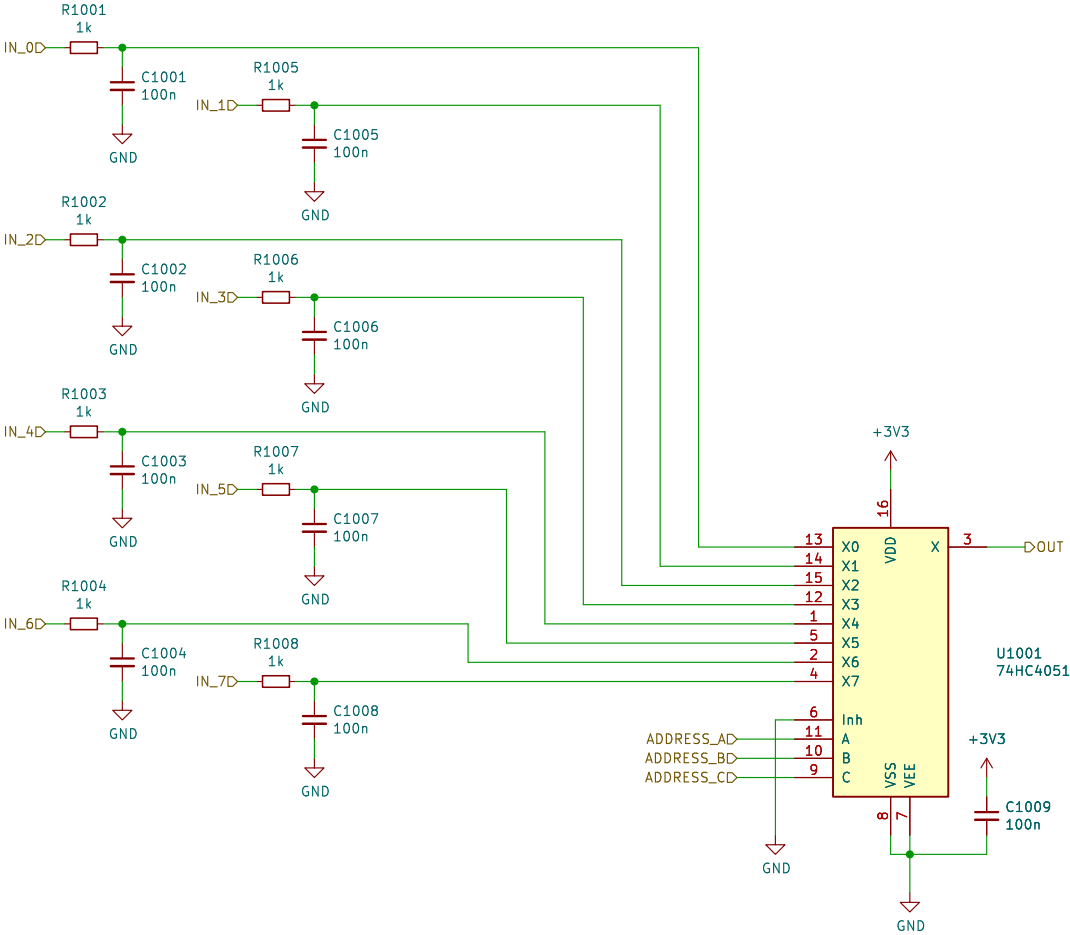
**Title:**

Size: A4  
KiCad E.D.A. 8.0.6

Date:

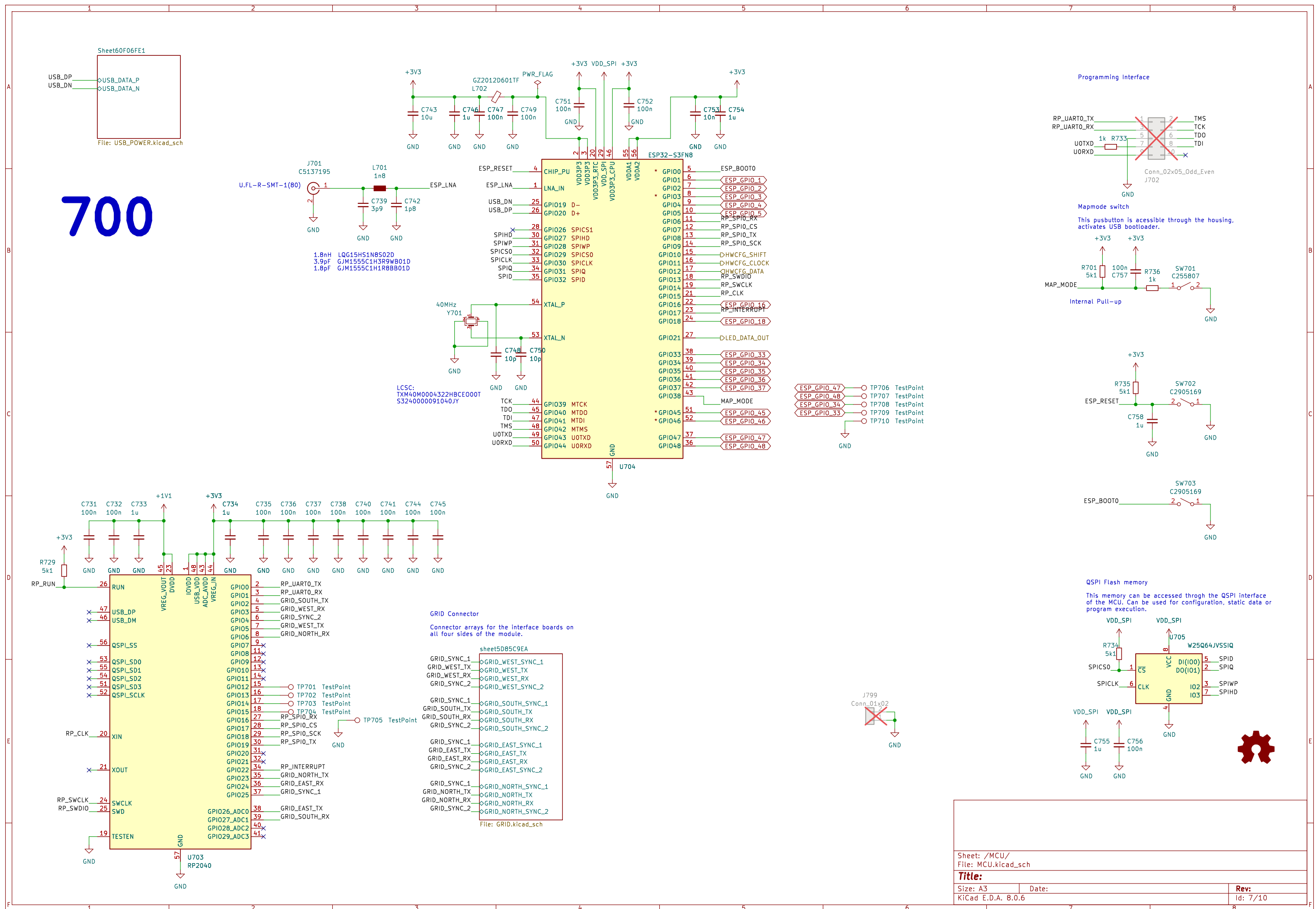
**Rev:**  
Id: 4/10

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<b>Title:</b>		
Size: A4	Date:	<b>Rev:</b>
KiCad E.D.A. 8.0.6		Id: 8/10
4	5	6

Size: A4	Date:	Rev:
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6	Id: 8/10
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Id: 8/10

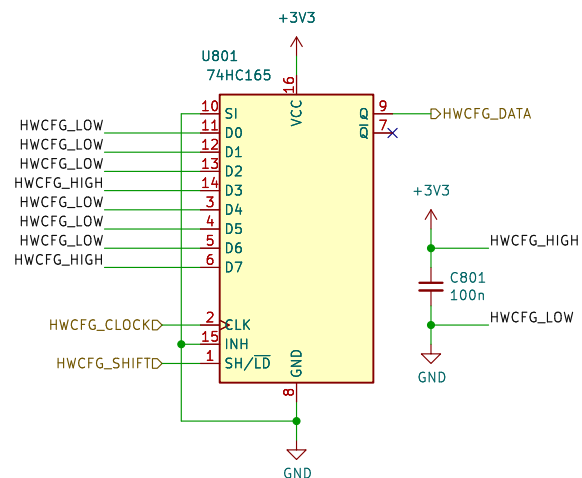
6



[illegible]

<b>Title:</b>		
Size: A4	Date:	<b>Rev:</b>
KiCad E.D.A. 8.0.6		Id: 9/10

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#### Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)  
D1: MODEL  
D2: MODEL  
D3: MODEL (MSB)  
D4: REVISION (LSB)  
D5: REVISION  
D6: REVISION  
D7: REVISION (MSB)

#### Model Codes (D3-D0):

Po16 0000  
Bo16 0001  
PBF4 0010  
EN16 0011  
...

#### Revision Codes (D7-D4):

RevA 0000  
RevB 0001  
RevC 0010  
RevD 0011  
...

Sheet: /HWCFG/  
File: HWCFG.kicad\_sch

#### Title:

Size: A4

Date:

KiCad E.D.A. 8.0.6

Rev:

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