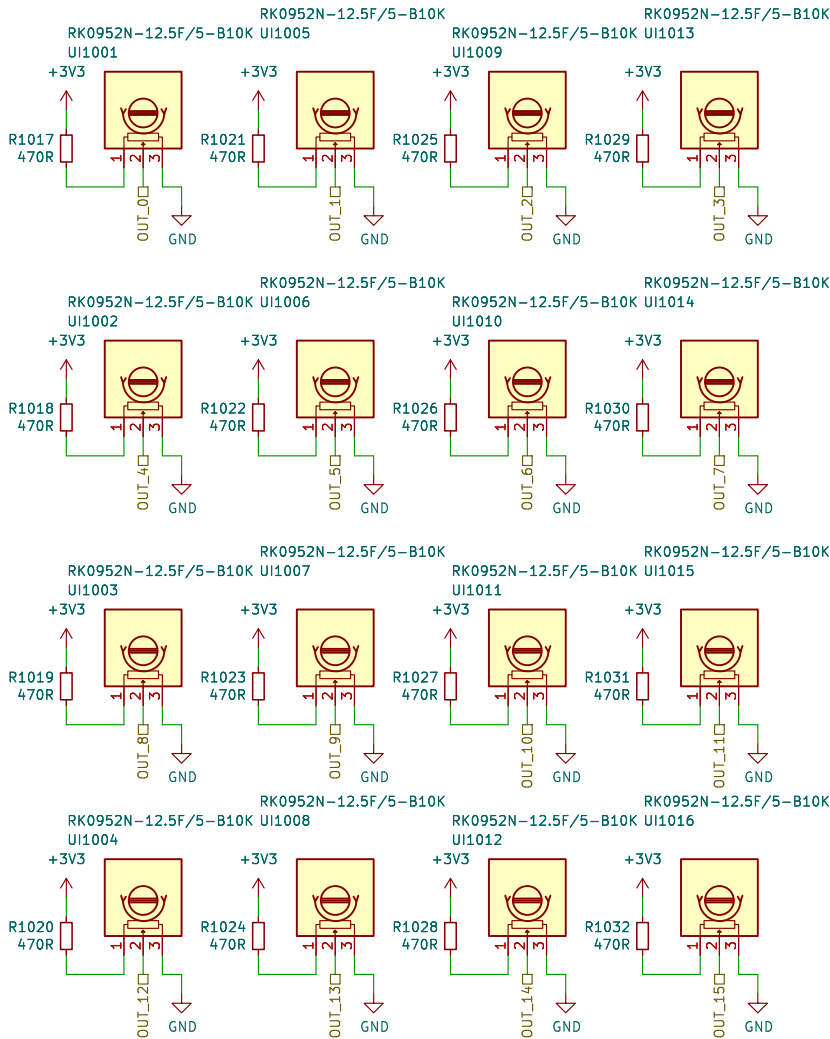


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File: UI\_POT.kicad\_sch

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Size: A4

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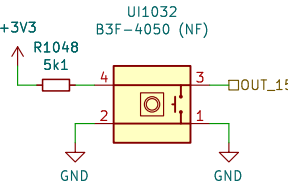
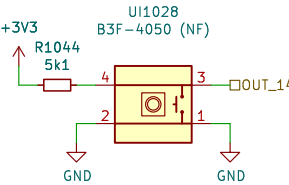
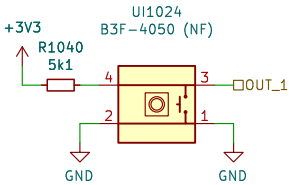
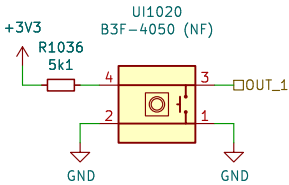
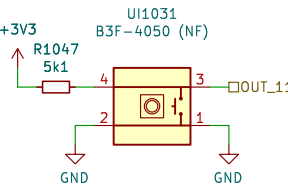
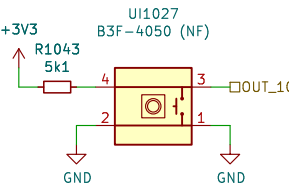
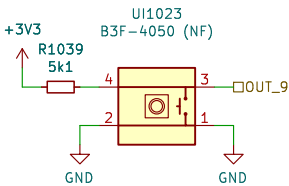
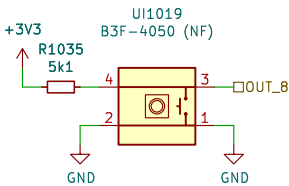
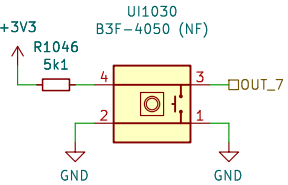
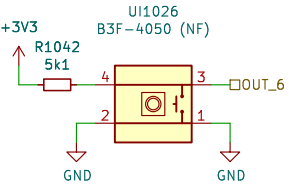
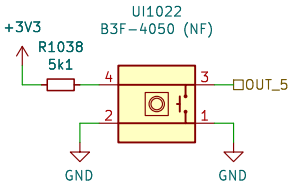
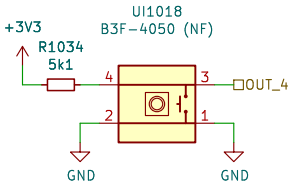
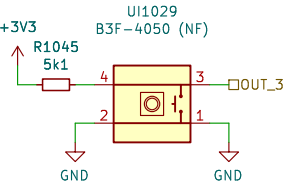
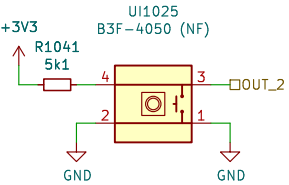
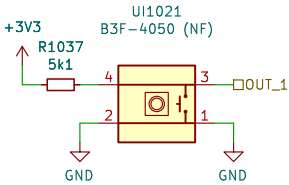
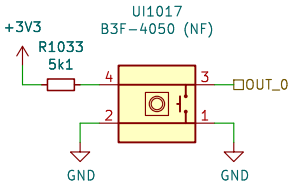
KiCad E.D.A. 8.0.8

**Rev:**

Id: 2/10

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Simulation:  
<http://tinyurl.com/y229mty4>



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File: UI\_BUTTON.kicad\_sch

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Size: A4  
KiCad E.D.A. 8.0.8

Date:

Rev:  
Id: 3/10

# 900



Sheet: /UI\_LED/  
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**Title:**

Size: A4  
KiCad E.D.A. 8.0.8

Date:

**Rev:**  
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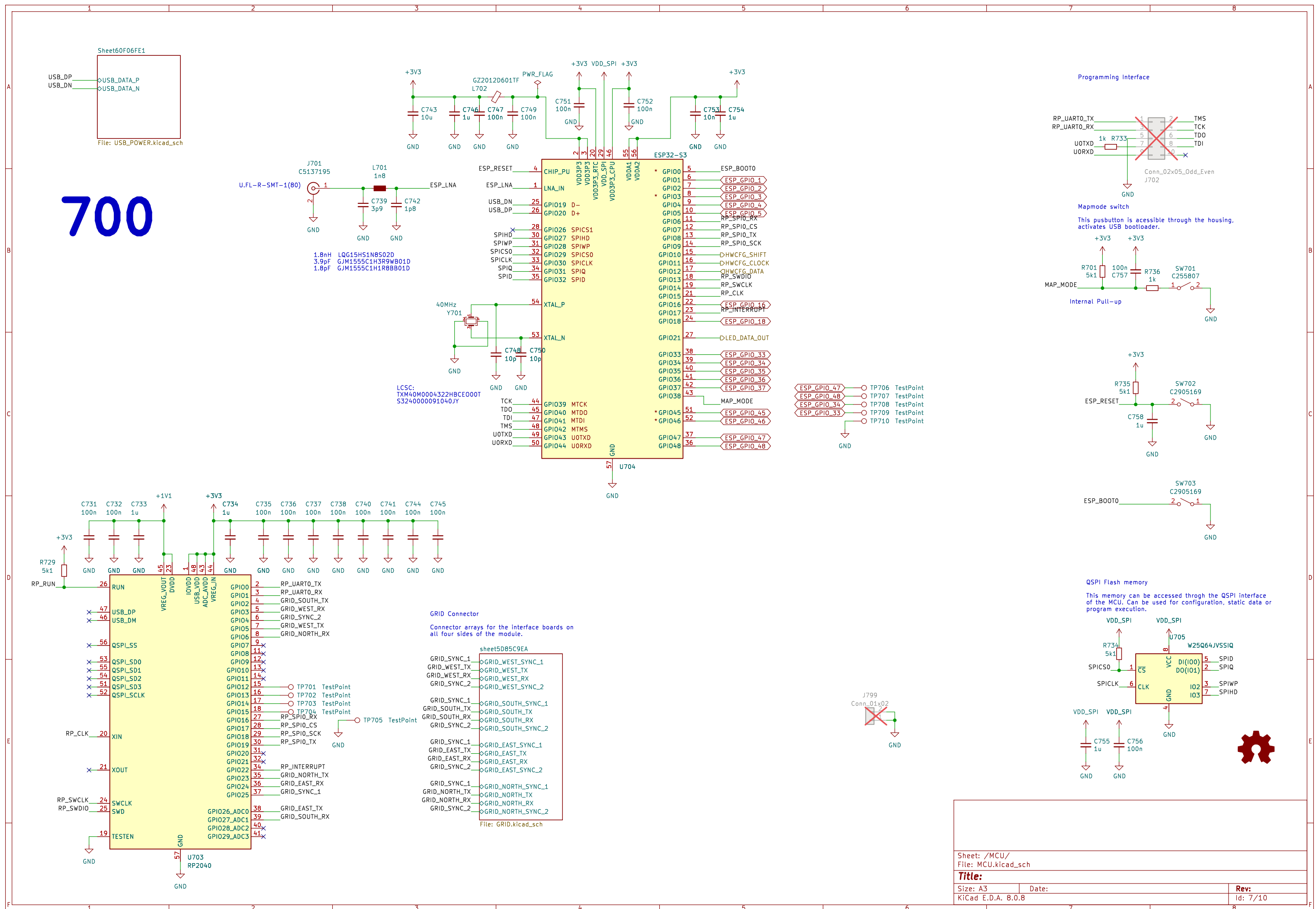
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Title:		
Size: A4	Date:	Rev:
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500

GRID Connector  
Bi-Directional Data  
2x SYNC

Board Mounting Pattern



Sheet: /MCU/sheet5D85C9EA/ File: GRID.kicad_sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.8	Id: 8/10	



# 600

## ESD Diodes

ESD protection for all of the externally accessible nets.



## +3V3 LDO Regulators

Regulators for generating independent power rails for the microcontroller and the user interface.



Sheet: /MCU/Sheet60F06FE1/  
File: USB\_POWER.kicad\_sch

### Title:

Size: A4

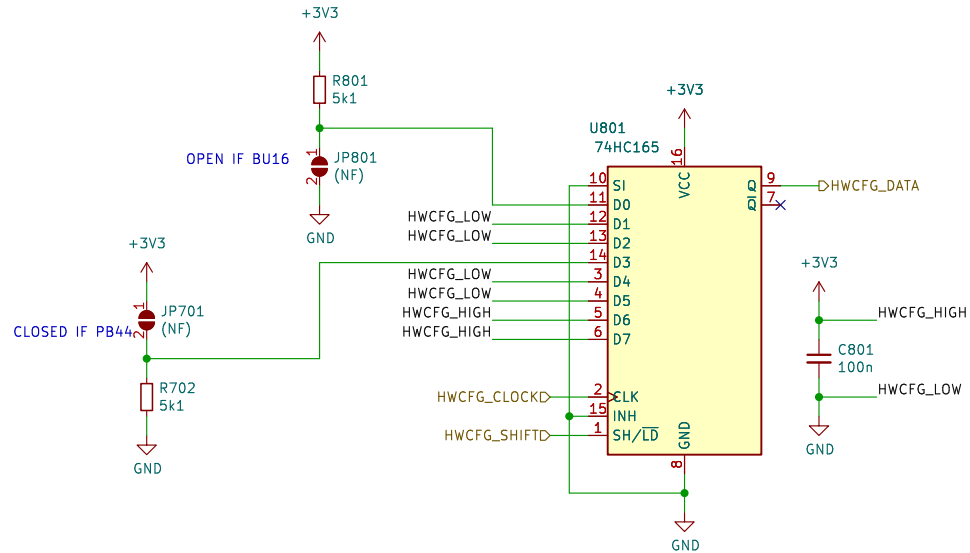
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Rev:

Id: 9/10

# 800



## Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)  
D1: MODEL  
D2: MODEL  
D3: MODEL (MSB)  
D4: REVISION (LSB)  
D5: REVISION  
D6: REVISION  
D7: REVISION (MSB)

## Model Codes (D3-D0):

Po16 0000  
Bo16 0001  
PBF4 0010  
EN16 0011  
...

## Revision Codes (D7-D4):

RevA 0000  
RevB 0001  
RevC 0010  
RevD 0011  
...

Sheet: /HWCFG/  
File: HWCFG.kicad\_sch

## Title:

Size: A4

Date:

KiCad E.D.A. 8.0.8

Rev:

Id: 10/10