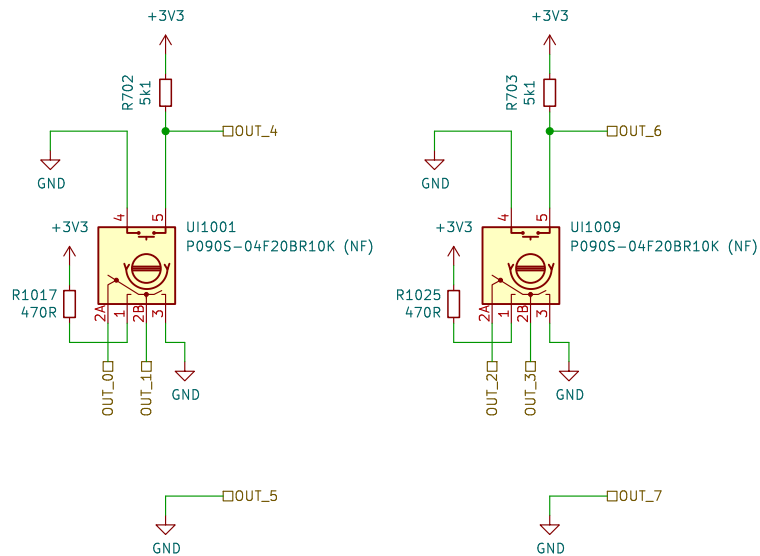


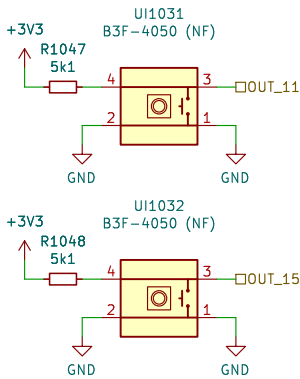
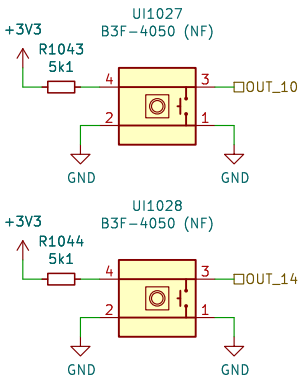
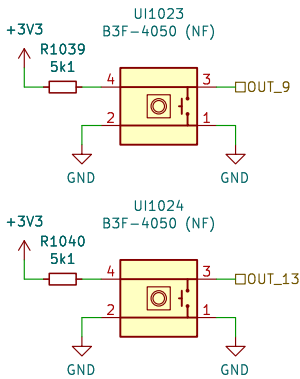
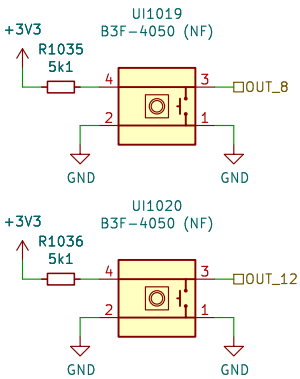
1000



Sheet: /UI_POT/ File: UI_POT.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.8	Id: 2/10	

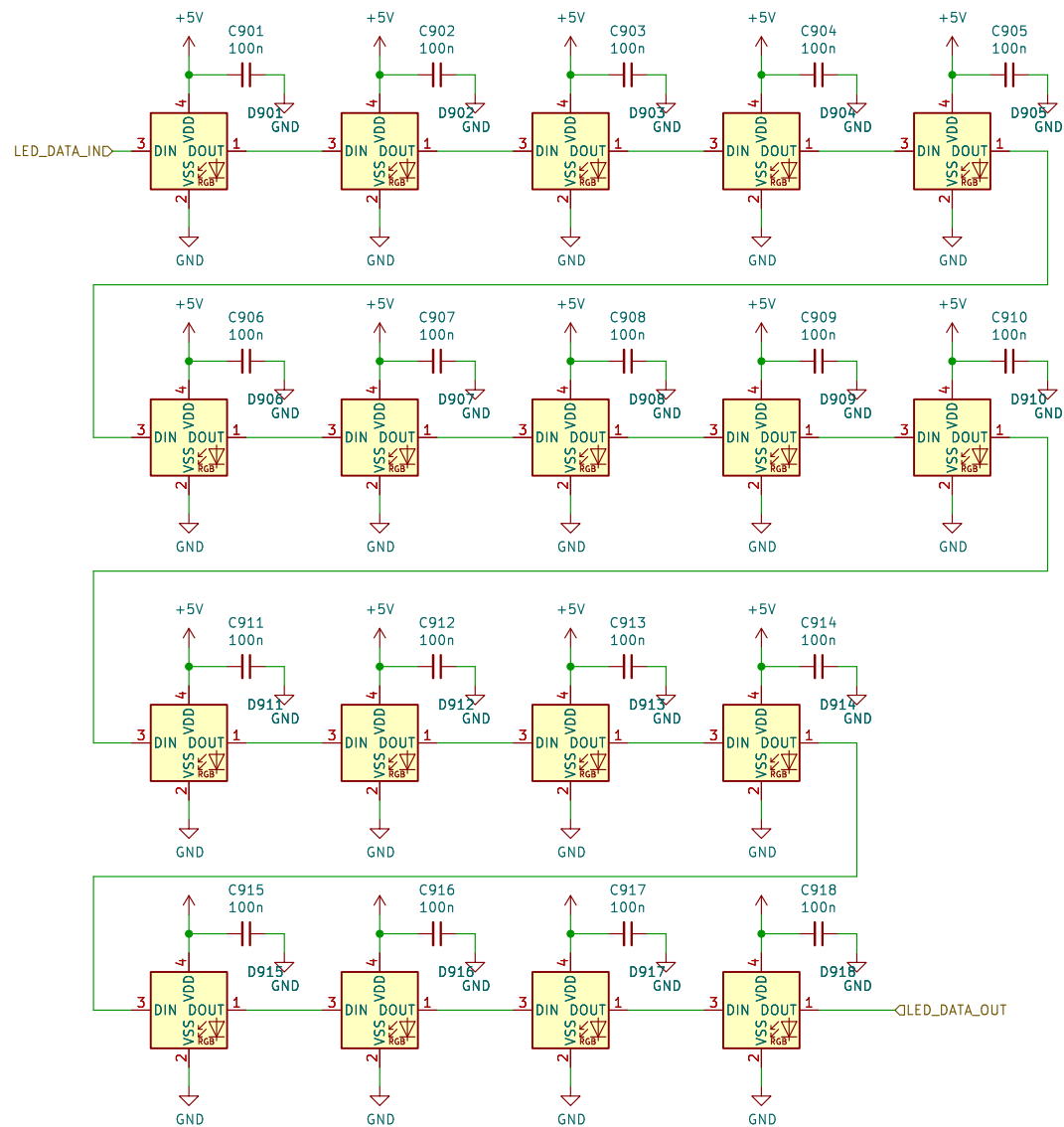
1000

Simulation:  
<http://tinyurl.com/y229mt4>



Sheet: /UI_BUTTON/ File: UI_BUTTON.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.8	Id: 3/10	

# 900



Sheet: /UI\_LED/  
File: UI\_LED.kicad\_sch

**Title:**

Size: A4  
KiCad E.D.A. 8.0.8

Date:

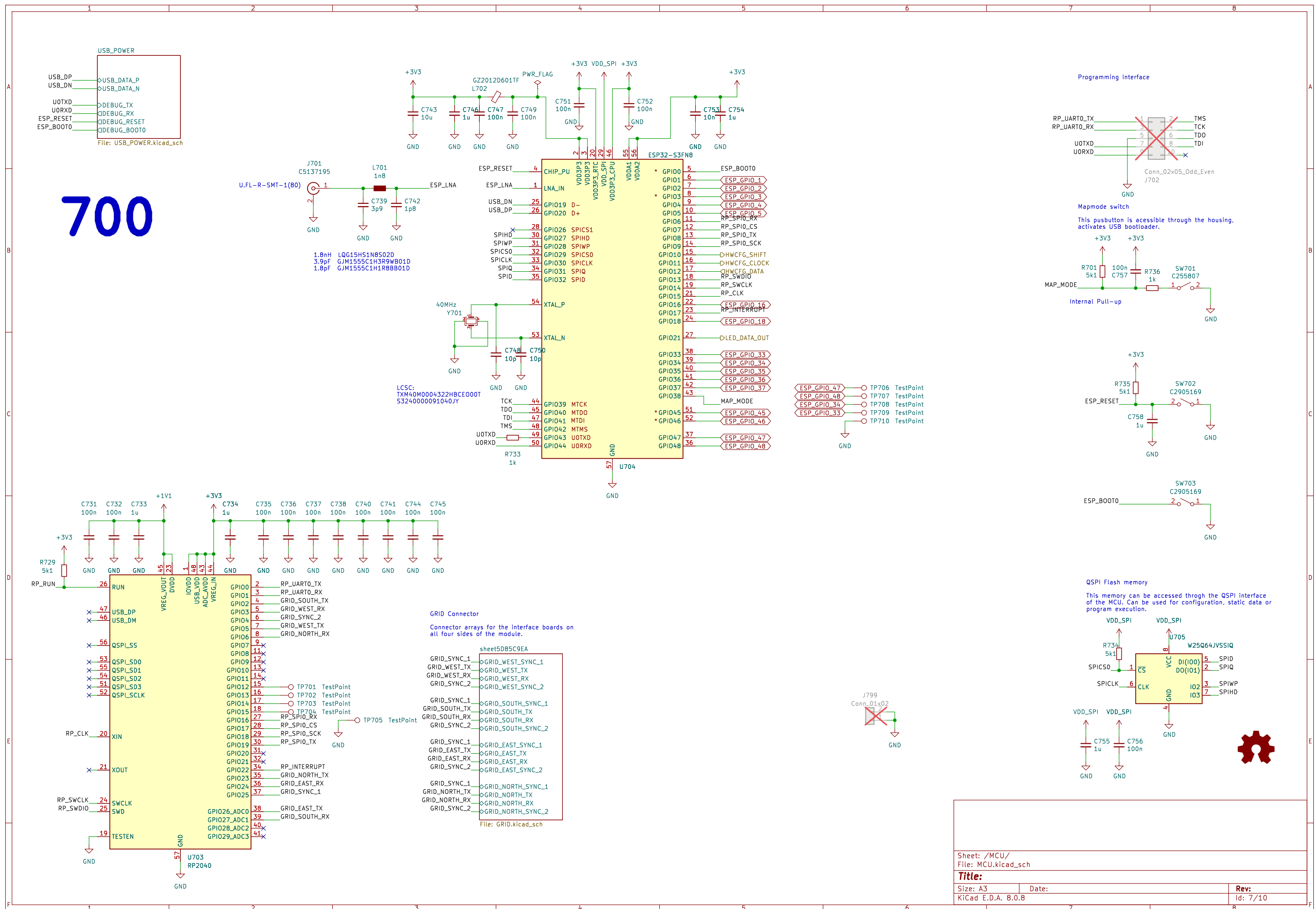
**Rev:**  
Id: 4/10

1000



1000





500

GRID Connector  
Bi-Directional Data  
2x SYNC

Board Mounting Pattern



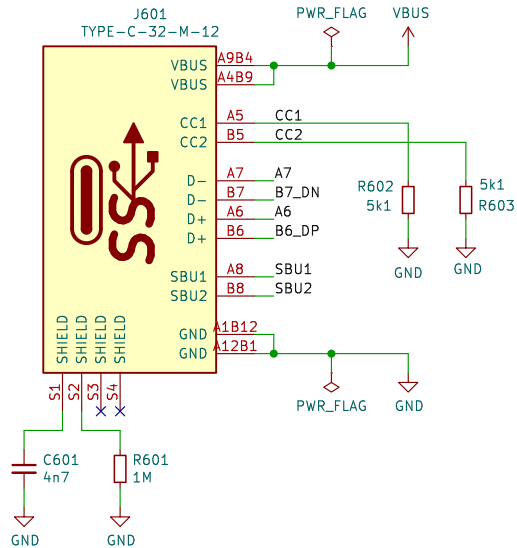
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<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.8	Id: 8/10	



600

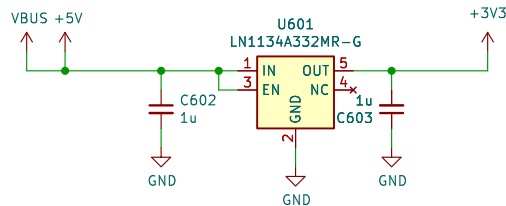
## USB Port

USB C upstream facing port configured for 5V 3A power consumption.



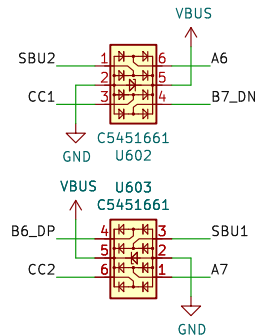
## 3V3 LDO

LDO regulator for generating the +3V3 power rail for the microcontroller and UI.

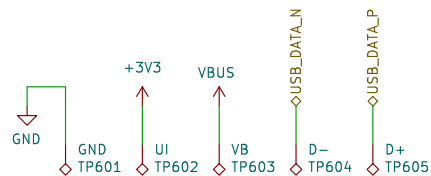


## ESD Prot.

ESP protection for all 8 signals externally accessible via the USB C connector.

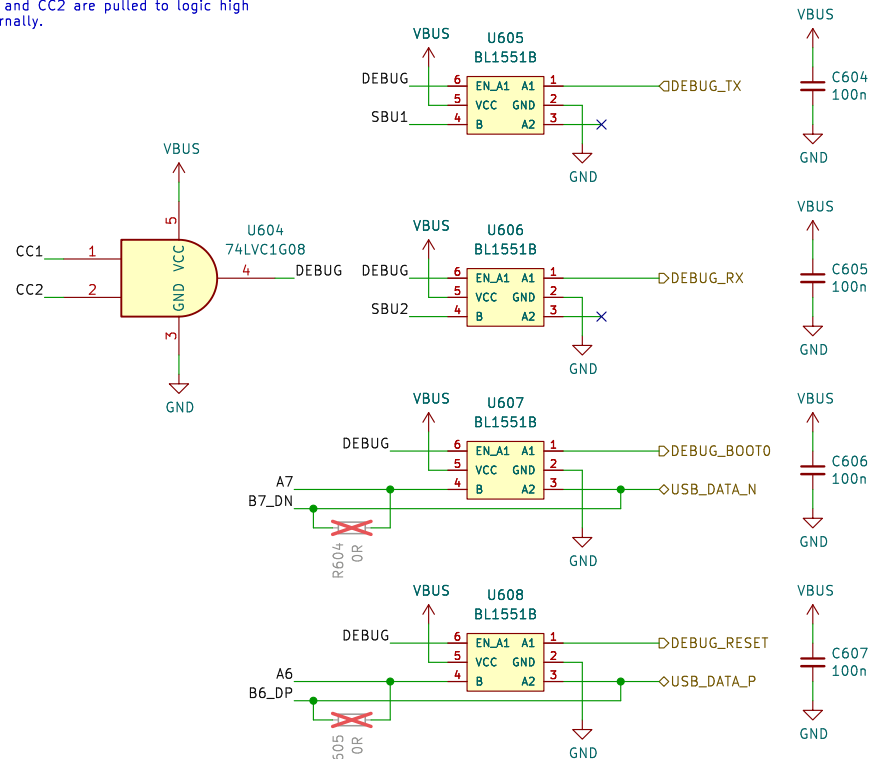


## Testpoints



## Debug-Mode Multiplexing

Debug.mode is activated when both CC1 and CC2 are pulled to logic high externally.



Sheet: /MCU/USB\_POWER/  
File: USB\_POWER.kicad\_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.8

Rev:

Id: 9/10

800

U801  
74HC165

+3V3

VCC

16

9

7

6

5

4

3

2

1

8

GND

HWCFG\_LOW

HWCFG\_LOW

HWCFG\_LOW

HWCFG\_LOW

HWCFG\_LOW

HWCFG\_LOW

HWCFG\_LOW

HWCFG\_HIGH

HWCFG\_CLOCKD

HWCFG\_SHIFTD

SI

D0

D1

D2

D3

D4

D5

D6

D7

Q

HWCFG\_DATA

+3V3

HWCFG\_HIGH

C801  
100n

HWCFG\_LOW

GND

Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)  
D1: MODEL  
D2: MODEL  
D3: MODEL (MSB)  
D4: REVISION (LSB)  
D5: REVISION  
D6: REVISION  
D7: REVISION (MSB)

Model Codes (D3–D0):

Po16 0000  
Bo16 0001  
PBF4 0010  
EN16 0011  
...

Revision Codes (D7–D4):

RevA 0000  
RevB 0001  
RevC 0010  
RevD 0011  
...

Sheet: /HWCFG/ File: HWCFG.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.8		Id: 10/10

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

D0: MODEL (LSB)  
D1: MODEL  
D2: MODEL  
D3: MODEL (MSB)  
D4: REVISION (LSB)  
D5: REVISION  
D6: REVISION  
D7: REVISION (MSB)

Po16	0000
Bo16	0001
PBF4	0010
EN16	0011
...	

```
RevA 0000
RevB 0001
RevC 0010
RevD 0011
...
```

Sheet: /HWCFG/ File: HWCFG.kicad_sch			
<b>Title:</b>			
Size: A4	Date:		<b>Rev:</b>
KiCad E.D.A. 8.0.8			Id: 10/10