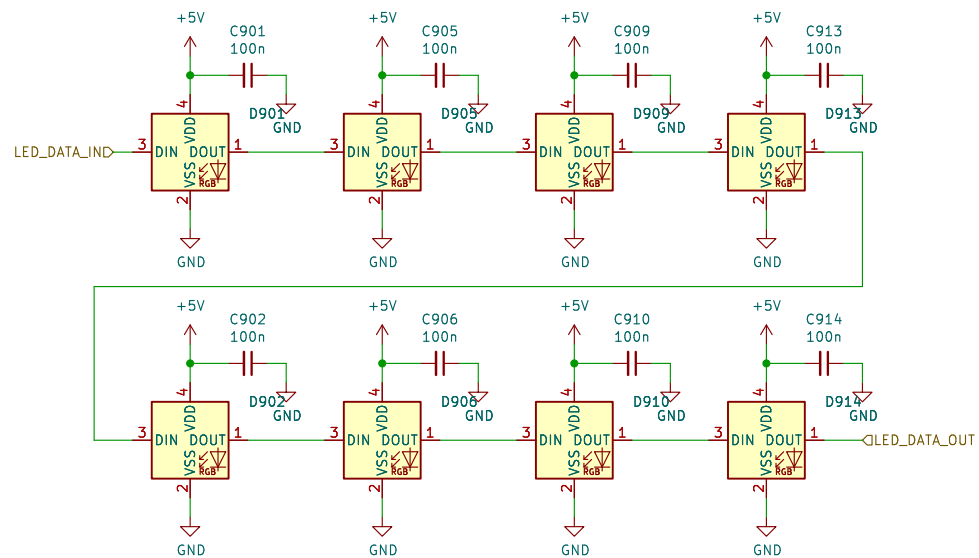


900



Sheet: /UI_LED/
File: UI_LED.kicad_sch

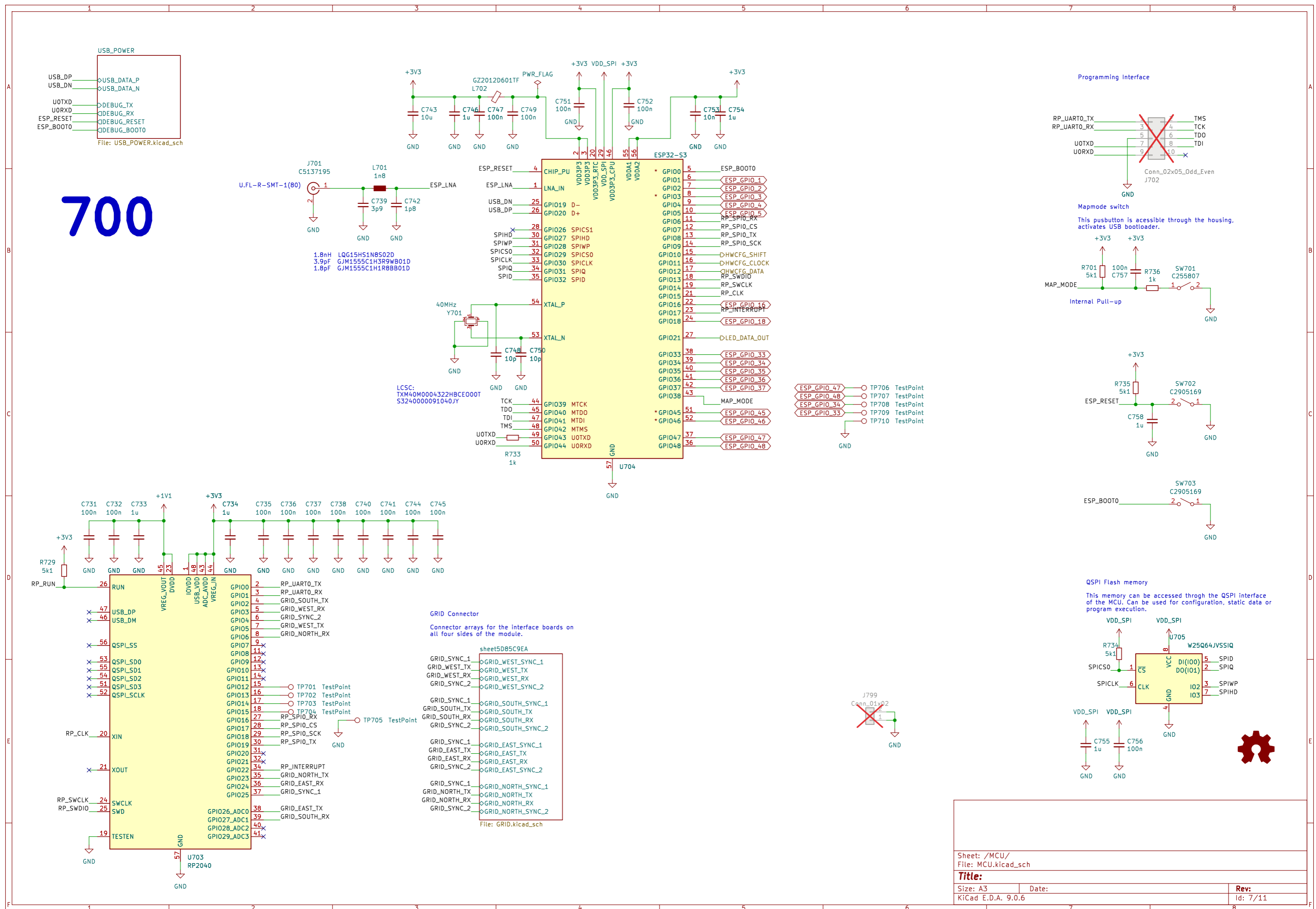
Title:

Size: A4
KiCad E.D.A. 9.0.6

Date:

Rev:

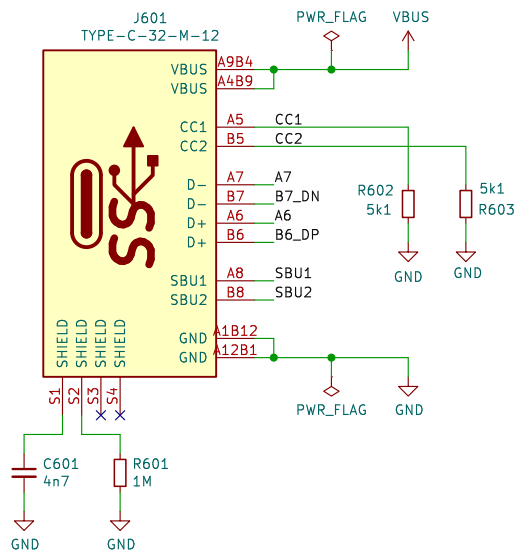
Id: 4/11



600

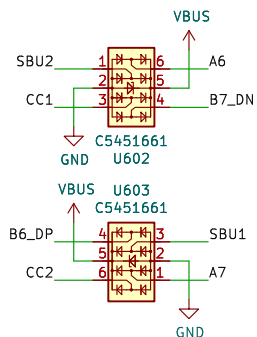
USB Port

USB C upstream facing port configured for 5V 3A power consumption.



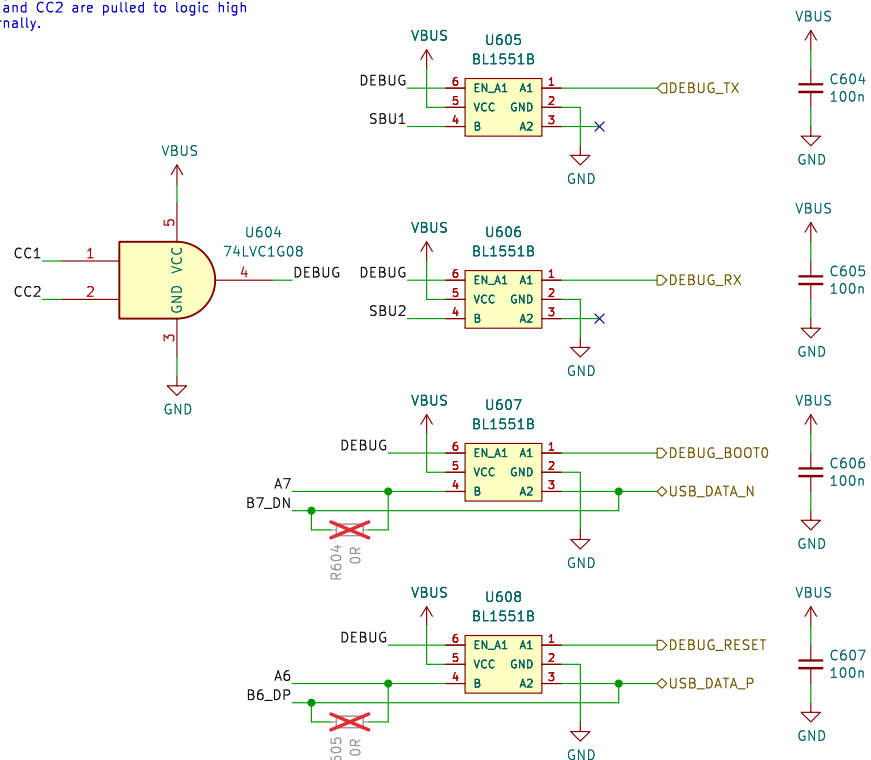
ESD Prot.

ESP protection for all 8 signals externally accessible via the USB C connector.



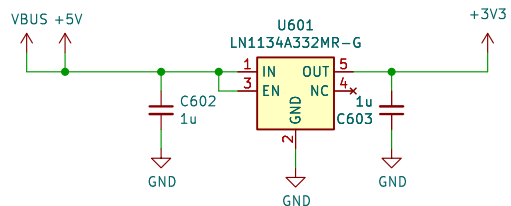
Debug-Mode Multiplexing

Debug.mode is activated when both CC1 and CC2 are pulled to logic high externally.

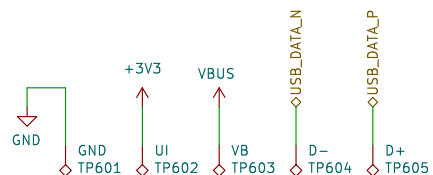


3V3 LDO

LDO regulator for generating the +3V3 power rail for the microcontroller and UI.



Testpoints



Sheet: /MCU/USB_POWER/
File: USB_POWER.kicad_sch

Title:

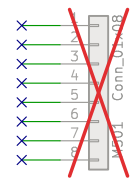
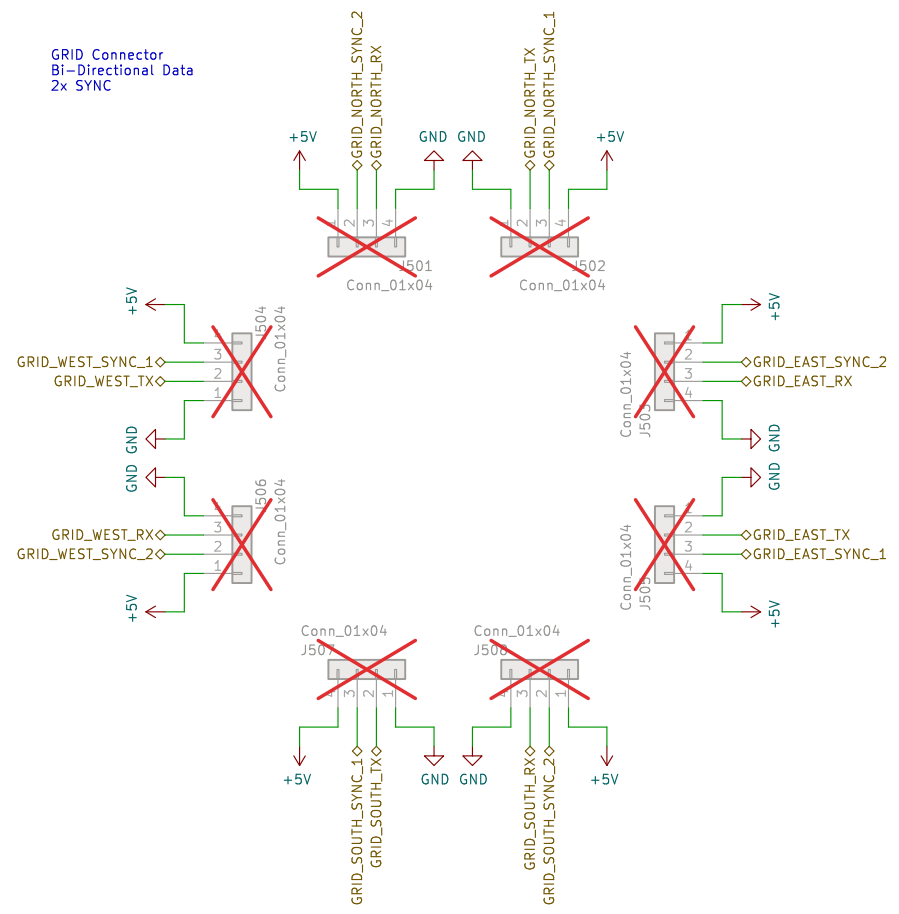
Size: A4	Date:
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Rev:
Id: 2/11

500

GRID Connector
Bi-Directional Data
2x SYNC

Board Mounting Pattern



Sheet: /MCU/sheet5D85C9EA/
File: GRID.kicad_sch

Title:

Size: A4

Date:

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800

OPEN IF EN16
CLOSED IF NO DETENT

JP801 (NF)

R801 5k1

+3V3

GND

DATA_IND

U801 74HC165

VCC

HWCFG_LOW

HWCFG_LOW

HWCFG_HIGH

HWCFG_LOW

HWCFG_LOW

HWCFG_HIGH

HWCFG_HIGH

HWCFG_CLOCKD

HWCFG_SHIFTD

HWCFG_DATA

HWCFG_HIGH

C801 100n

HWCFG_LOW

GND

GND

GND

Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Model Codes (D3–D0):

Po16 0000
Bo16 0001
PBF4 0010
EN16 0011
...

Revision Codes (D7–D4):

RevA 0000
RevB 0001
RevC 0010
RevD 0011
...

Sheet: /HWCFG/ File: HWCFG.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.6		Id: 10/11

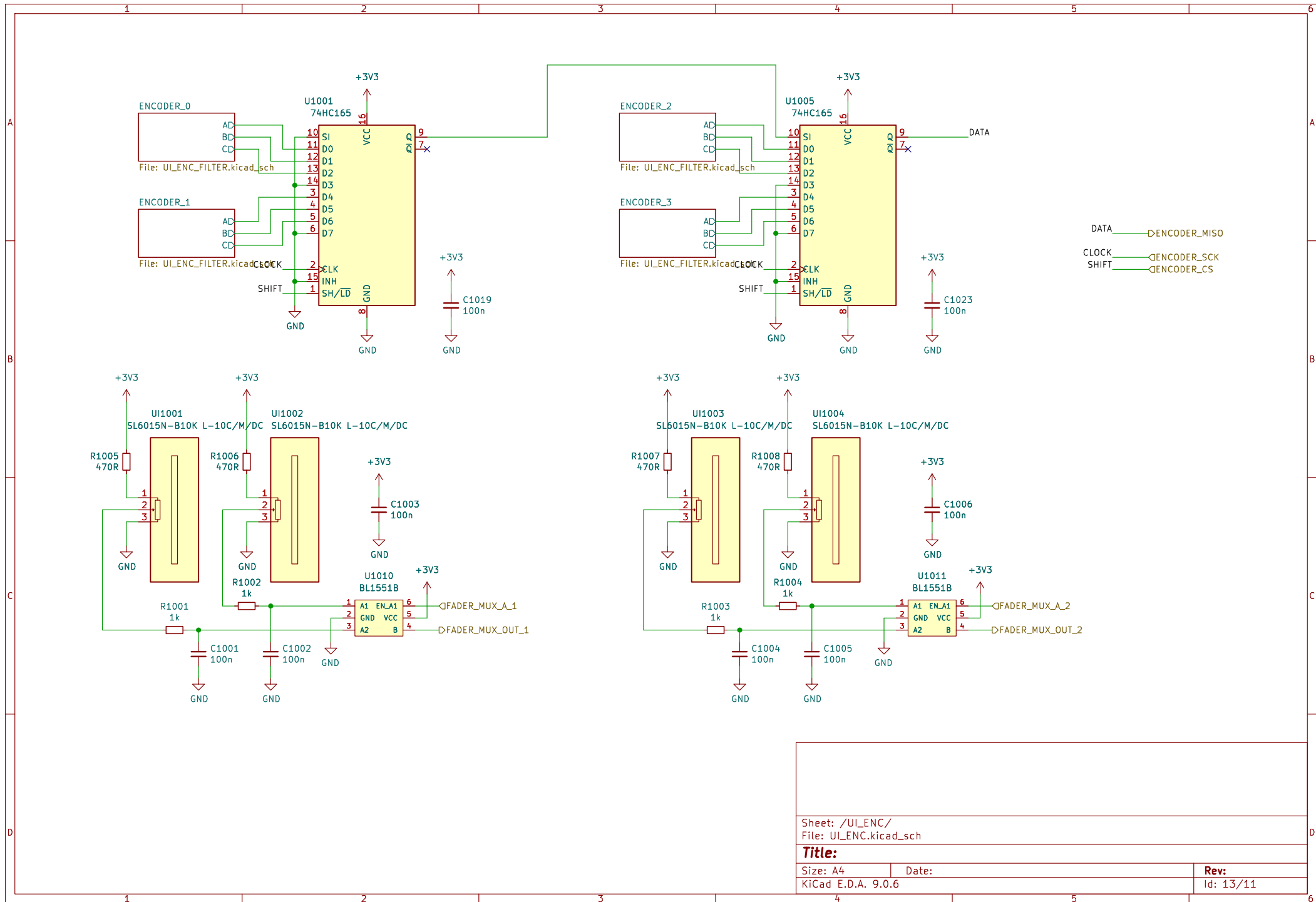
Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

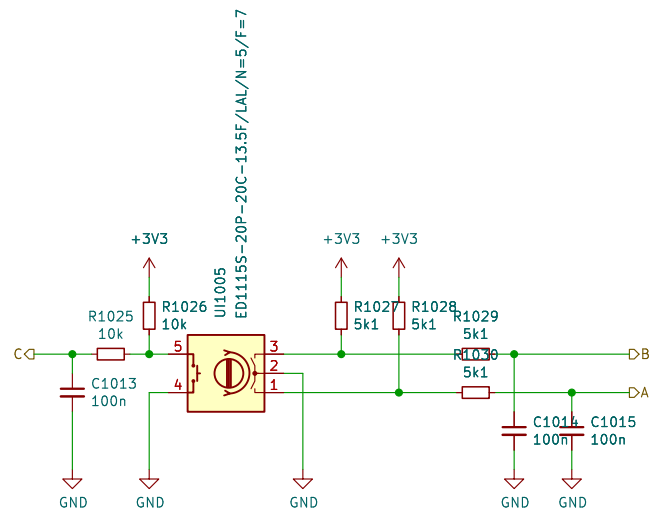
D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Po16	0000
Bo16	0001
PBF4	0010
EN16	0011
...	

```
RevA 0000
RevB 0001
RevC 0010
RevD 0011
...
```

Sheet: /HWCFG/ File: HWCFG.kicad_sch			
Title:			
Size: A4	Date:		Rev:
KiCad E.D.A. 9.0.6			Id: 10/11





Sheet: /UI_ENC/ENCODER_0/
File: UI_ENC_FILTER.kicad_sch

Title:

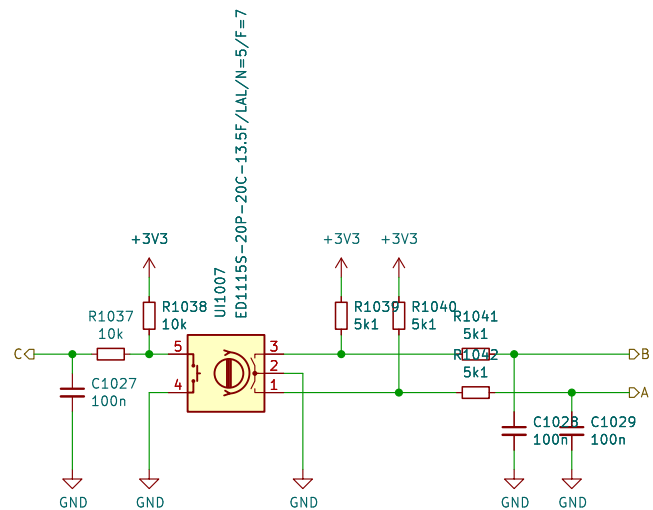
Size: A4

Date:

KiCad E.D.A. 9.0.6

Rev:

Id: 11/11



Sheet: /UI_ENC/ENCODER_3/
File: UI_ENC_FILTER.kicad_sch

Title:

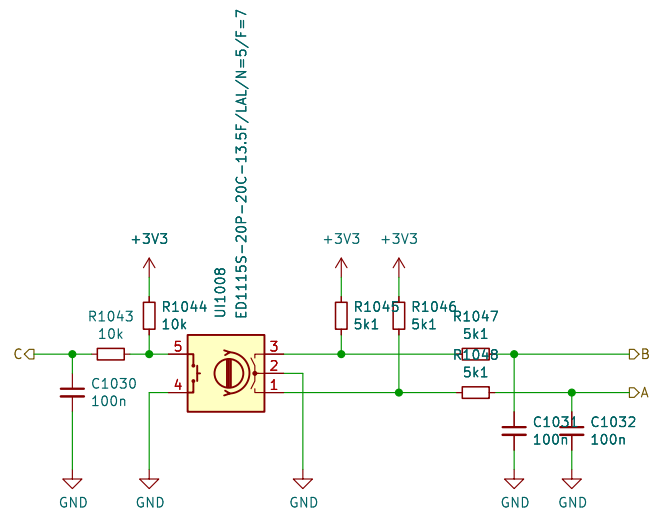
Size: A4

Date:

KiCad E.D.A. 9.0.6

Rev:

Id: 14/11



Sheet: /UI_ENC/ENCODER_1/
File: UI_ENC_FILTER.kicad_sch

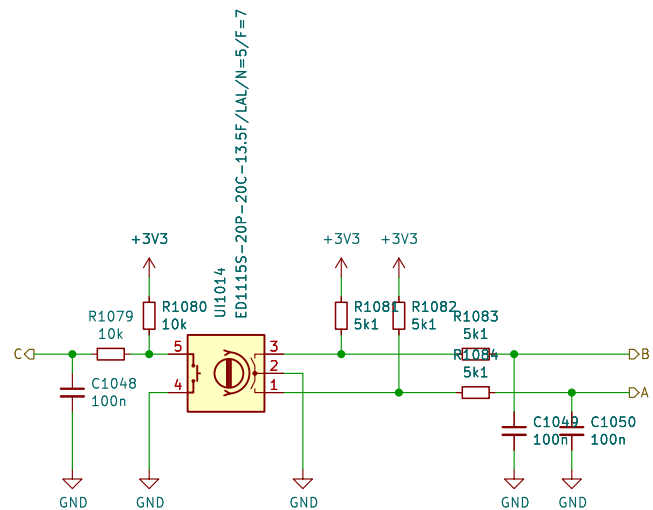
Title:

Size: A4 Date:

KiCad E.D.A. 9.0.6

Rev:

Id: 15/11



Sheet: /UI_ENC/ENCODER_2/
File: UI_ENC_FILTER.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 9.0.6

Rev:

Id: 21/11