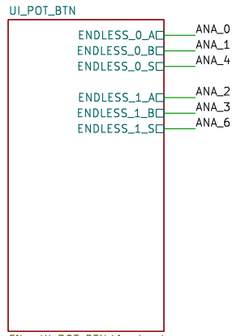
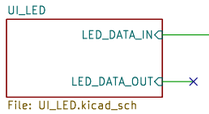
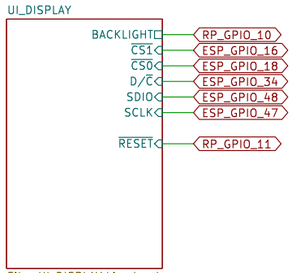
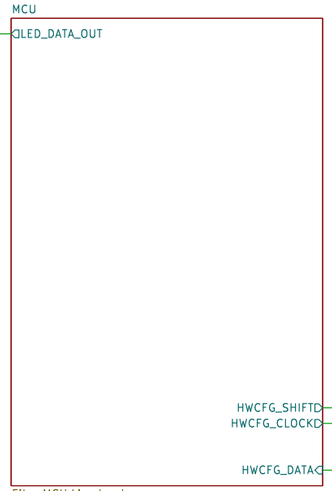
Common Sheets:
500 GRID
600 USB_POWER
700 MCU

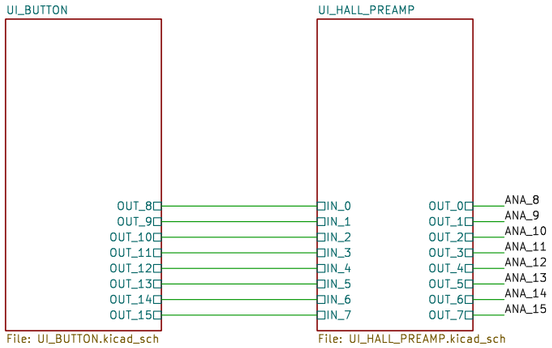
Module Specific:
800 HWCFG
900 LED
1000 UI

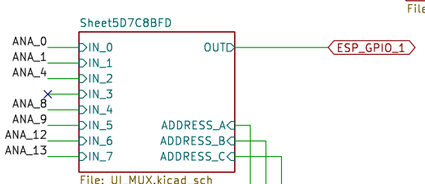


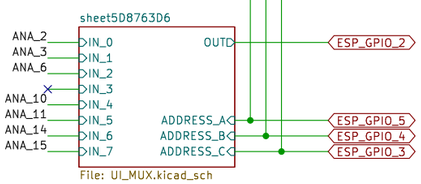


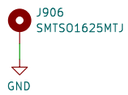


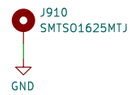


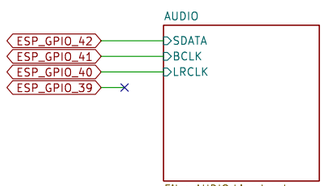












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KiCad E.D.A. 9.0.1		Id: 1/22

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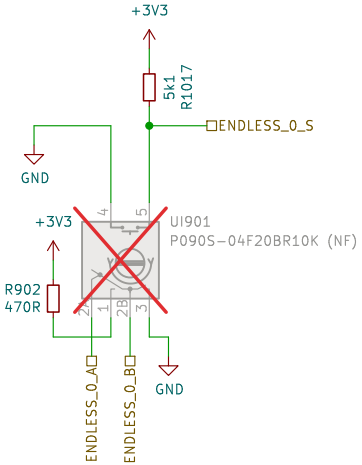

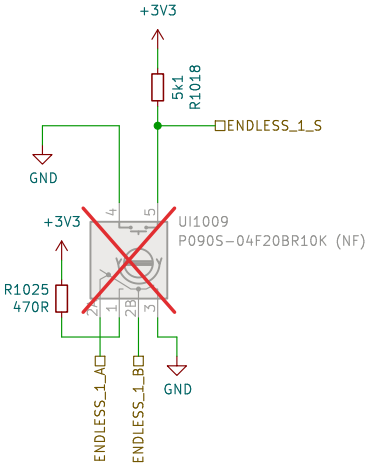

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Simulation:

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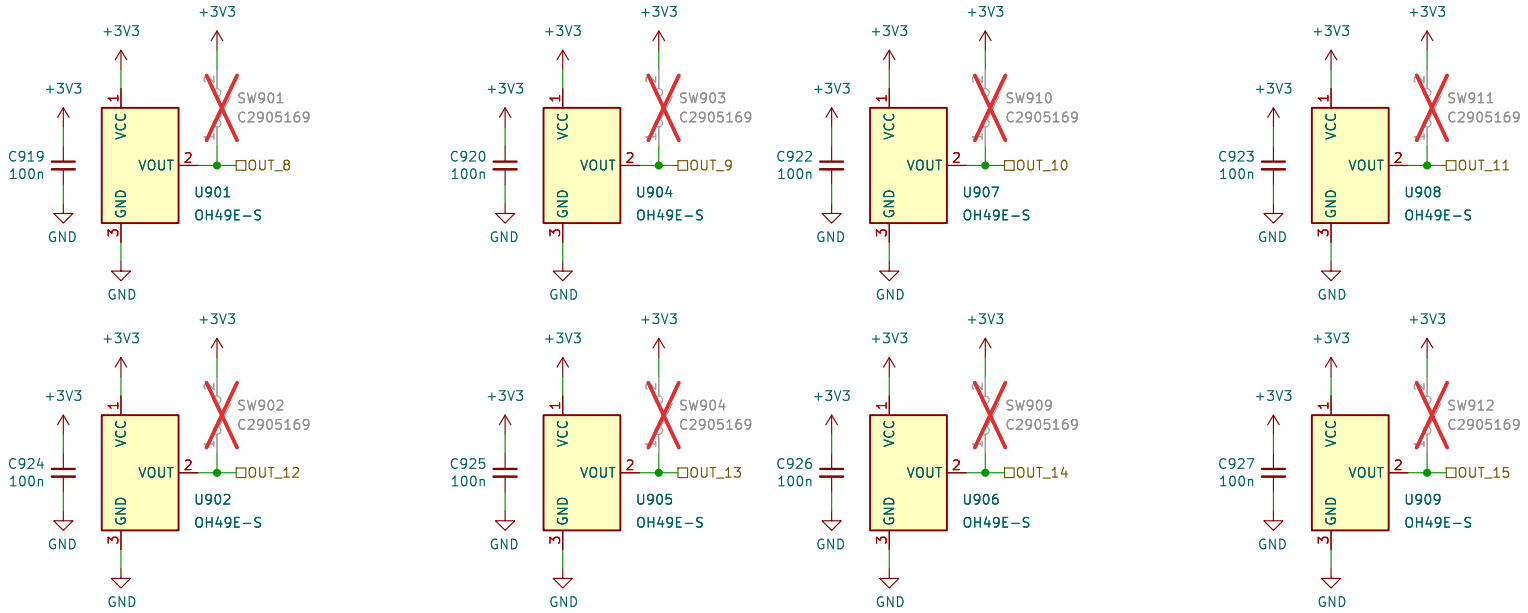

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KiCad E.D.A. 9.0.1 | Id: 3/22

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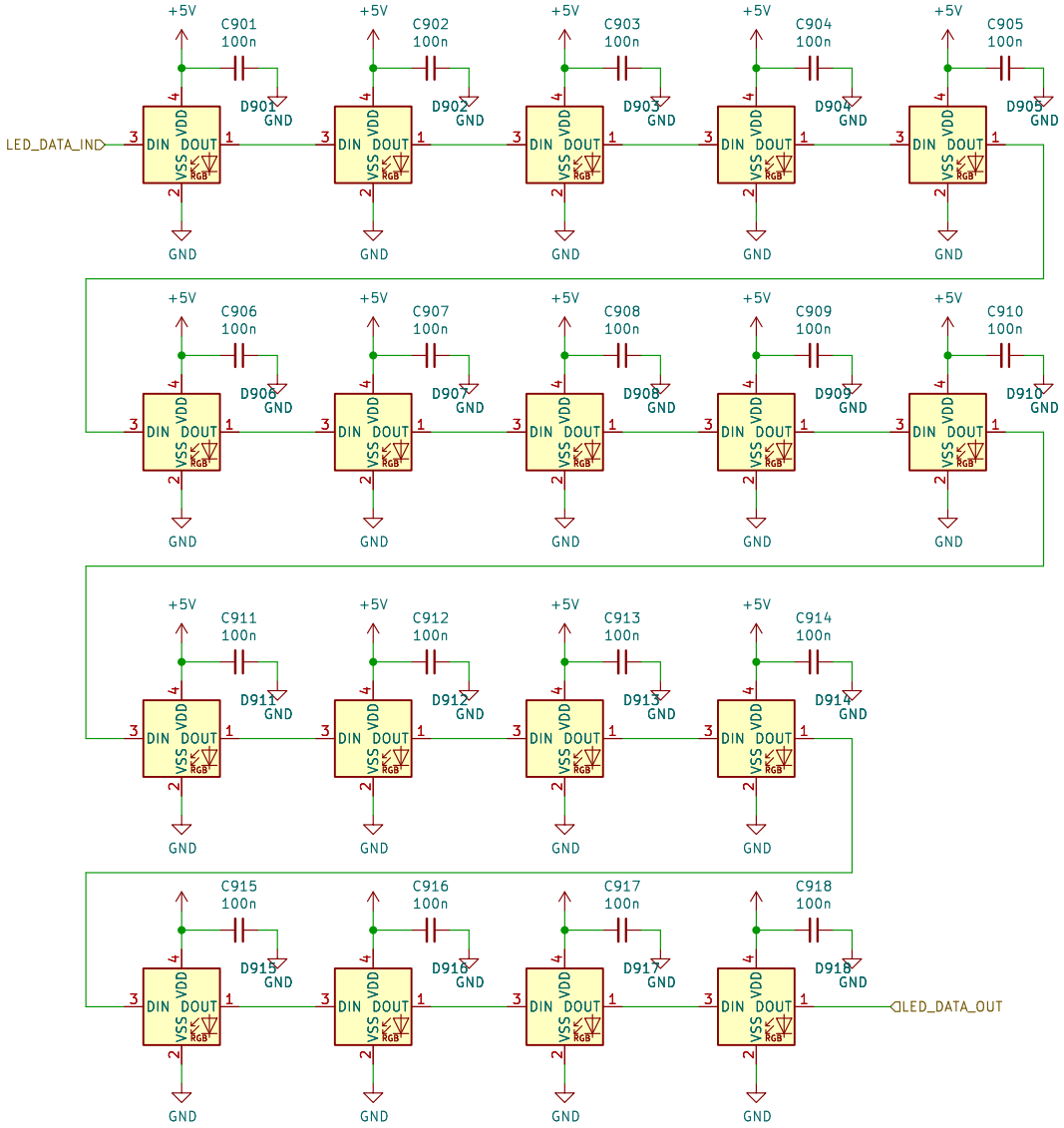

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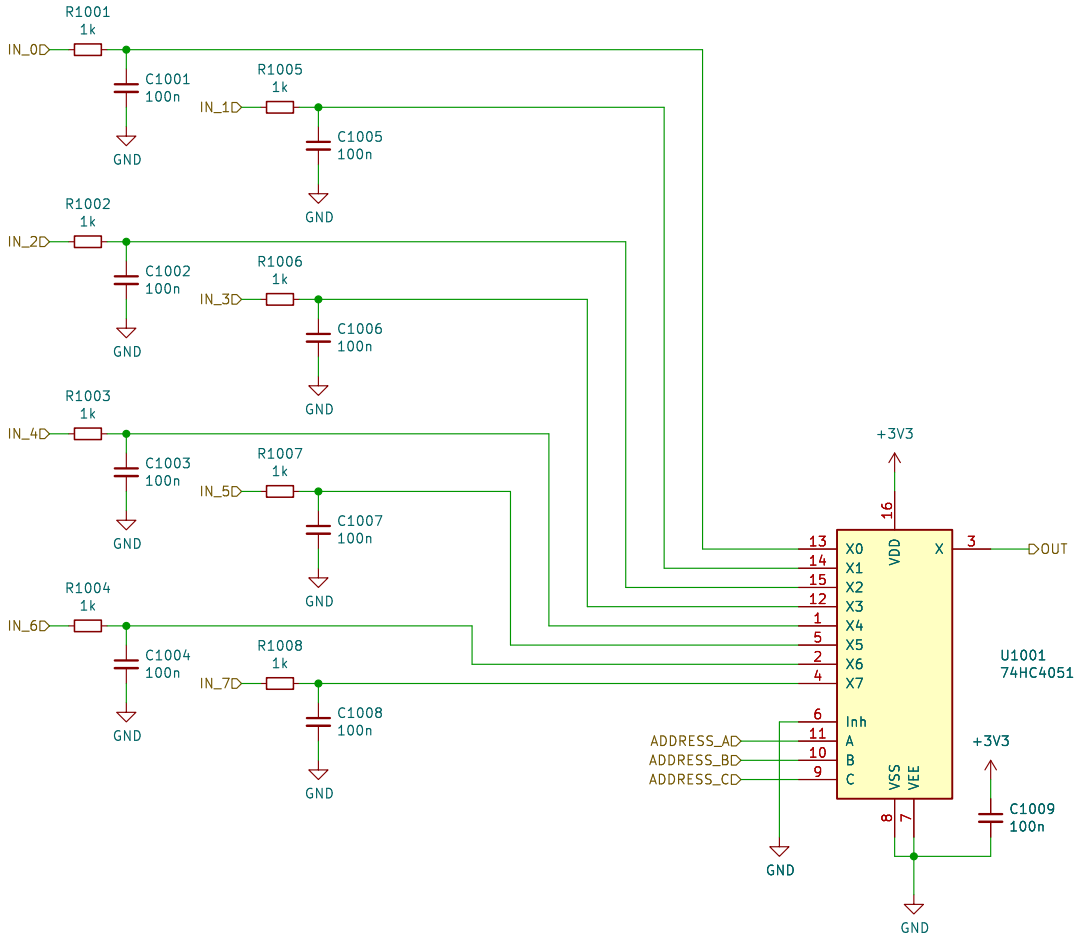

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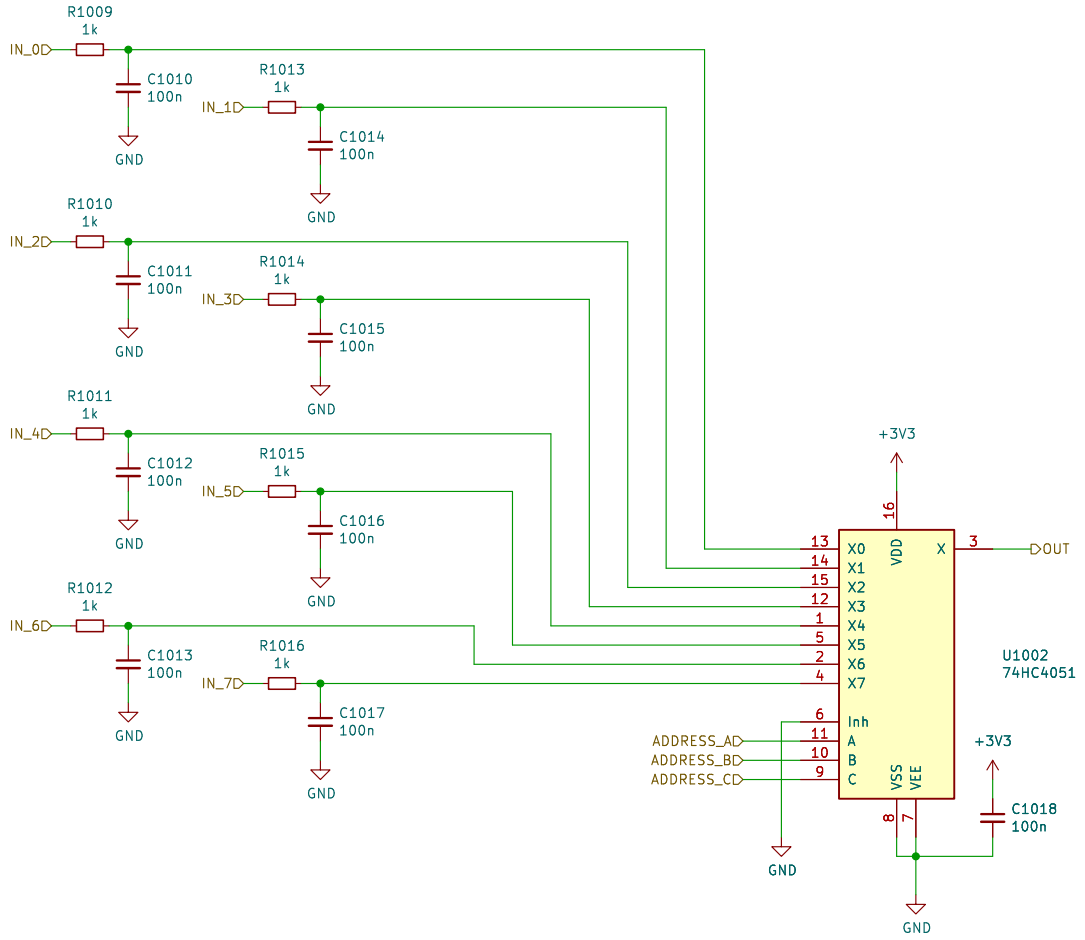

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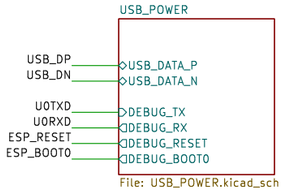
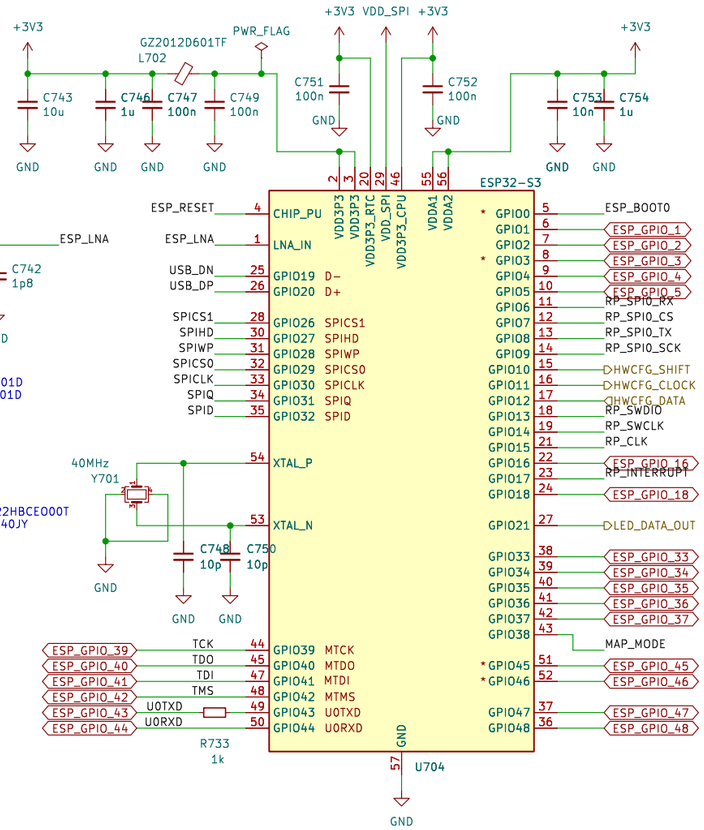
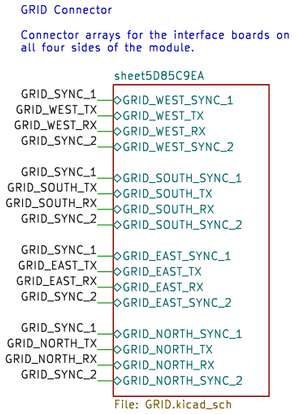
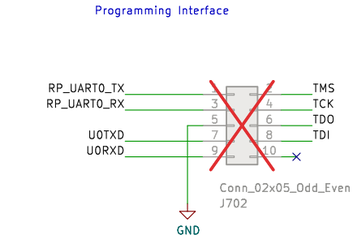
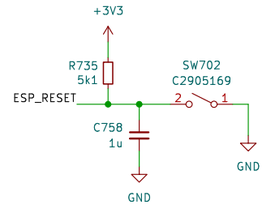
700

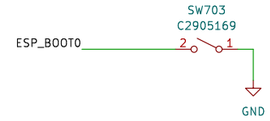


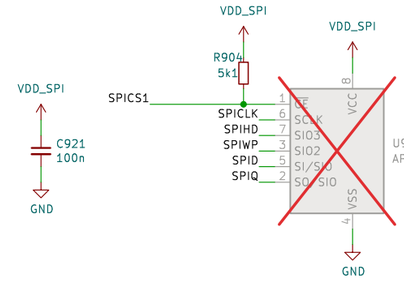


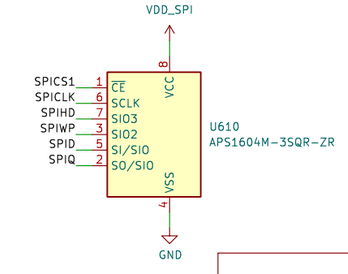


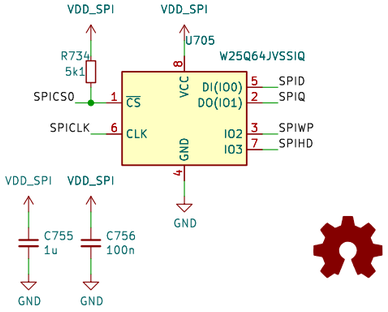












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KiCad E.D.A. 9.0.1		Id: 7/22

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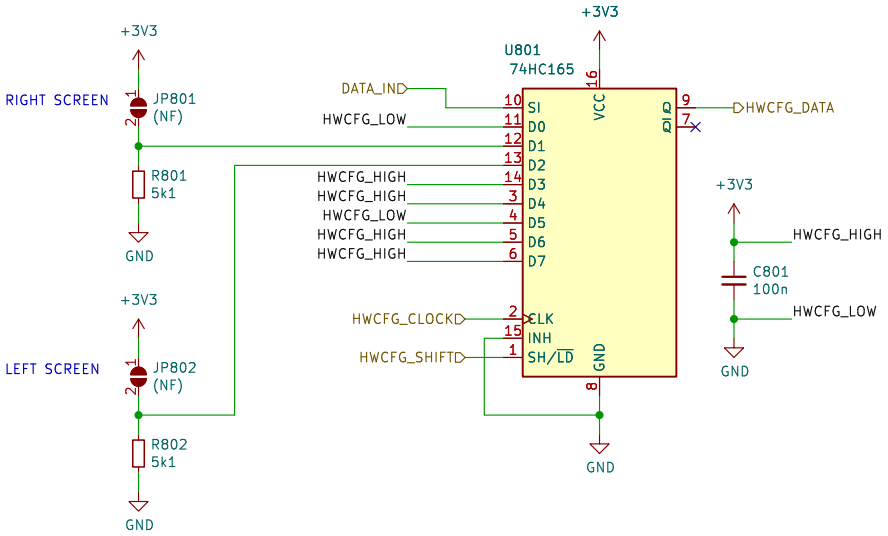

Board Identification

Grid firmware can identify the hardware and the board revision thorugh a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Model Codes (D3–D0):

Po16 0000
Bo16 0001
PBF4 0010
EN16 0011
...

Revision Codes (D7–D4):

RevA 0000
RevB 0001
RevC 0010
RevD 0011
...

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KiCad E.D.A. 9.0.1		Id: 10/22

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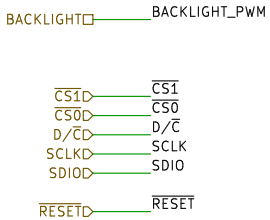

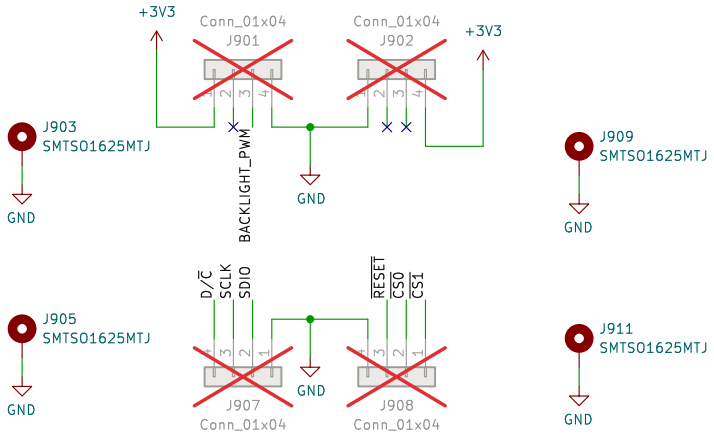

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Title:		
Size: A4	Date:	**Rev:**
KiCad E.D.A. 9.0.1		Id: 11/22

simulation

Sheet: /UI_HALL_PREAMP/
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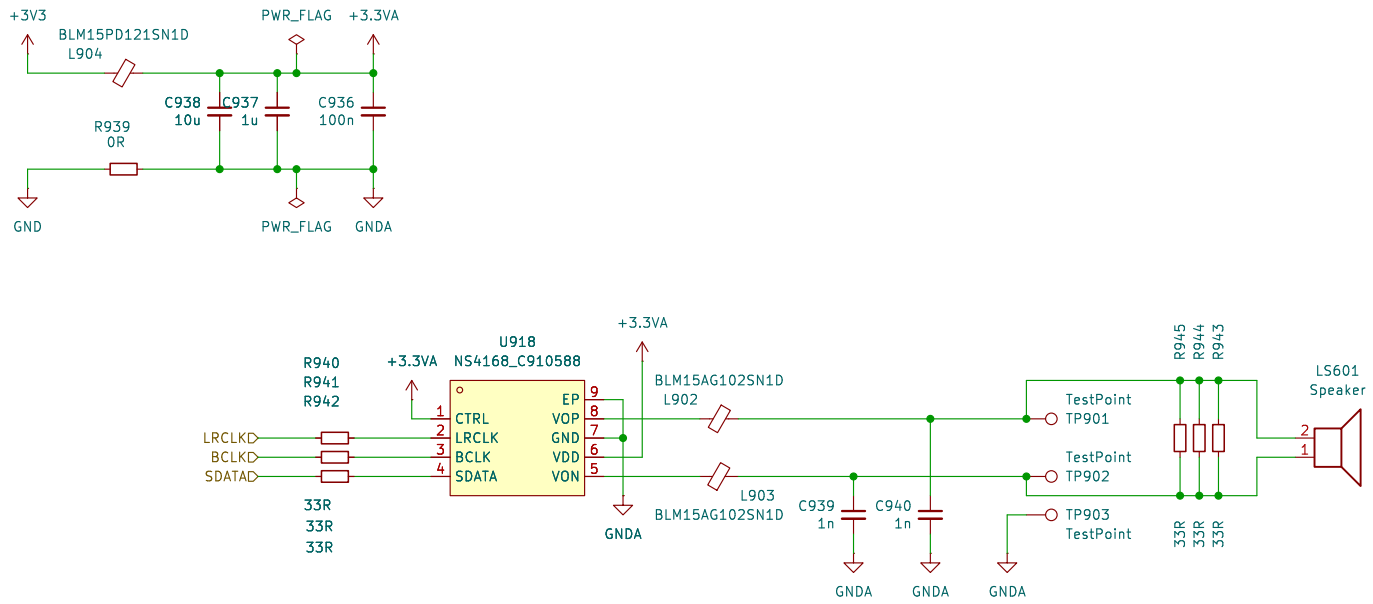
Size: A4	Date:	**Rev:**
KiCad E.D.A. 9.0.1		Id: 12/22



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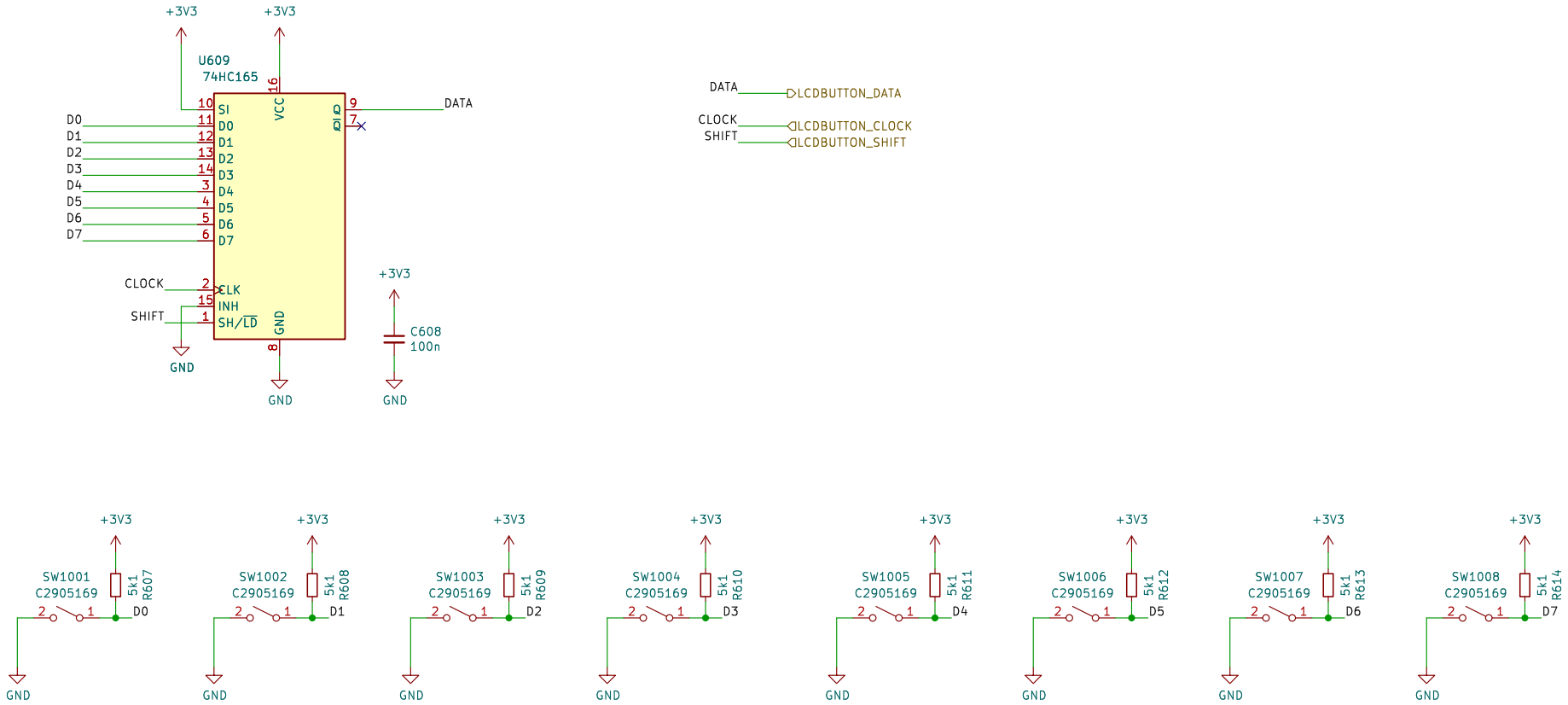
KiCad E.D.A. 9.0.1		Id: 21/22



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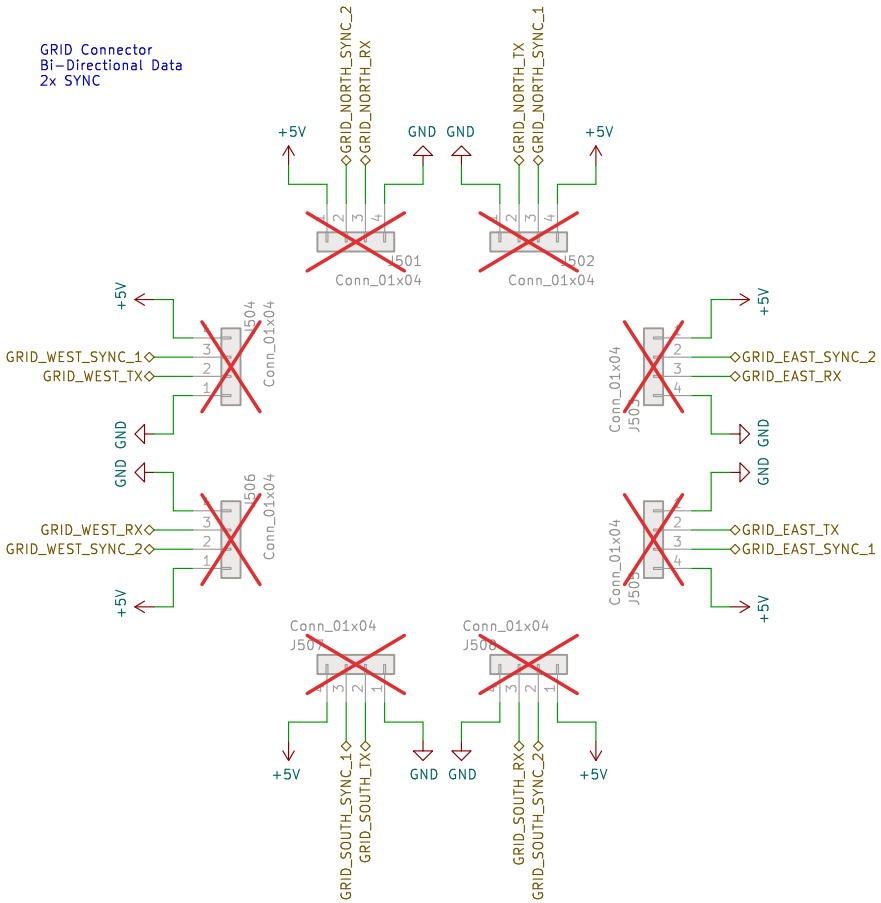
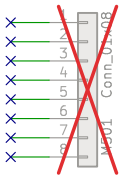
Size: A4	Date:	**Rev:**
KiCad E.D.A. 9.0.1		Id: 22/22

500

GRID Connector
Bi-Directional Data
2x SYNC



Board Mounting Pattern



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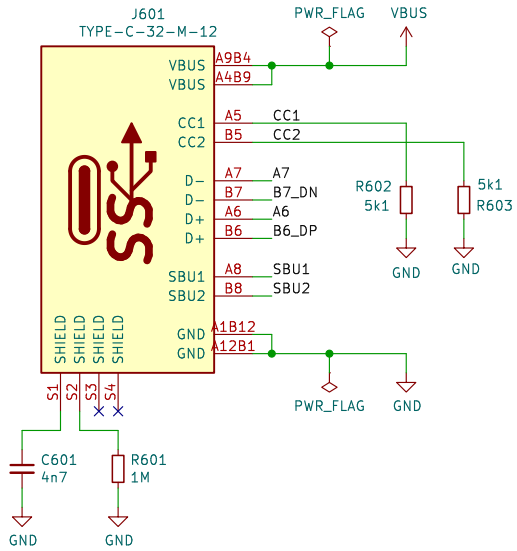
Size: A4	Date:	**Rev:**
KiCad E.D.A. 9.0.1		Id: 8/22

600

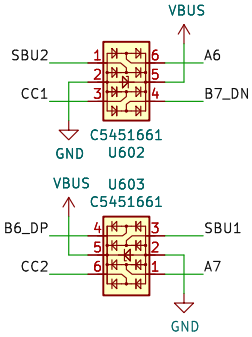
USB Port

USB C upstream facing port configured for 5V 3A power consumption.



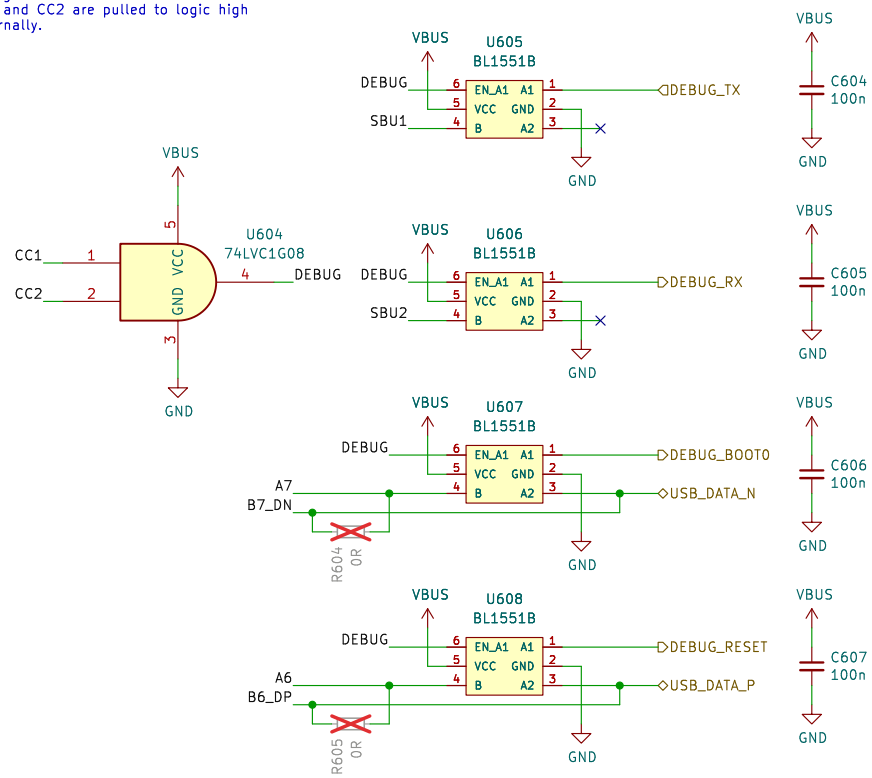
ESD Prot.

ESP protection for all 8 signals externally accessable via the USB C connector.



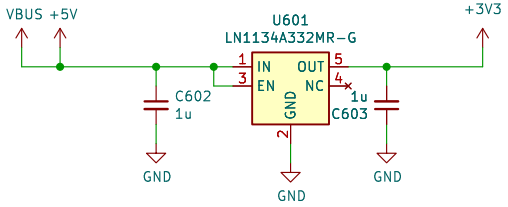
Debug-Mode Multiplexing

Debug.mode is activated when both CC1 and CC2 are pulled to logic high externally.

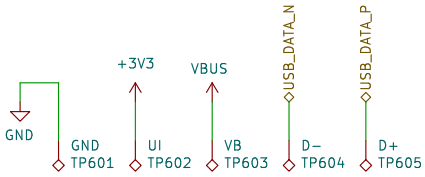


3V3 LDO

LDO regulator for generating the +3V3 power rail for the microcontroller and UI.



Testpoints



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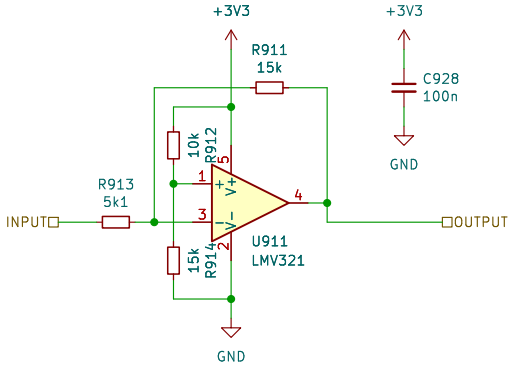
KiCad E.D.A. 9.0.1 | Id: 9/22



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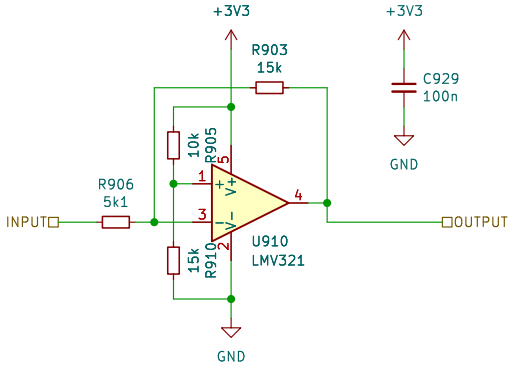
KiCad E.D.A. 9.0.1		Id: 13/22



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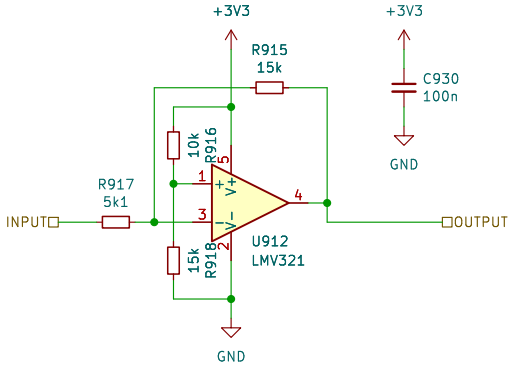
KiCad E.D.A. 9.0.1		Id: 14/22



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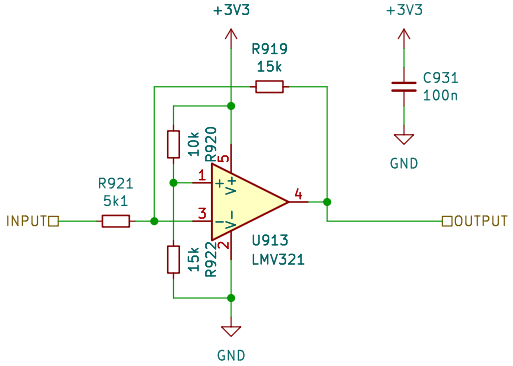

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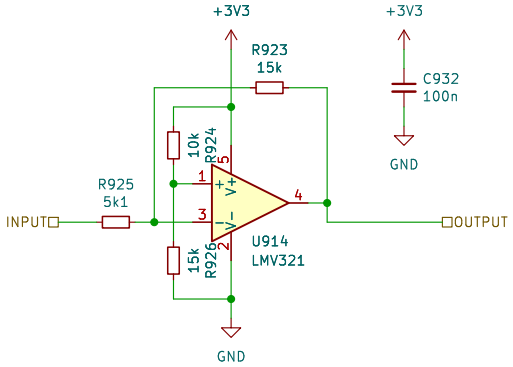

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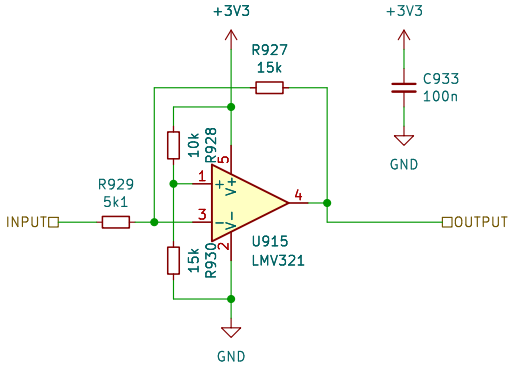

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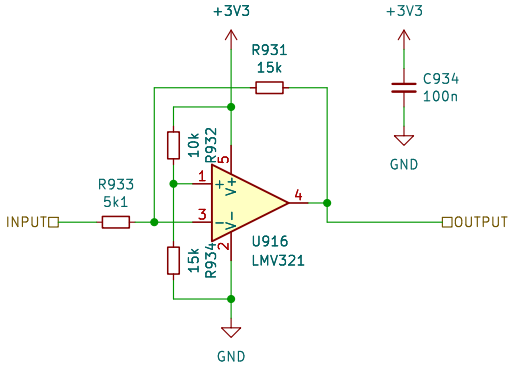

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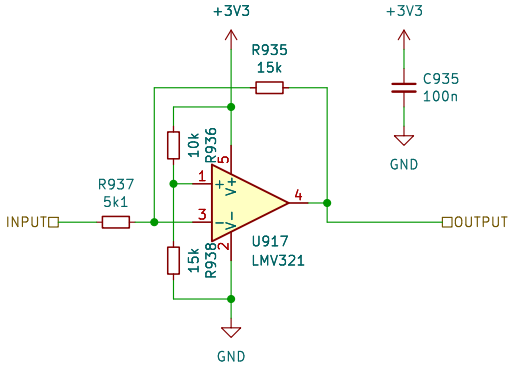

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