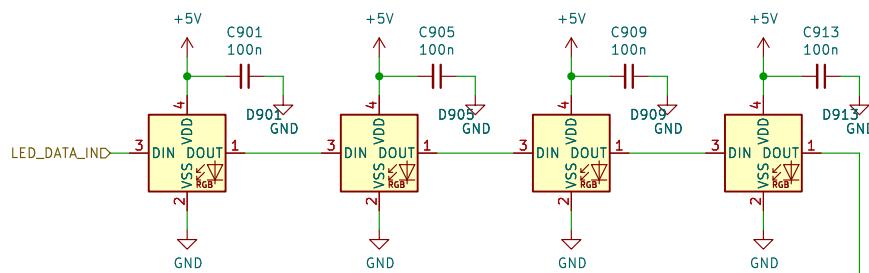
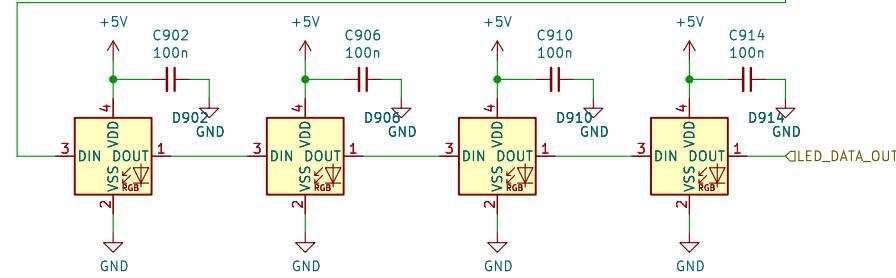


# 900

A



B



C

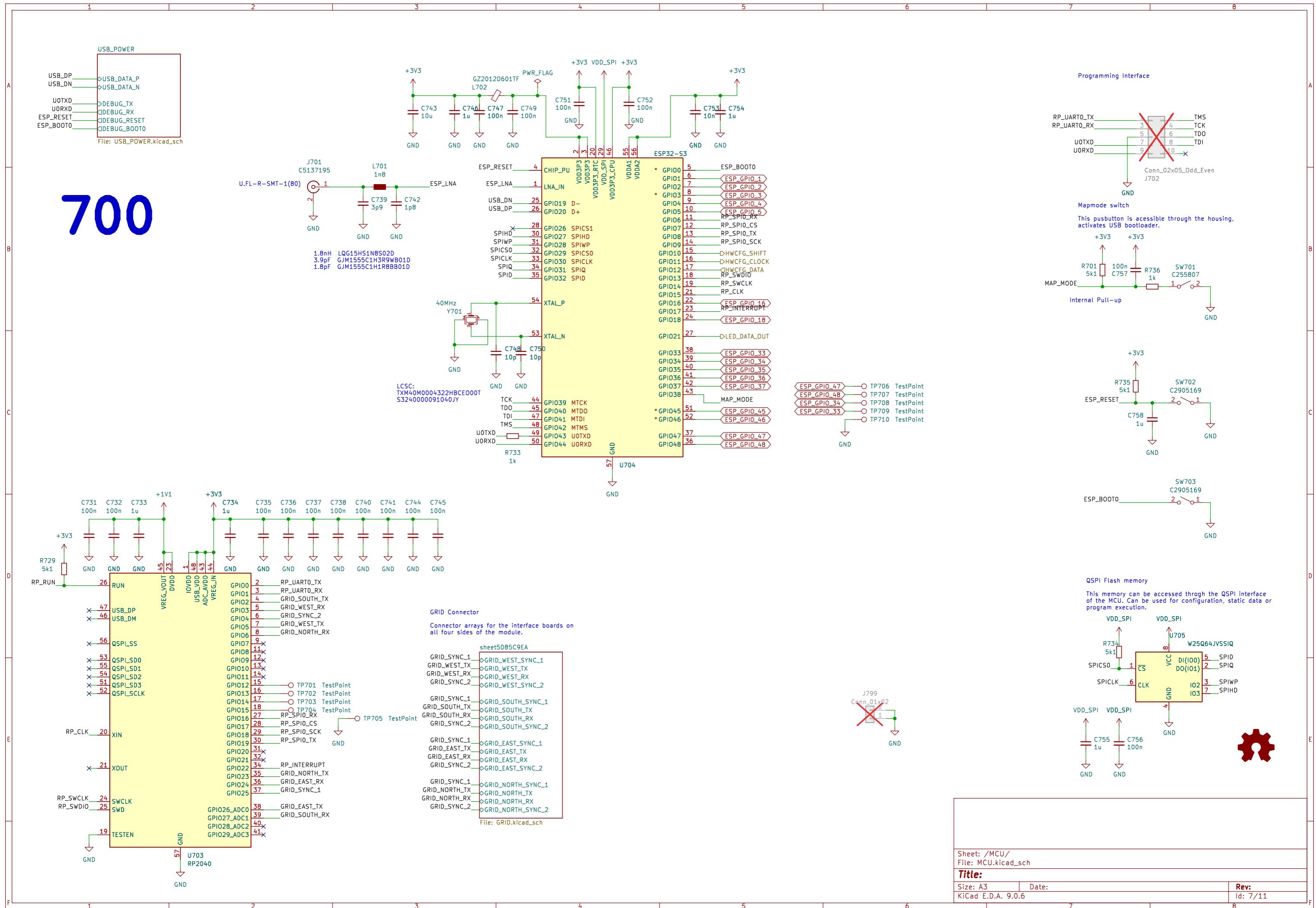
D

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Size: A4 | Date:  
KiCad E.D.A. 9.0.6

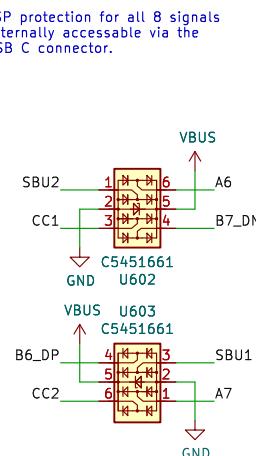
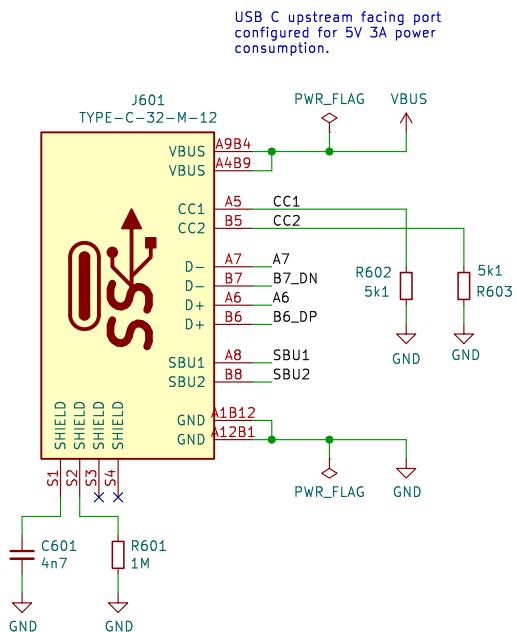
**Rev:**  
Id: 4/11



**600**

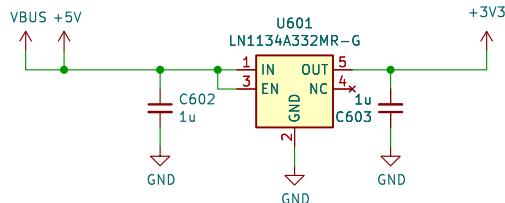
## USB Port

## ESD Prot.

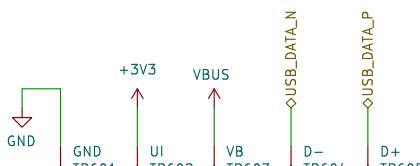


## 3V3 LDO

LDO regulator for generating the +3V3 power rail for the microcontroller and UI.

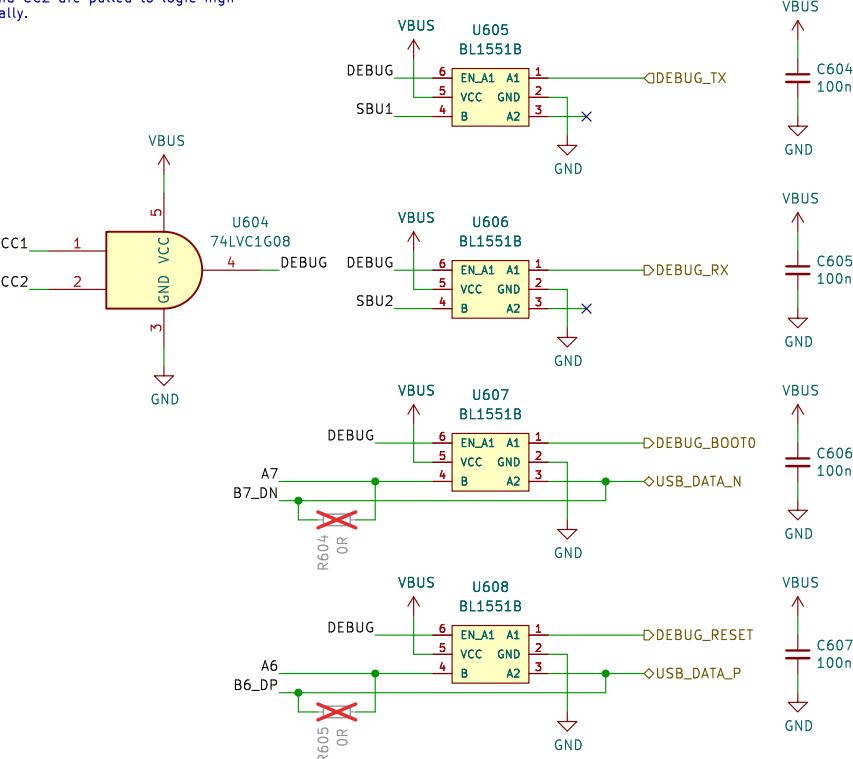


## Testpoints



## Debug-Mode Multiplexing

Debug.mode is activated when both CC1 and CC2 are pulled to logic high externally.



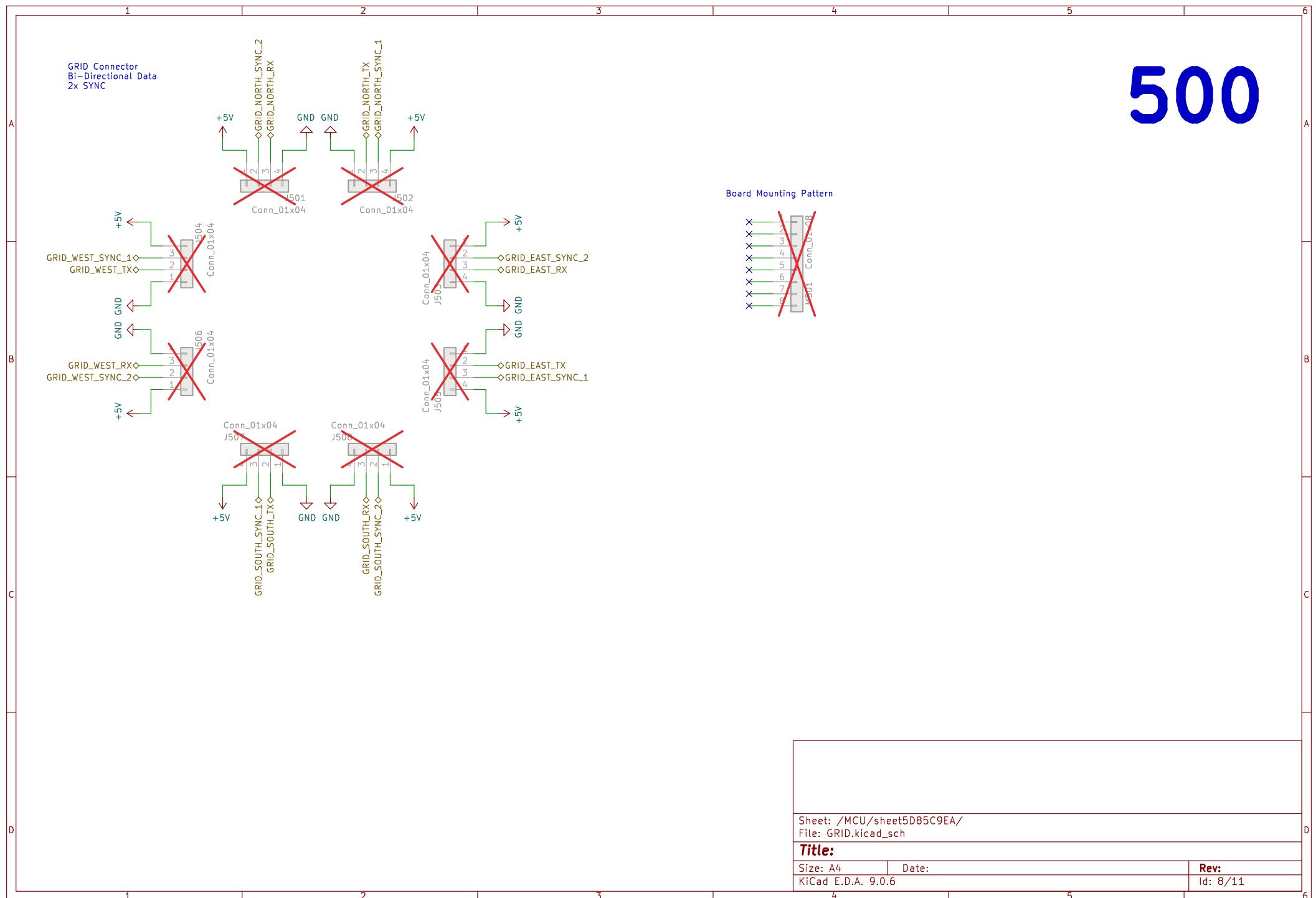
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Size: A4 Date:  
KiCad E.D.A. 9.0.6

Rev:  
Id: 2/11

500



# 800

A

A

B

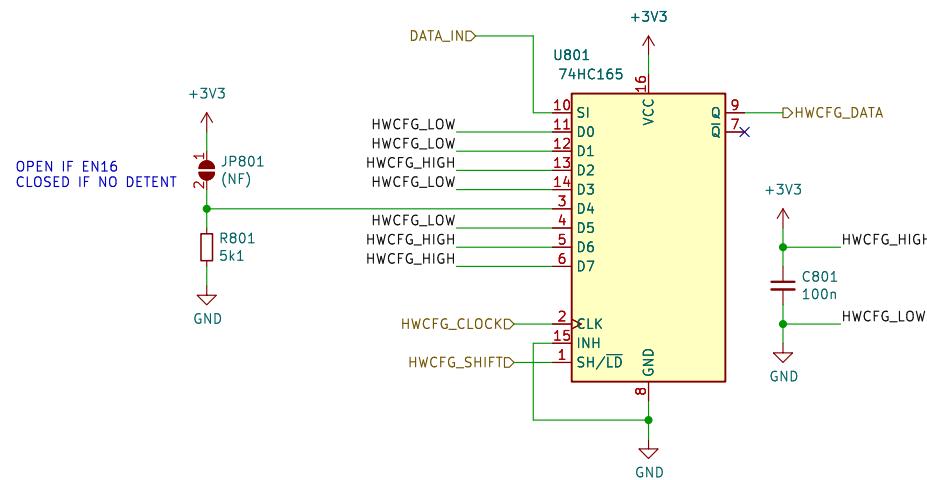
B

C

C

D

D



#### Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or soldered configuration jumpers.

4b' Model + 4b' Revision + nb' Reserved (Multiple shift registers)

D0: MODEL (LSB)

D1: MODEL

D2: MODEL

D3: MODEL (MSB)

D4: REVISION (LSB)

D5: REVISION

D6: REVISION

D7: REVISION (MSB)

#### Model Codes (D3–D0):

Po16 0000

Bo16 0001

PBF4 0010

EN16 0011

...

#### Revision Codes (D7–D4):

RevA 0000

RevB 0001

RevC 0010

RevD 0011

...

Sheet: /HWCFG/  
File: HWCFG.kicad\_sch

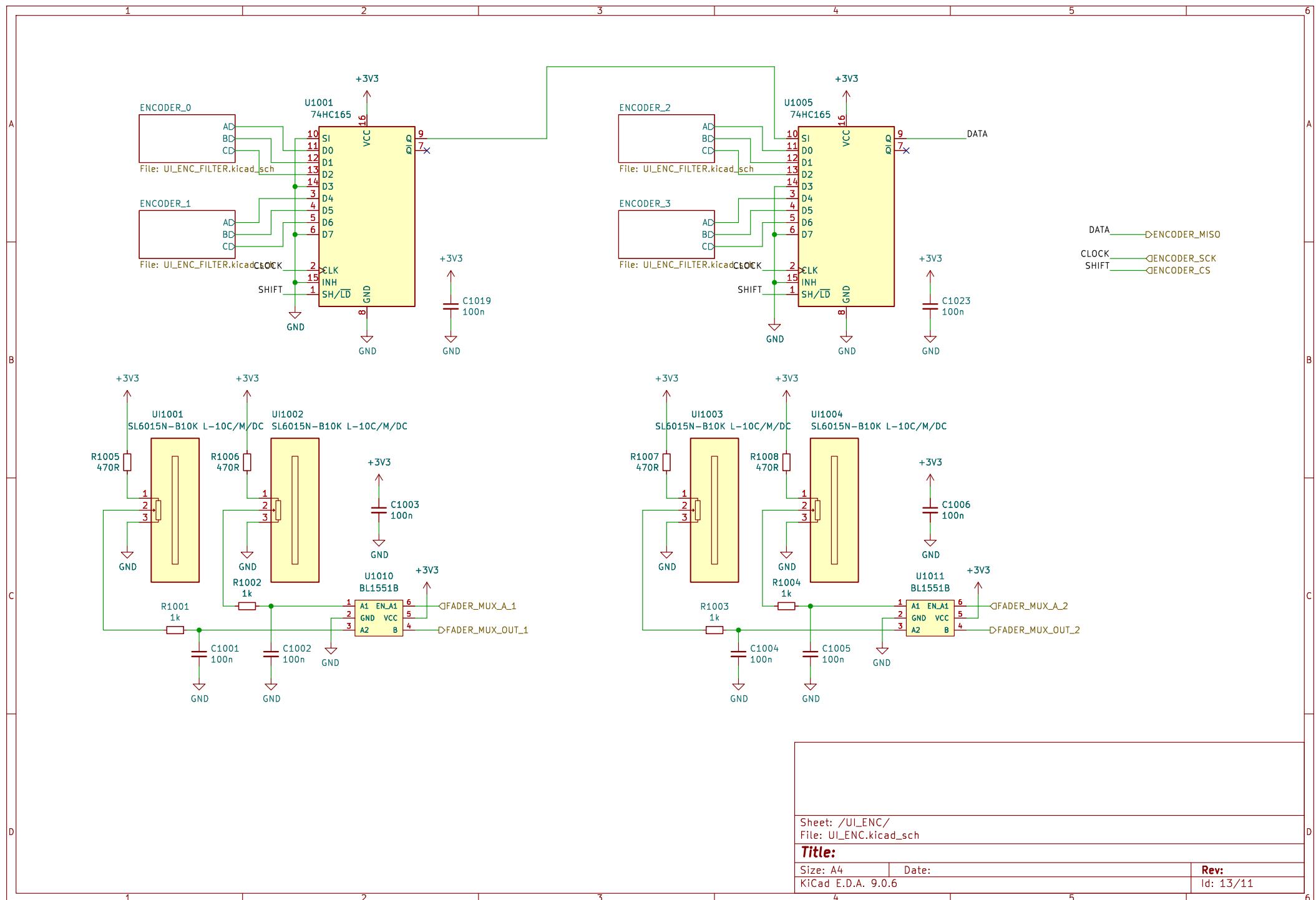
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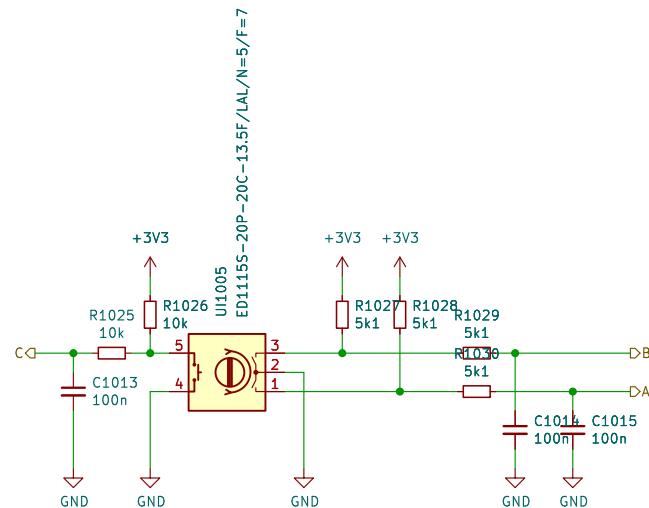
Size: A4 | Date:

KiCad E.D.A. 9.0.6

Rev:

Id: 10/11



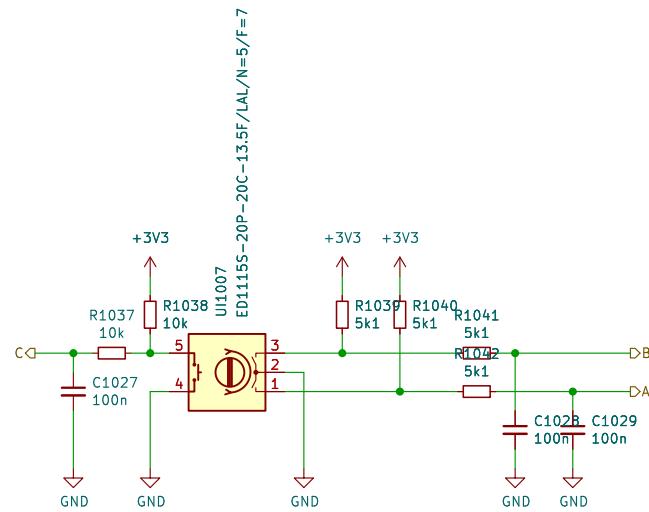


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Size: A4 Date:  
KiCad E.D.A. 9.0.6

**Rev:**  
Id: 11/11

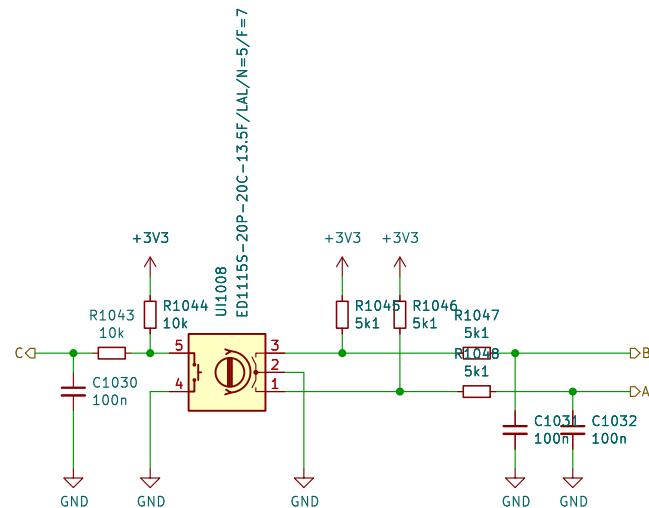


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**Title:**

Size: A4 Date:  
KiCad E.D.A. 9.0.6

**Rev:**  
Id: 14/11

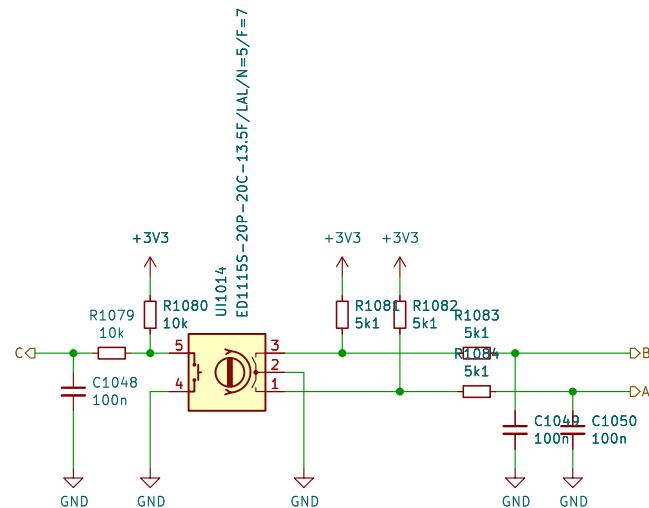


Sheet: /UI\_ENC/ENCODER\_1/  
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**Title:**

Size: A4 Date:  
KiCad E.D.A. 9.0.6

**Rev:**  
Id: 15/11



Sheet: /UI\_ENC/ENCODER\_2/  
File: UI\_ENC\_FILTER.kicad\_sch

**Title:**

Size: A4      Date:  
KiCad E.D.A. 9.0.6

**Rev:**  
Id: 21/11