

A

B

C

D

E

F

A

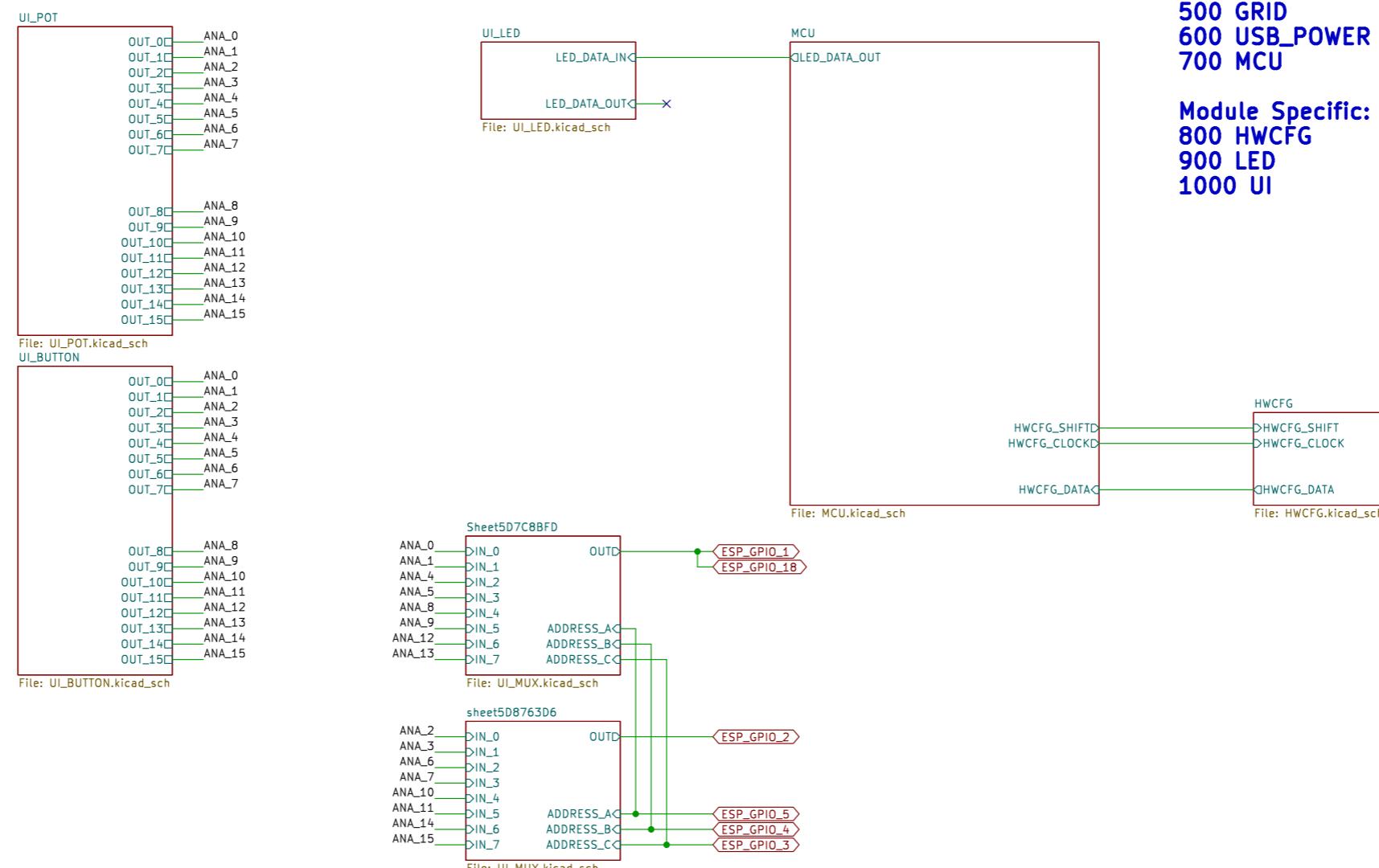
B

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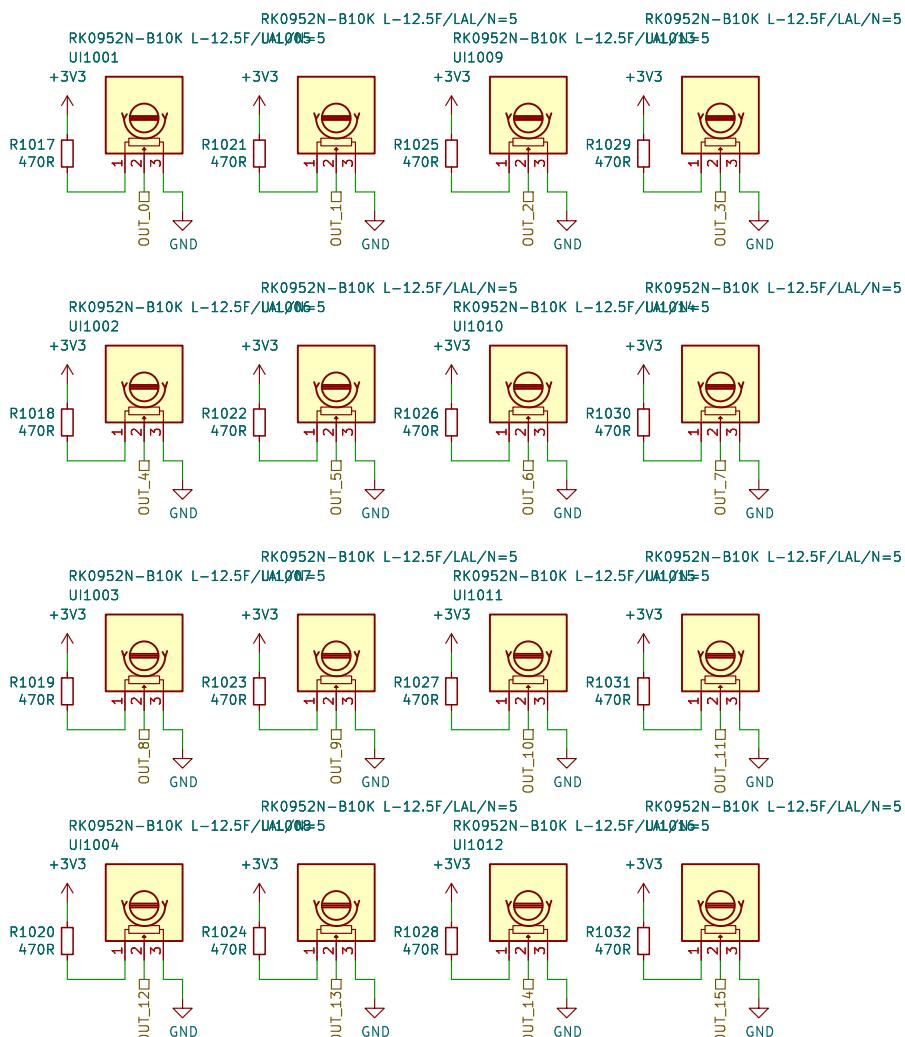
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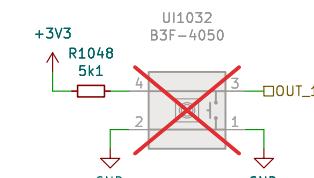
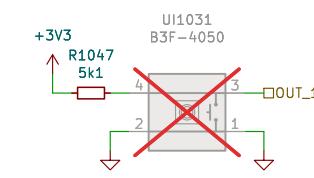
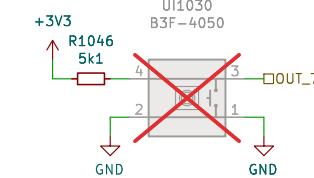
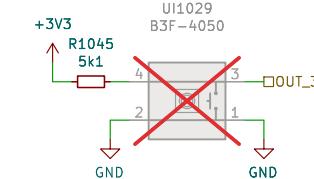
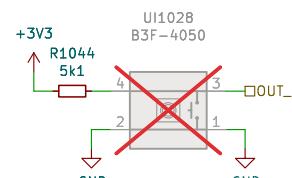
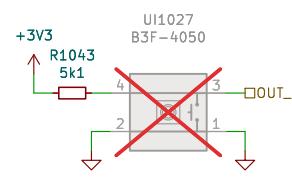
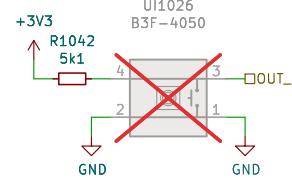
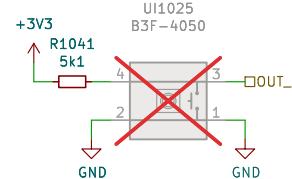
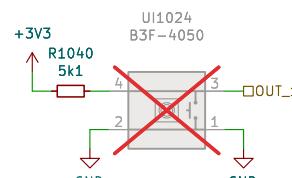
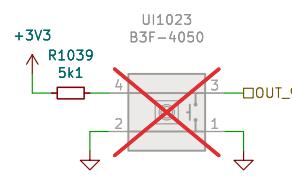
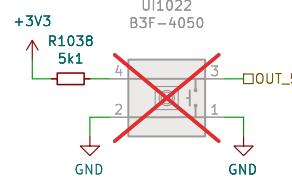
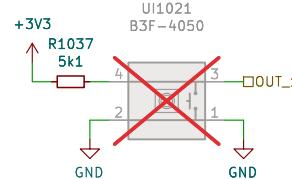
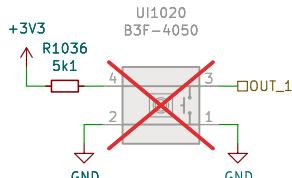
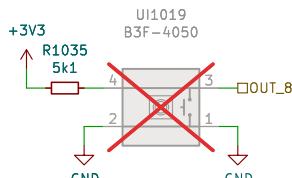
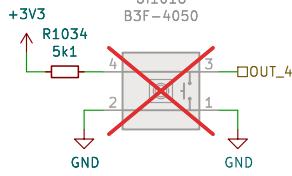
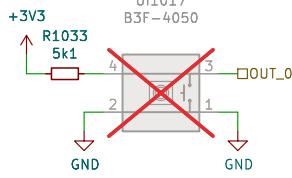
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Simulation:



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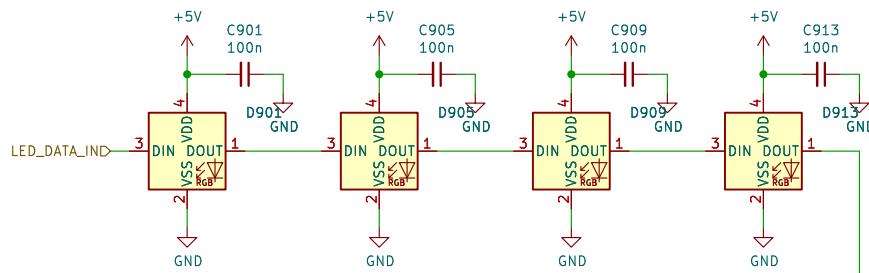
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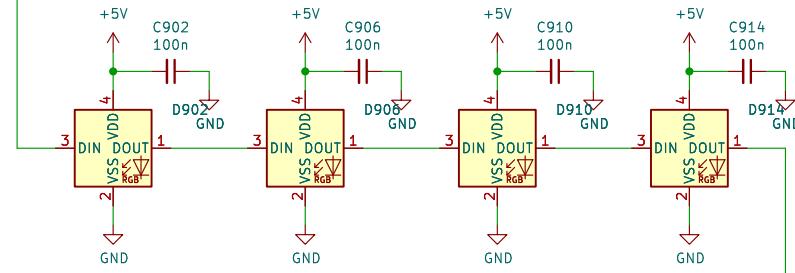
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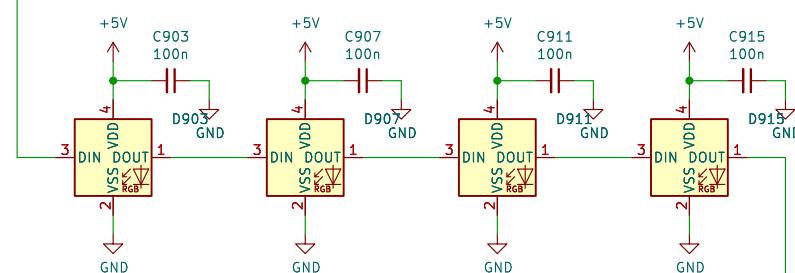
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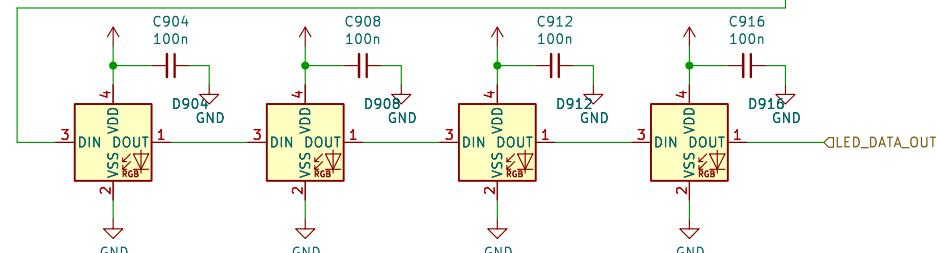
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C



D



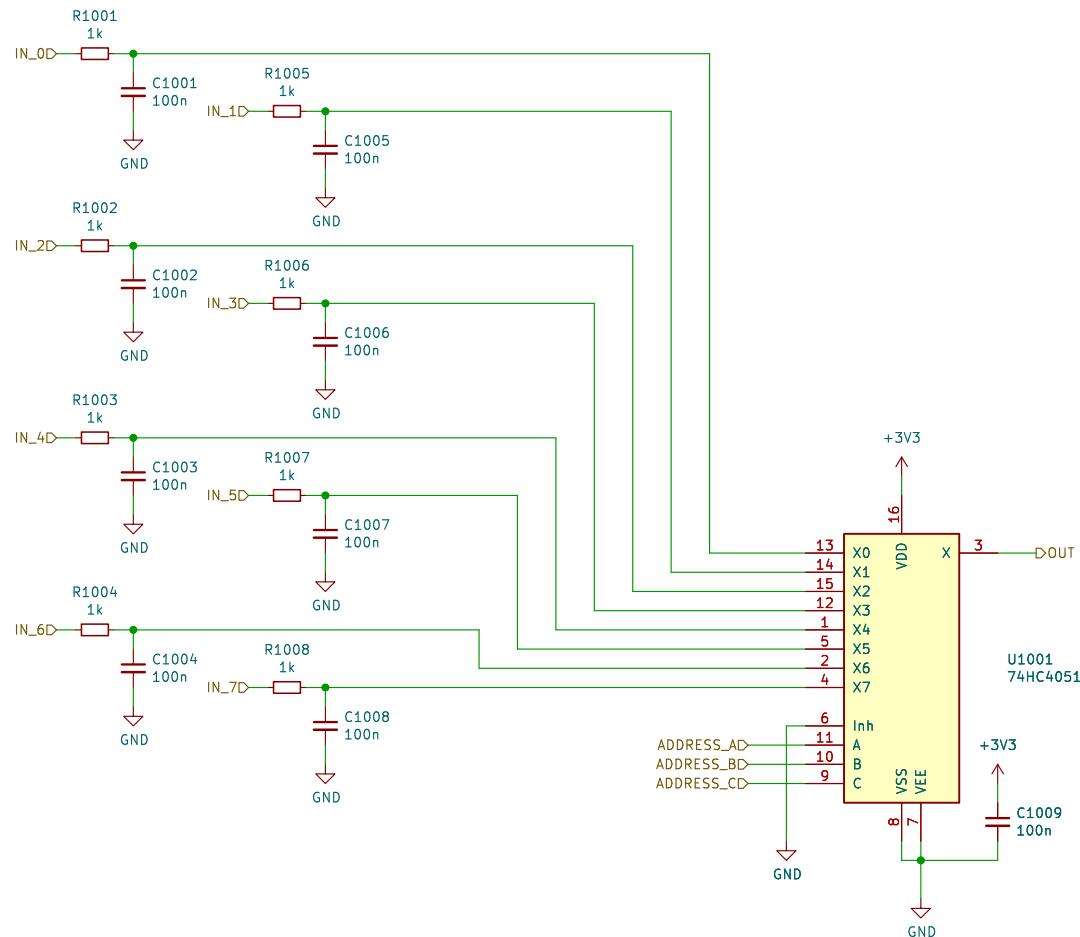
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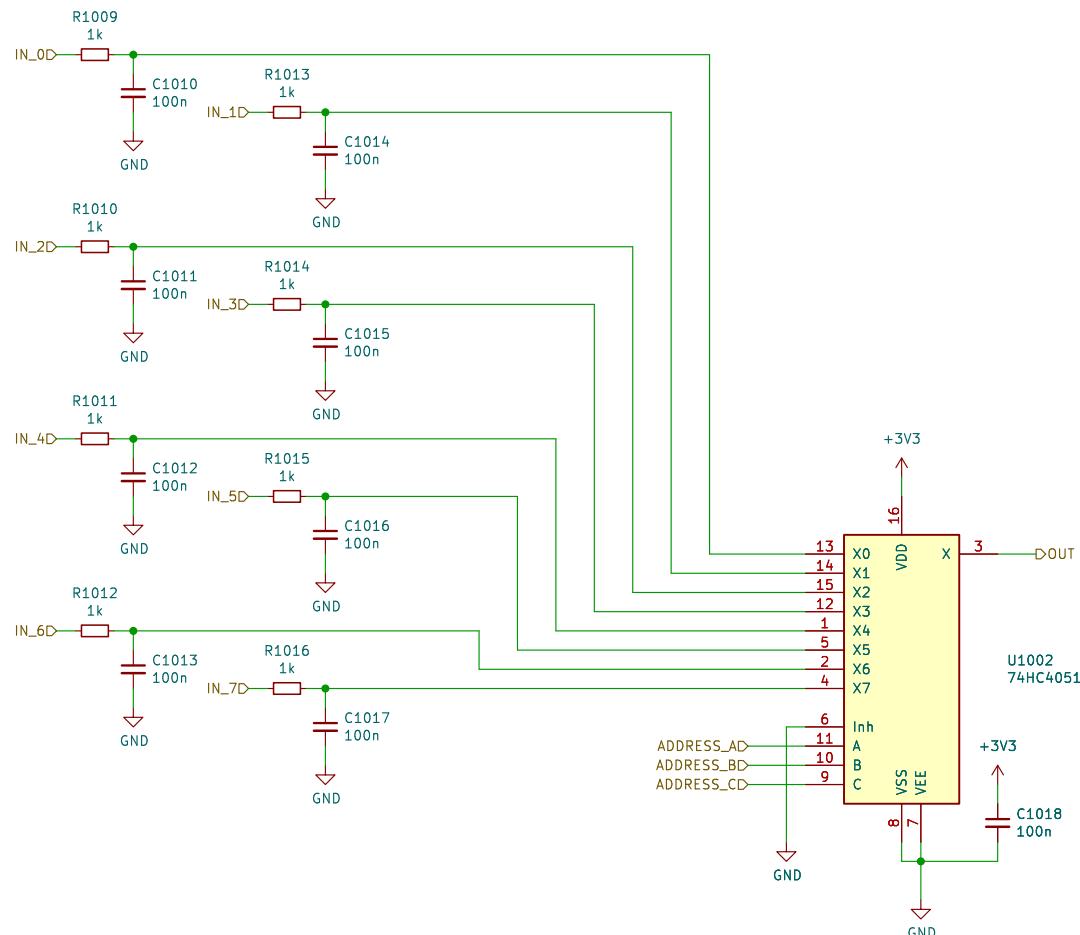
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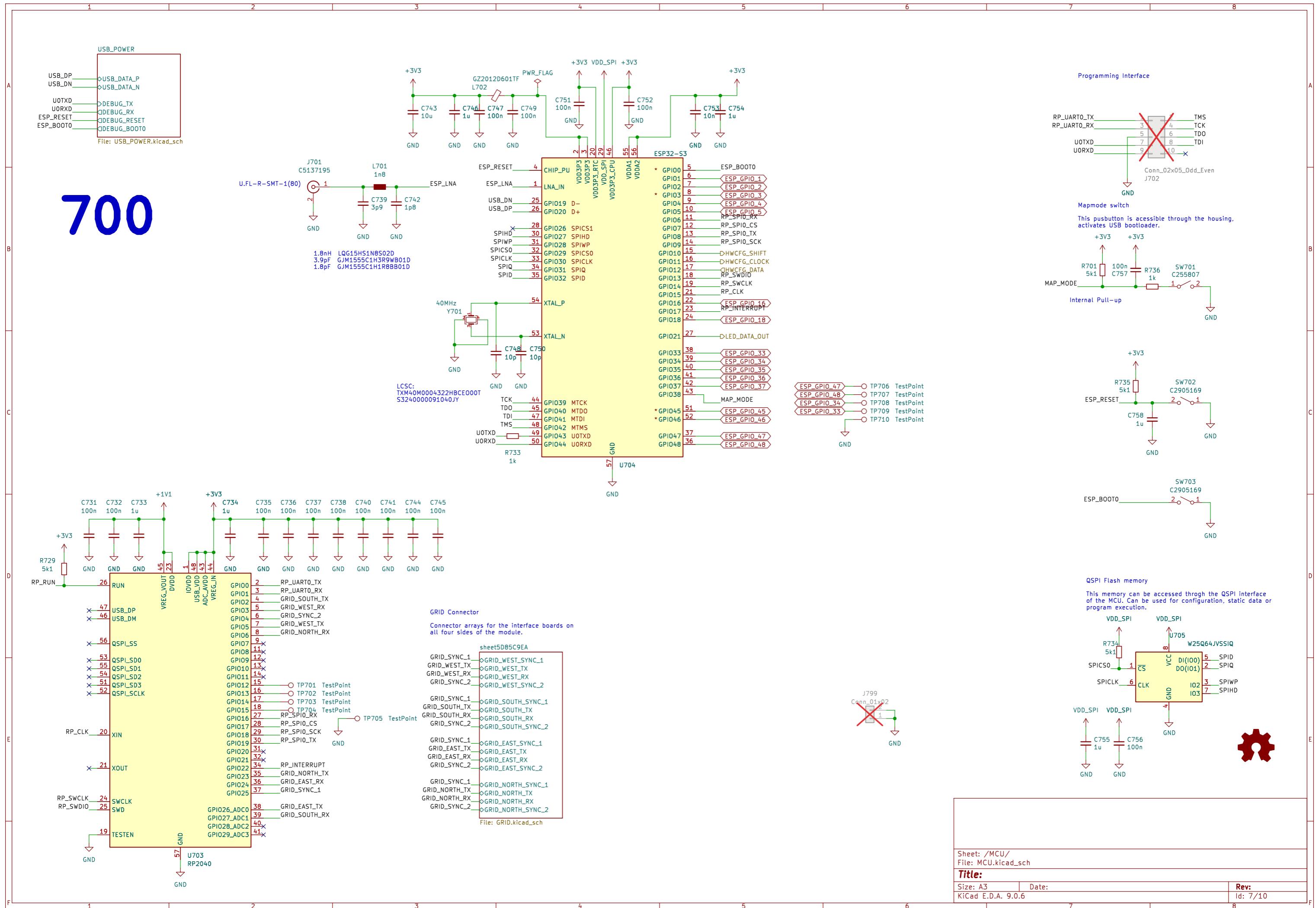


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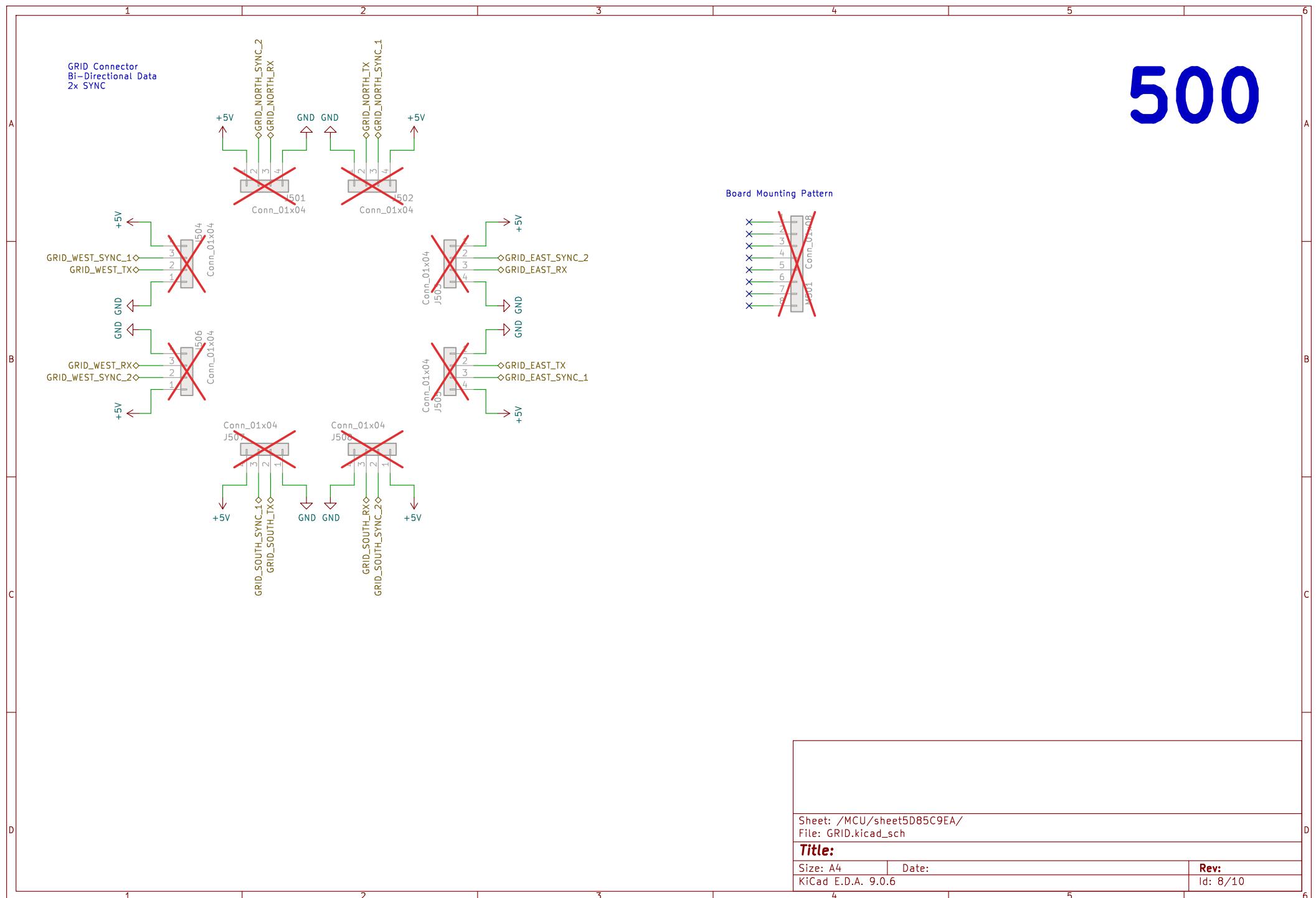
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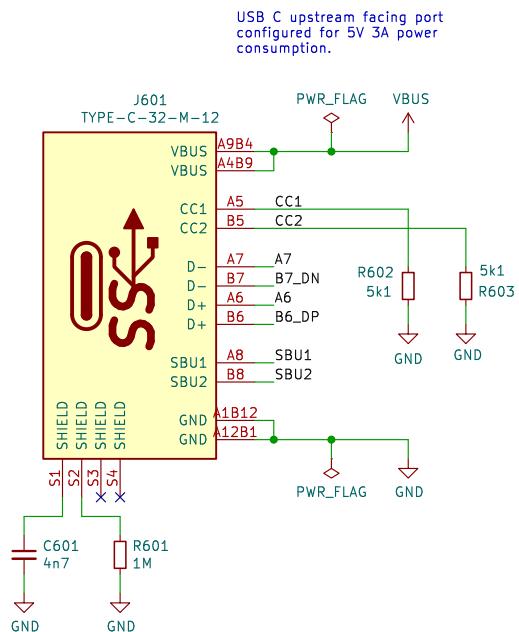
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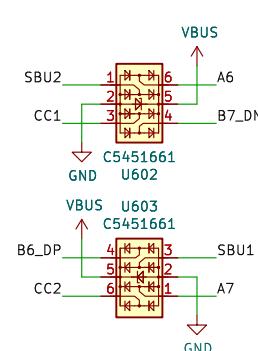
600

USB Port

ESD Prot.

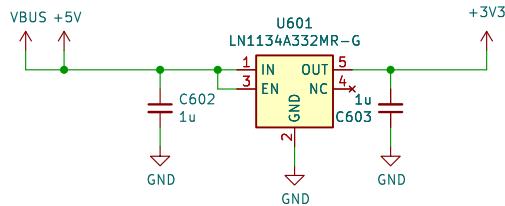


ESD protection for all 8 signals externally accessible via the USB C connector.

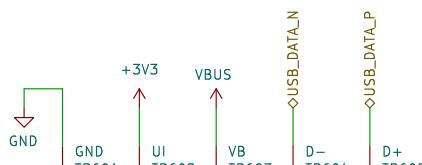


3V3 LDO

LDO regulator for generating the +3V3 power rail for the microcontroller and UI.

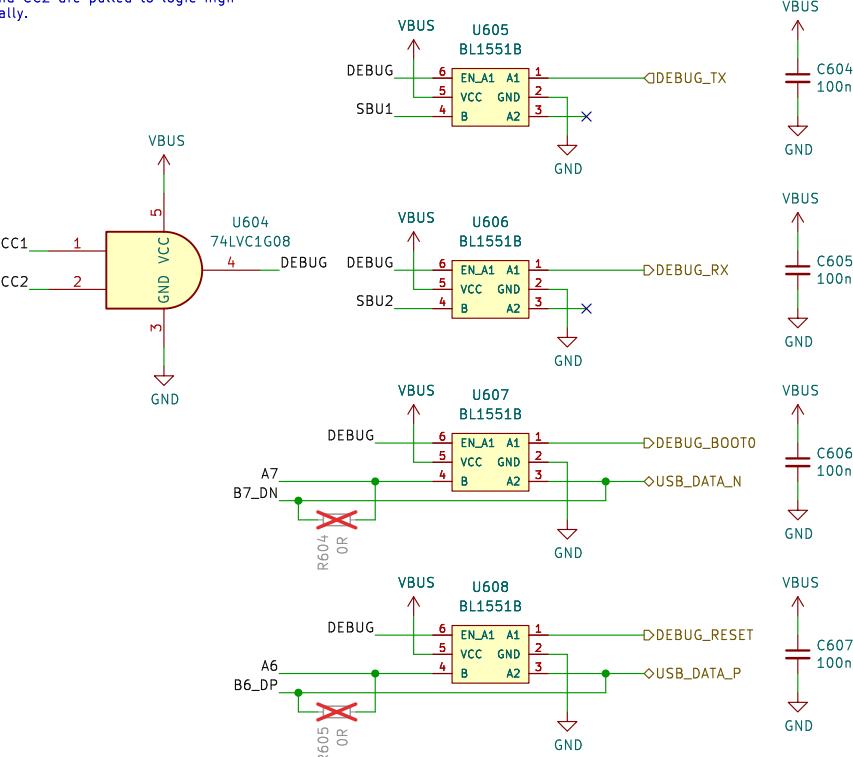


Testpoints



Debug-Mode Multiplexing

Debug.mode is activated when both CC1 and CC2 are pulled to logic high externally.



Sheet: /MCU/USB_POWER/
File: USB_POWER.kicad_sch

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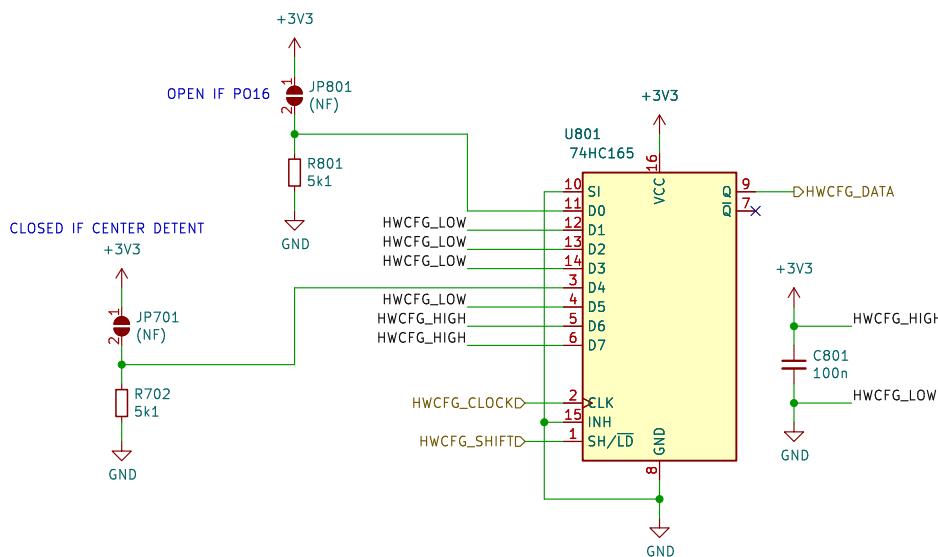
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D



Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or soldered configuration jumpers.

4b' Model + 4b' Revision + nb' Reserved (Multiple shift registers)

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Model Codes (D3–D0):

Po16 0000
Bo16 0001
PBF4 0010
EN16 0011
...

Revision Codes (D7–D4):

RevA 0000
RevB 0001
RevC 0010
RevD 0011
...

Sheet: /HWCFG/
File: HWCFG.kicad_sch

Title:

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Id: 10/10