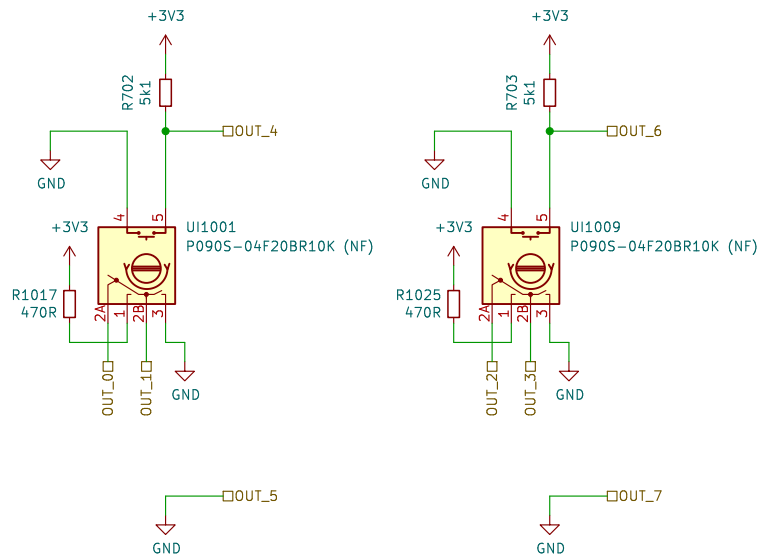


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Sheet: /UI\_POT/  
File: UI\_POT.kicad\_sch

**Title:**

Size: A4

Date:

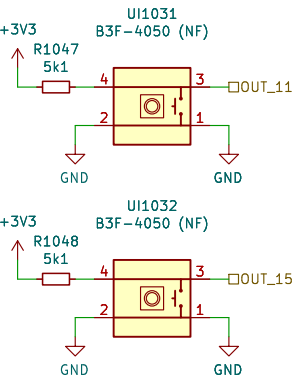
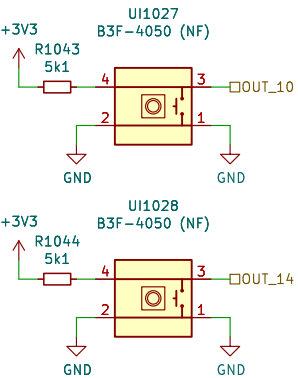
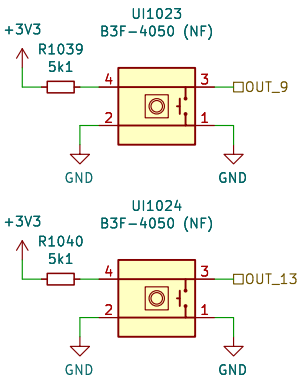
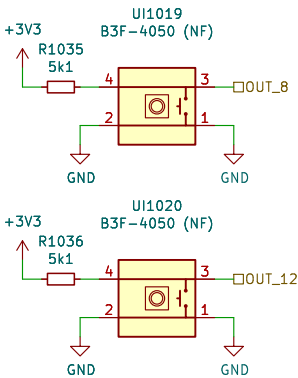
**Rev:**

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Id: 2/10

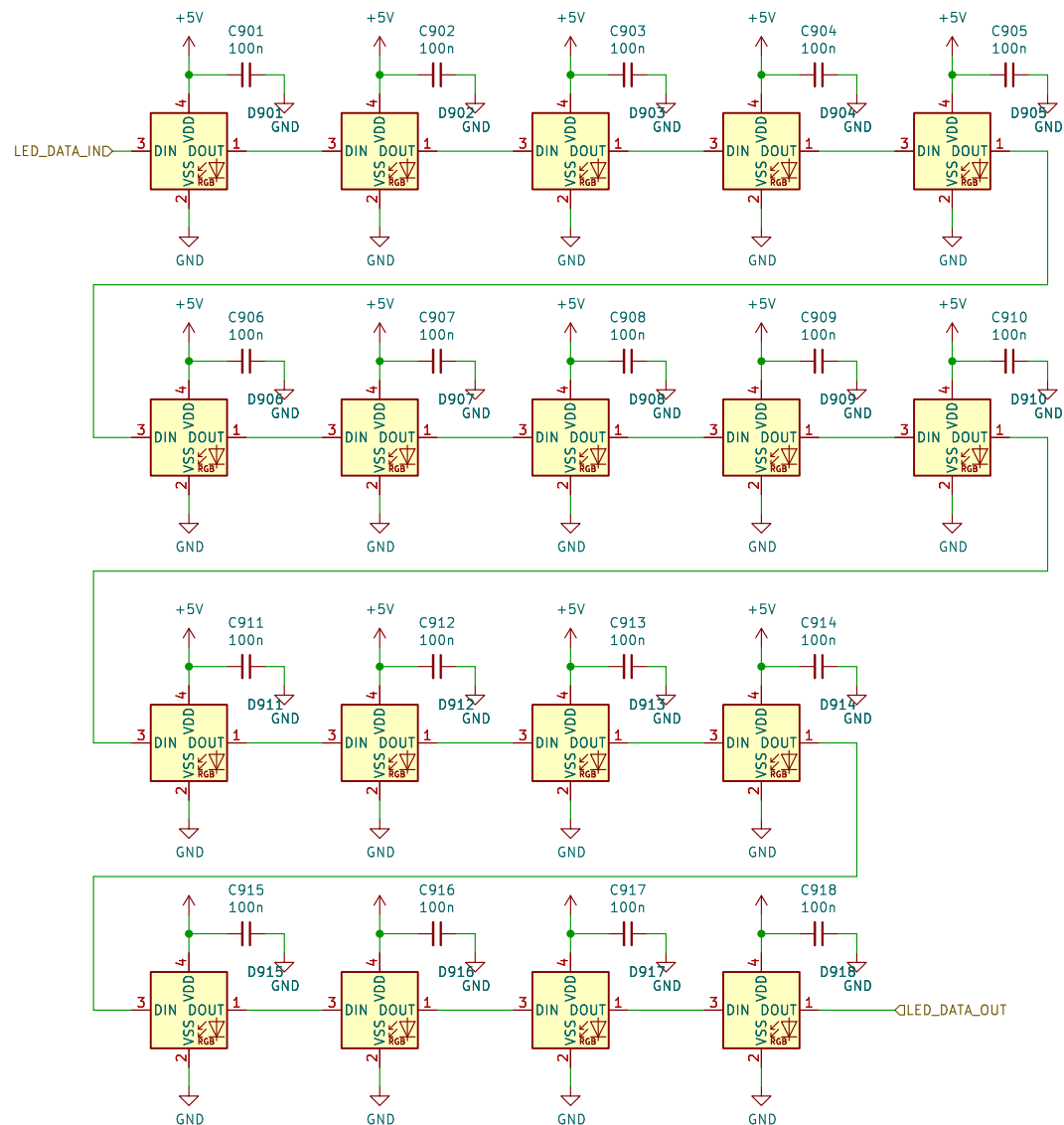
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Simulation:  
<http://tinyurl.com/y229mt4>



Sheet: /UI_BUTTON/ File: UI_BUTTON.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.1	Id: 3/10	

# 900



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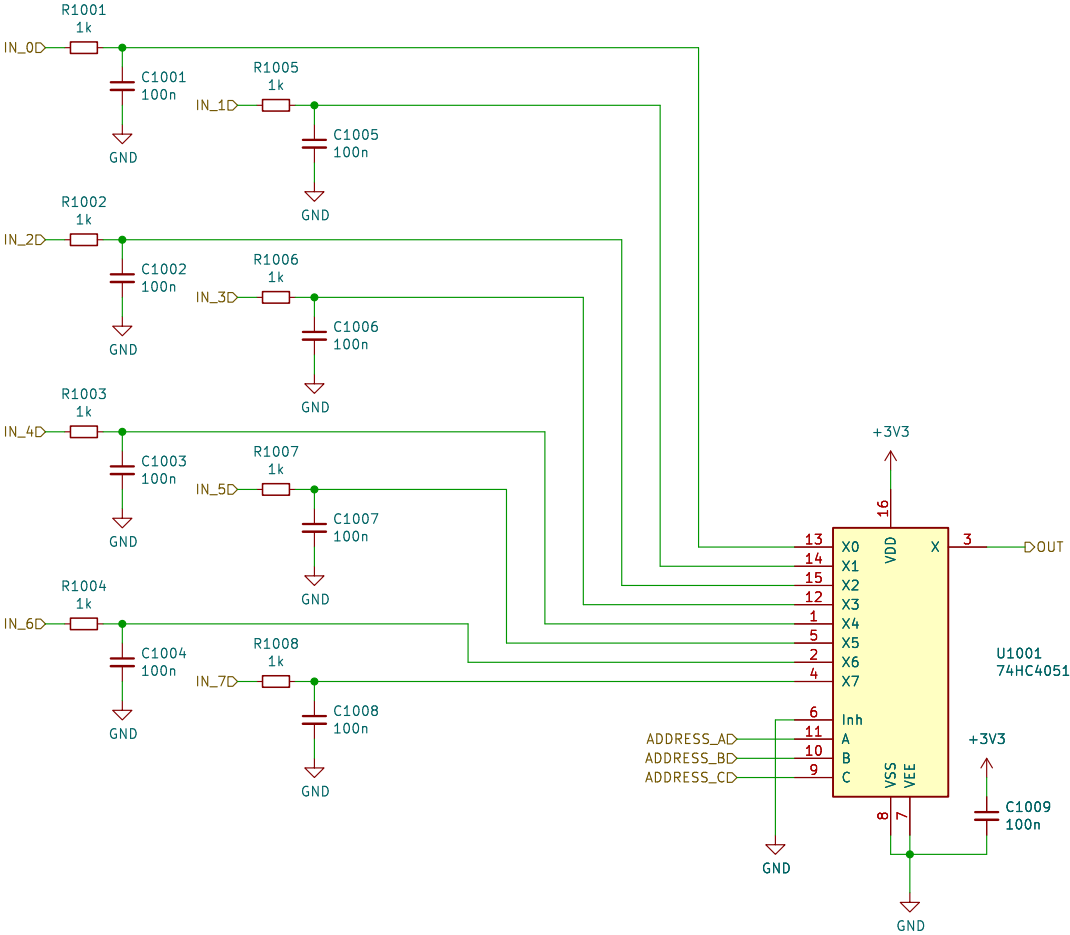
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KiCad E.D.A. 9.0.1

**Rev:**

Id: 4/10

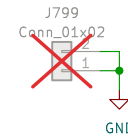
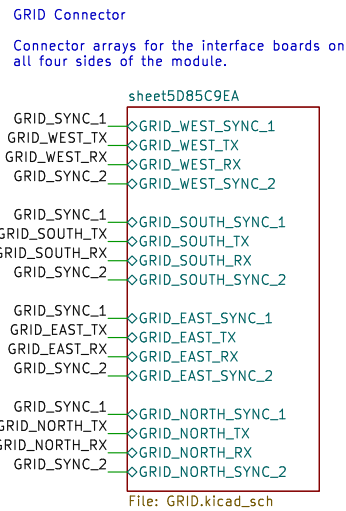
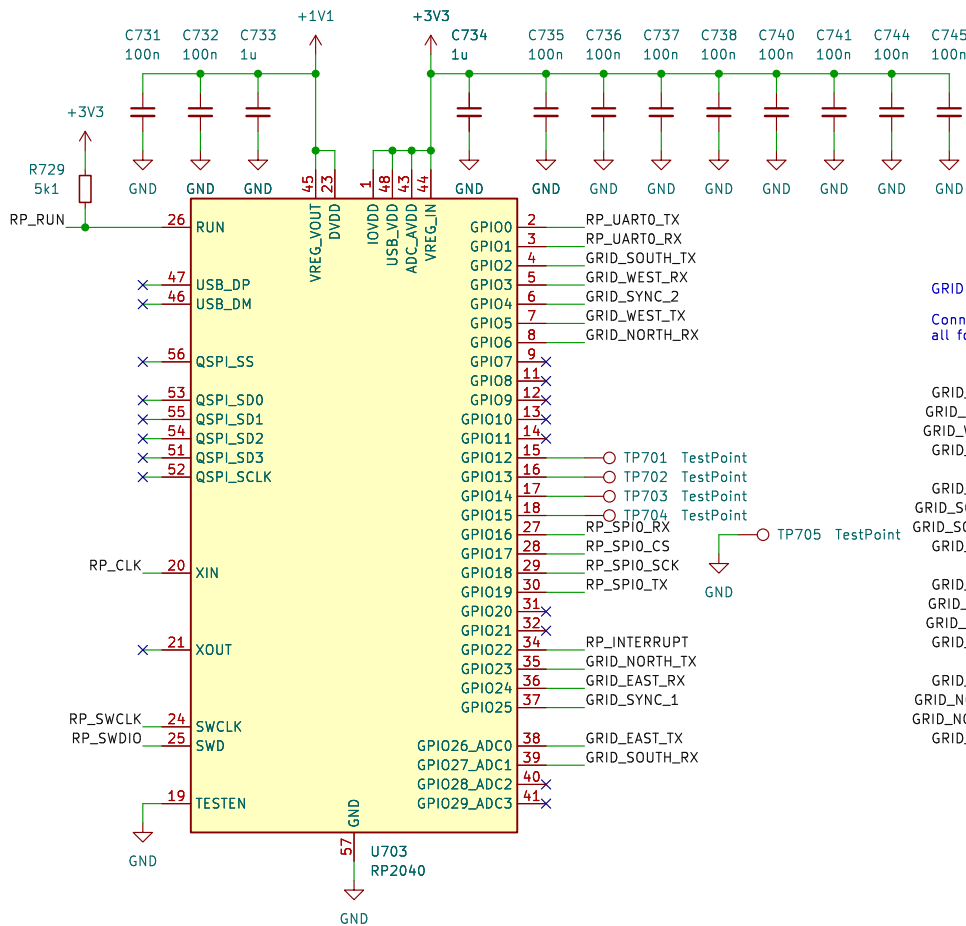
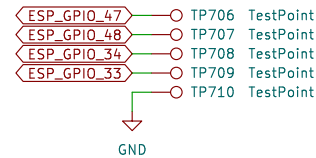
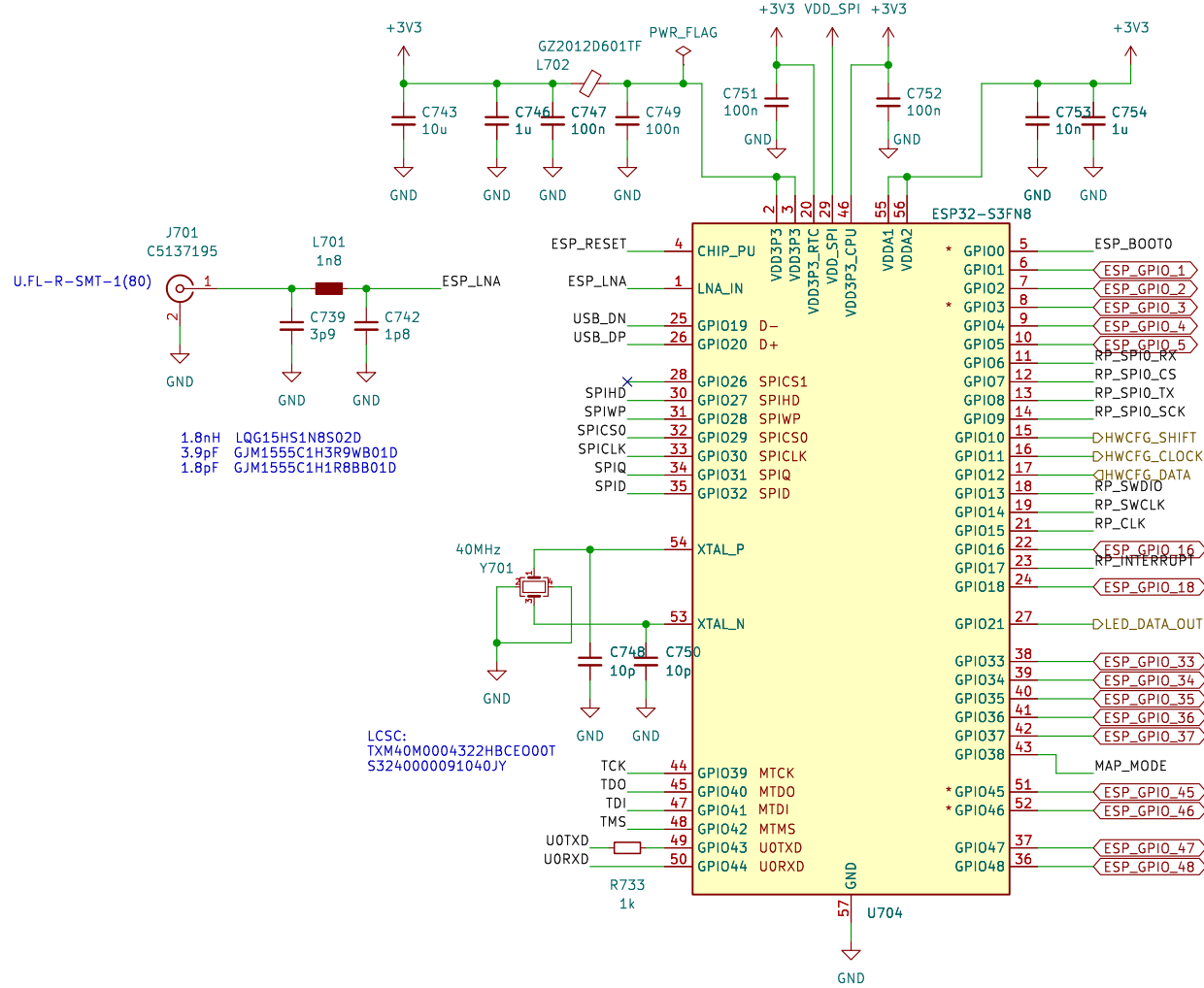
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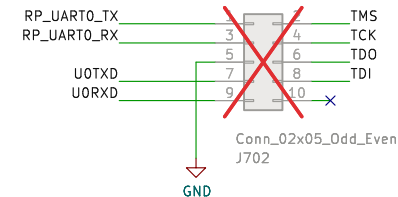
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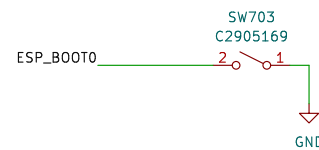
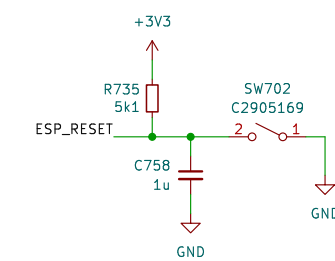
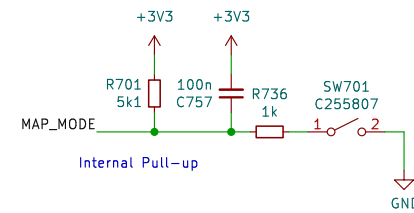


### Programming Interface



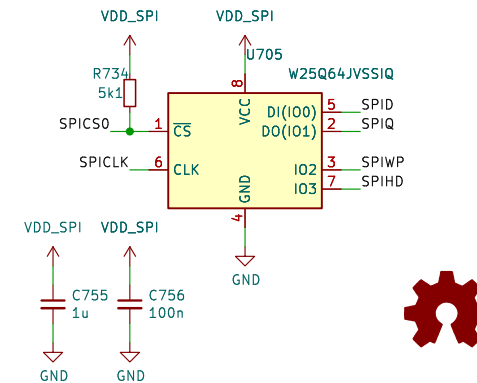
### Mapmode switch

This pusbutton is accessible through the housing, activates USB bootloader.

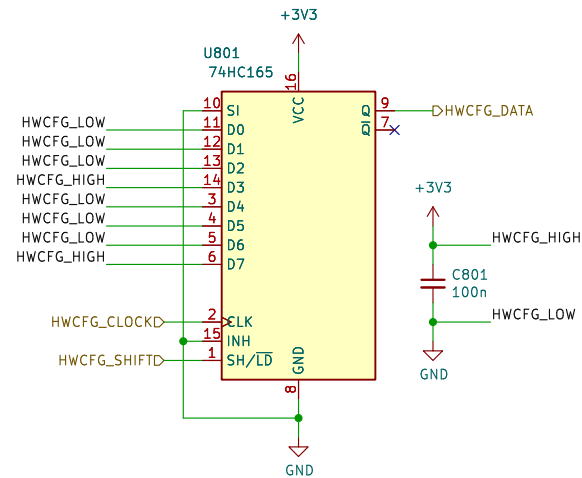


### QSPI Flash memory

This memory can be accessed through the QSPI interface of the MCU. Can be used for configuration, static data or program execution.



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#### Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)  
D1: MODEL  
D2: MODEL  
D3: MODEL (MSB)  
D4: REVISION (LSB)  
D5: REVISION  
D6: REVISION  
D7: REVISION (MSB)

#### Model Codes (D3-D0):

Po16 0000  
Bo16 0001  
PBF4 0010  
EN16 0011  
...

#### Revision Codes (D7-D4):

RevA 0000  
RevB 0001  
RevC 0010  
RevD 0011  
...

Sheet: /HWCFG/  
File: HWCFG.kicad\_sch

#### Title:

Size: A4

Date:

KiCad E.D.A. 9.0.1

Rev:

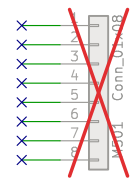
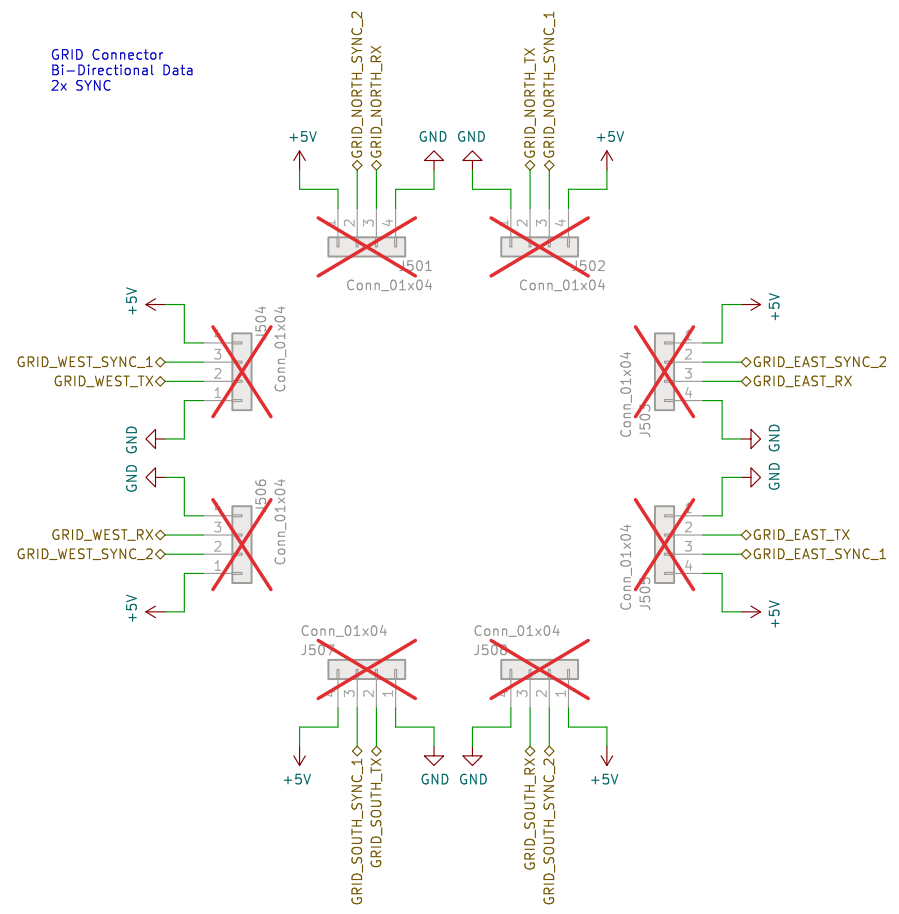
Id: 10/10



500

GRID Connector  
Bi-Directional Data  
2x SYNC

Board Mounting Pattern

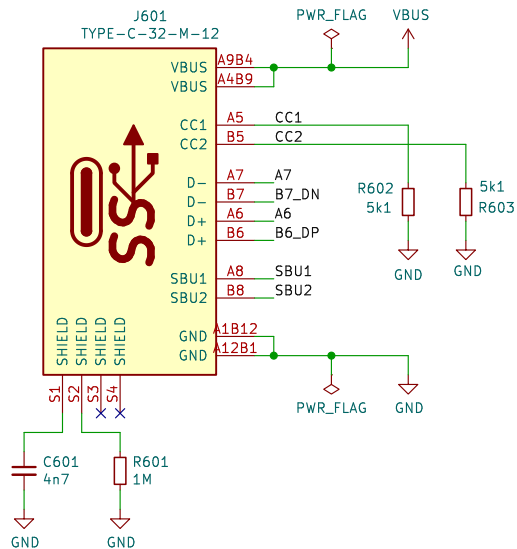


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Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.1		Id: 8/10

# 600

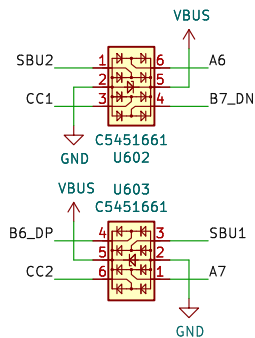
## USB Port

USB C upstream facing port configured for 5V 3A power consumption.



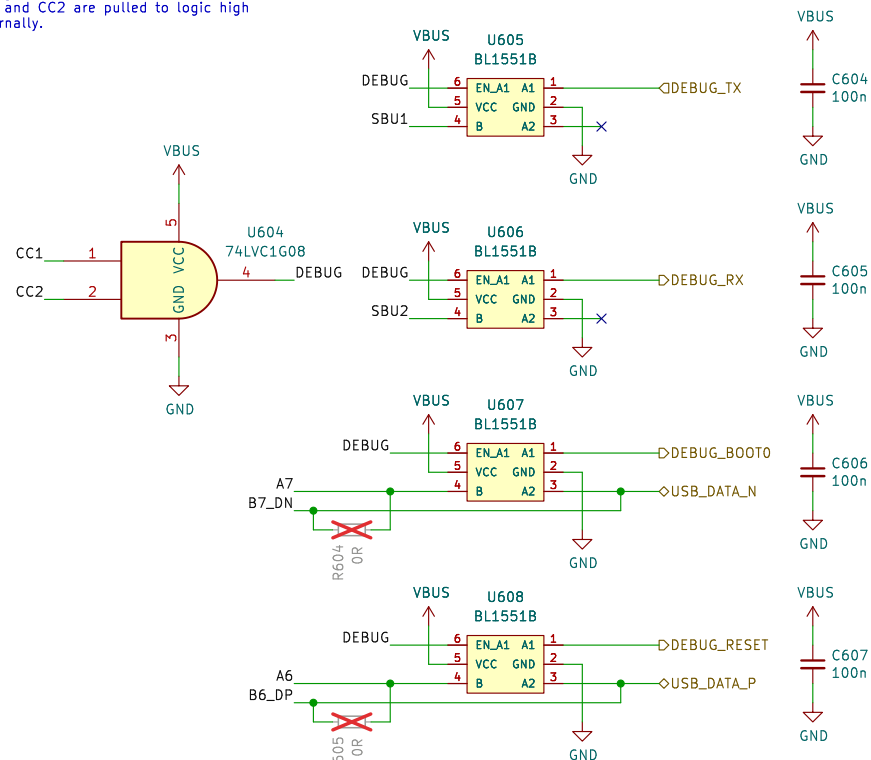
## ESD Prot.

ESP protection for all 8 signals externally accessible via the USB C connector.



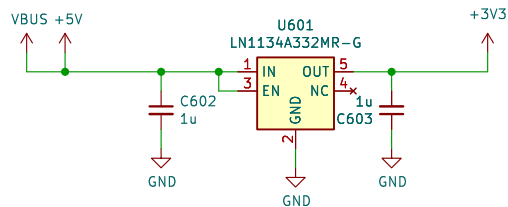
## Debug-Mode Multiplexing

Debug.mode is activated when both CC1 and CC2 are pulled to logic high externally.

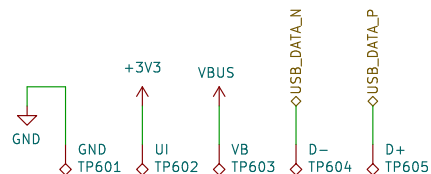


## 3V3 LDO

LDO regulator for generating the +3V3 power rail for the microcontroller and UI.



## Testpoints



Sheet: /MCU/USB\_POWER/  
File: USB\_POWER.kicad\_sch

**Title:**

Size: A4 Date:

KiCad E.D.A. 9.0.1

**Rev:**

Id: 9/10