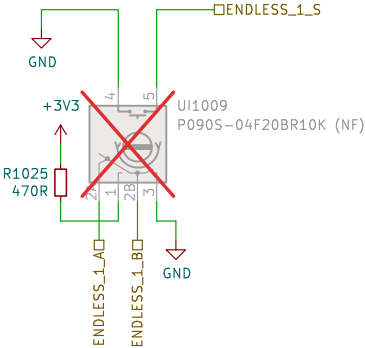
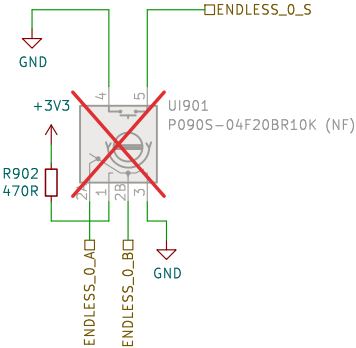


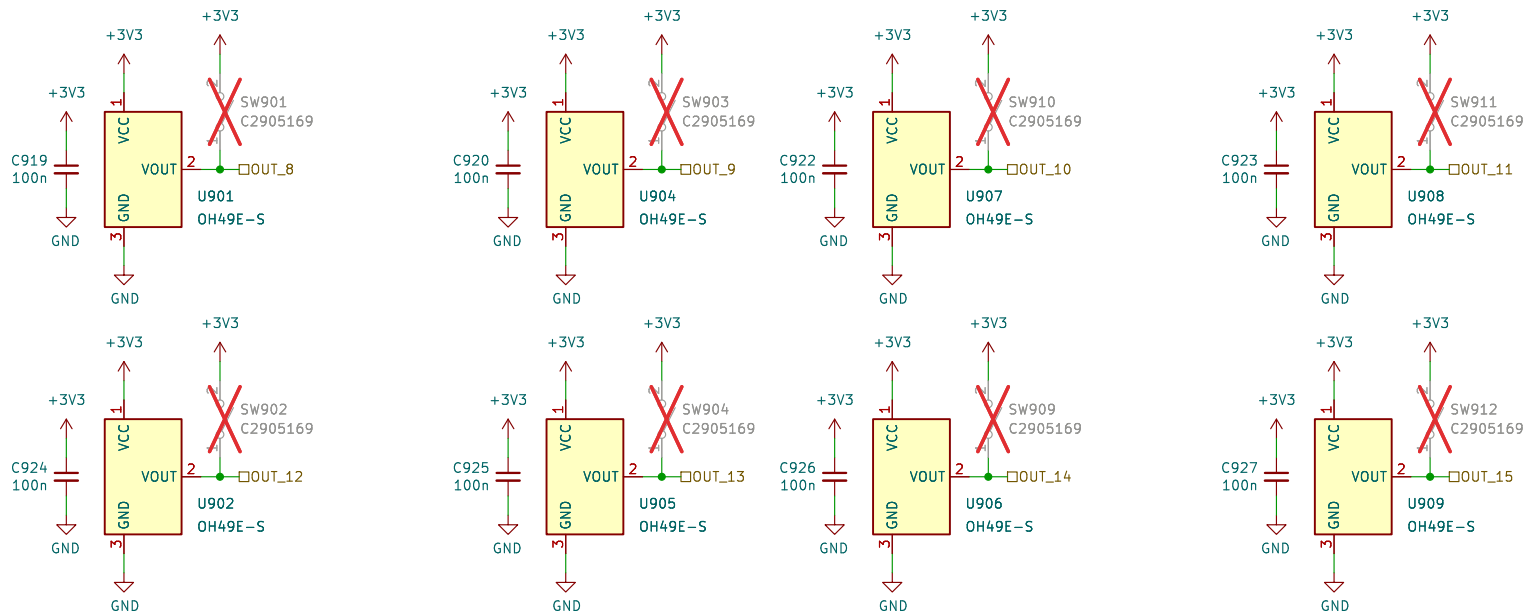
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Sheet: /UI_POT_BTN/ File: UI_POT_BTN.kicad_sch		
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Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.1	Id: 2/22	

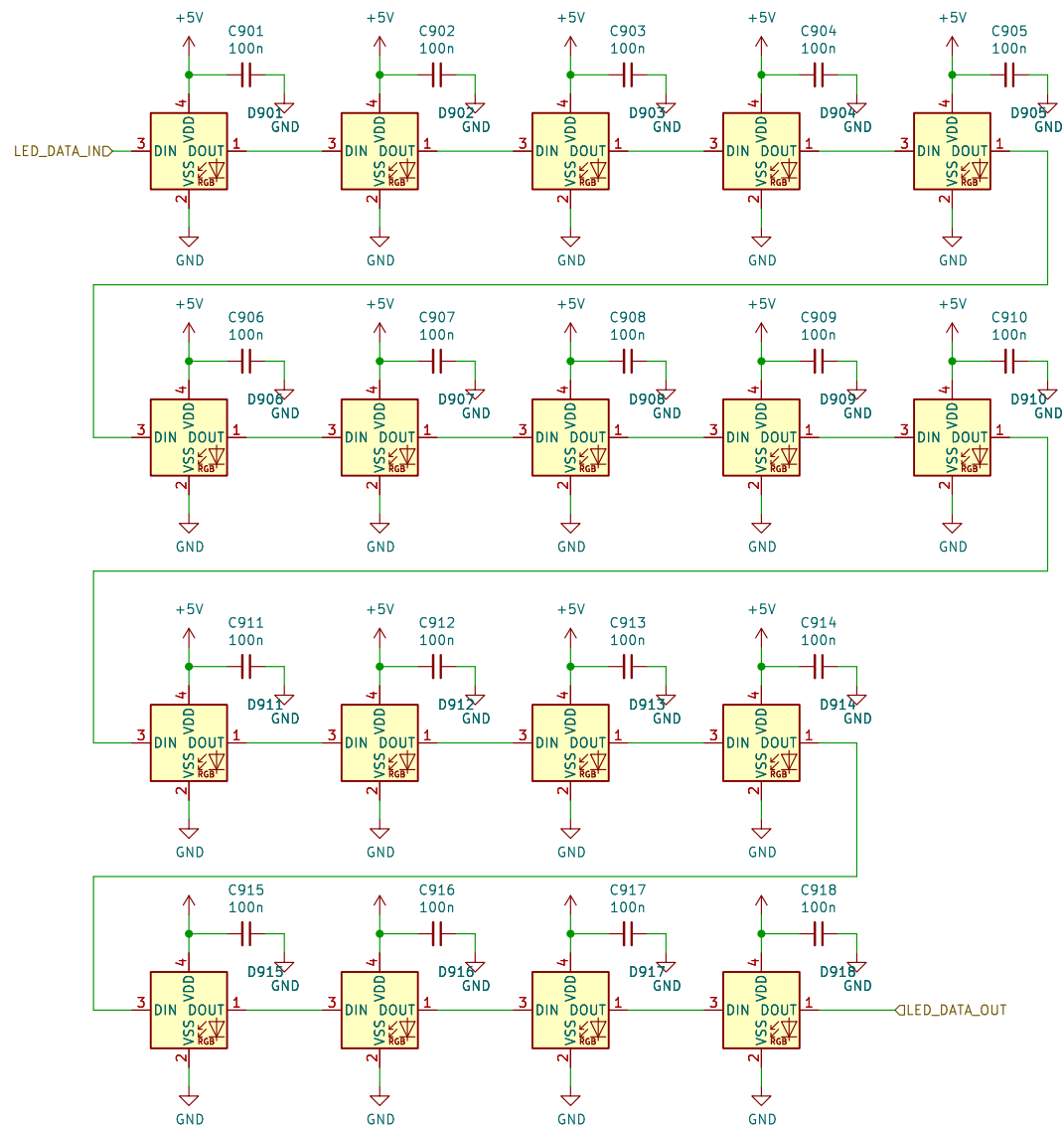
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Simulation:
<http://tinyurl.com/y229mt4>



Sheet: /UI_BUTTON/ File: UI_BUTTON.kicad_sch		
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Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.1	Id: 3/22	

900



Sheet: /UI_LED/
File: UI_LED.kicad_sch

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Size: A4
KiCad E.D.A. 9.0.1

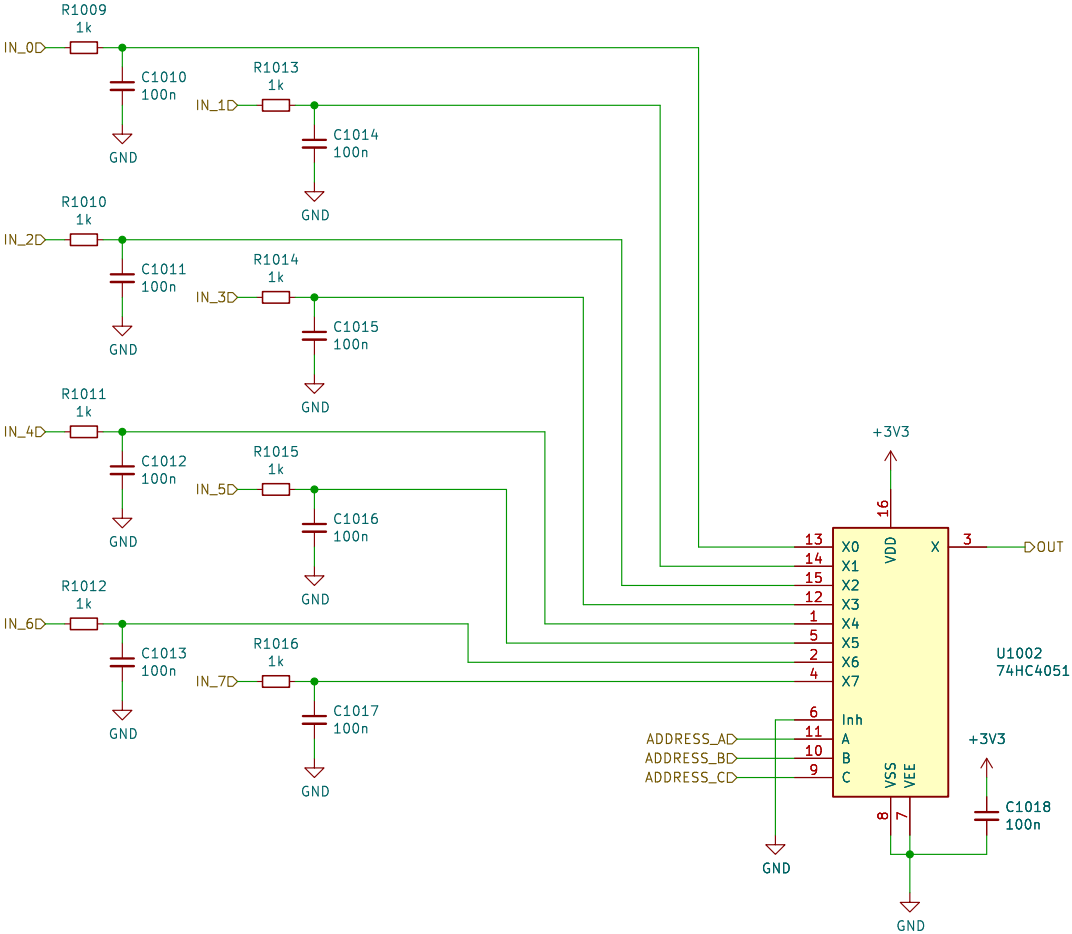
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Rev:
Id: 4/22

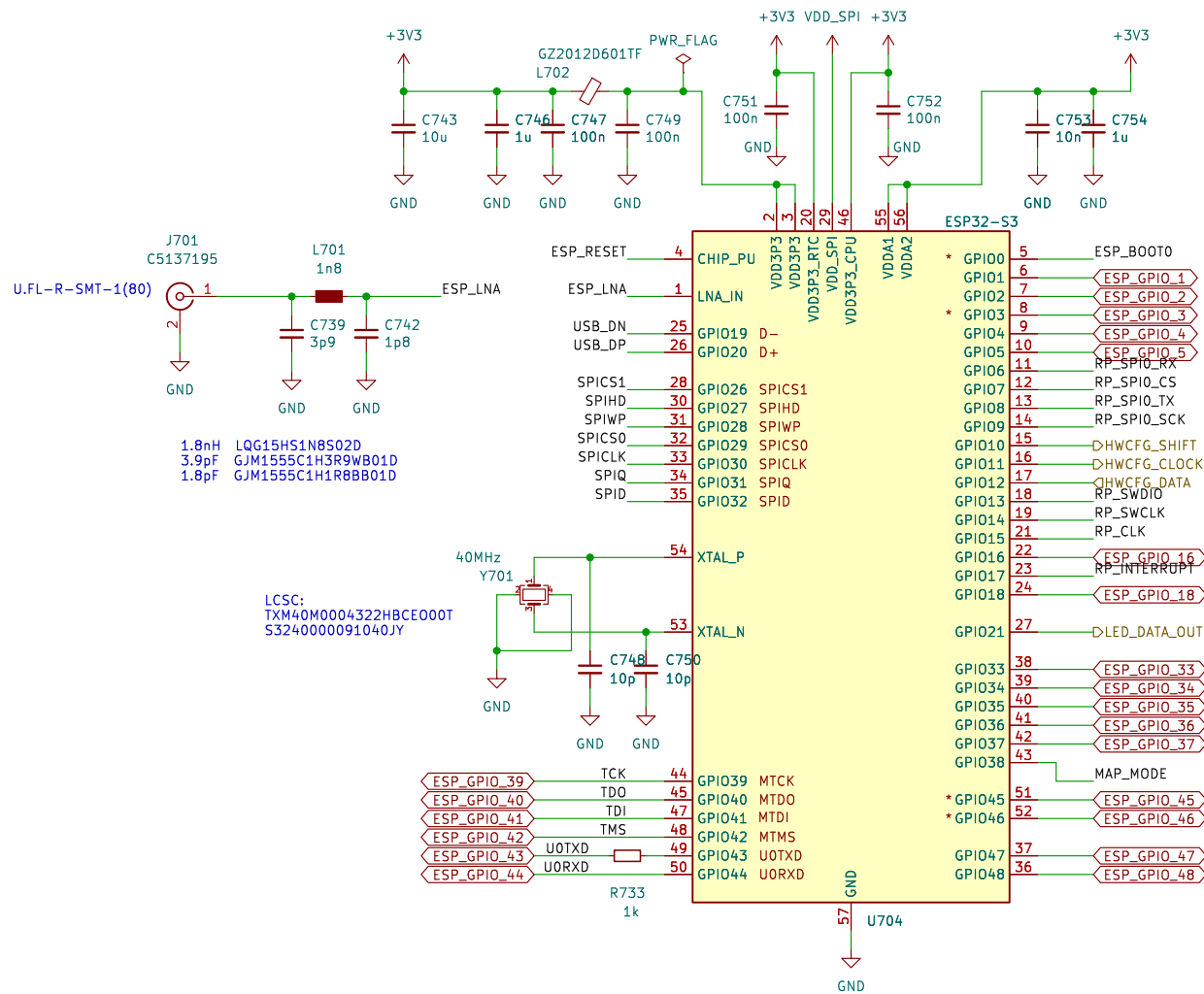
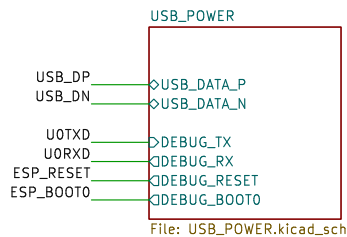
1000



1000



700



800

The schematic diagram illustrates the HWCFG board's internal components and connections. At the top left, a large blue '800' is displayed. The circuit includes two screen connectors, JP801 (RIGHT SCREEN) and JP802 (LEFT SCREEN), each with a 5k1 pull-up resistor (R801, R802) to +3V3. These are connected to the data inputs of a 74HC165 shift register (U801). The shift register's VCC is connected to +3V3, and its GND is connected to ground. The shift register's output Q (pin 9) is connected to HWCFG_DATA. The shift register's clock input CLK (pin 2) is connected to HWCFG_CLOCKD, and its shift/load input SH/LD (pin 1) is connected to HWCFG_SHIFTD. The shift register's data outputs D0-D7 (pins 11-18) are connected to HWCFG_HIGH and HWCFG_LOW pins. A 100nF capacitor (C801) is connected between HWCFG_HIGH and HWCFG_LOW. The board also features a 3V3 power supply and a ground connection.

Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4b'Model + 4b'Revision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

Model Codes (D3-D0):

Po16 0000
Bo16 0001
PBF4 0010
EN16 0011
...

Revision Codes (D7-D4):

RevA 0000
RevB 0001
RevC 0010
RevD 0011
...

Sheet: /HWCFG/ File: HWCFG.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.1		Id: 10/22

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

D0: MODEL (LSB)
D1: MODEL
D2: MODEL
D3: MODEL (MSB)
D4: REVISION (LSB)
D5: REVISION
D6: REVISION
D7: REVISION (MSB)

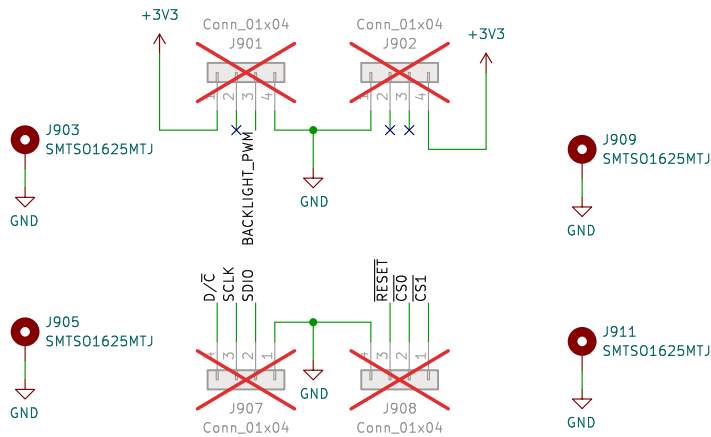
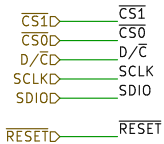
Po16	0000
Bo16	0001
PBF4	0010
EN16	0011
...	

```
RevA 0000
RevB 0001
RevC 0010
RevD 0011
...
```

Sheet: /HWCFG/ File: HWCFG.kicad_sch			D
Title:			
Size: A4	Date:	Rev:	
KiCad E.D.A. 9.0.1		Id: 10/22	

1000

BACKLIGHT \square BACKLIGHT_PWM



Sheet: /UI_DISPLAY/
File: UI_DISPLAY.kicad_sch

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Size: A4

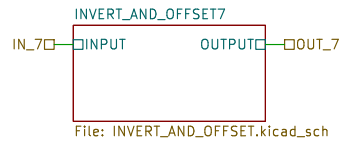
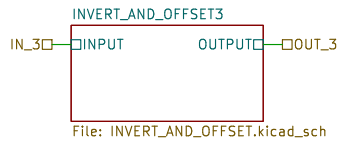
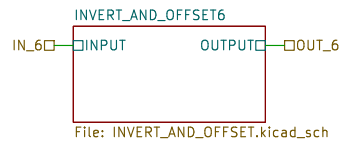
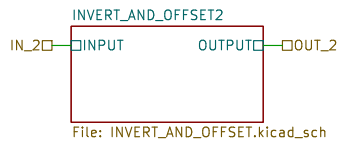
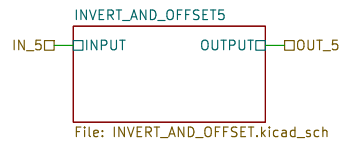
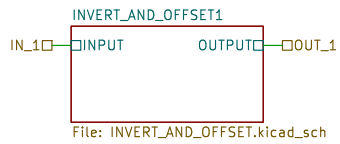
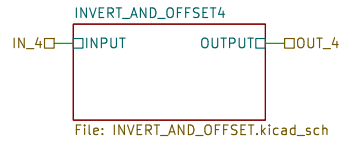
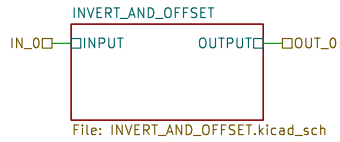
Date:

KiCad E.D.A. 9.0.1

Rev:

Id: 11/22

simulation

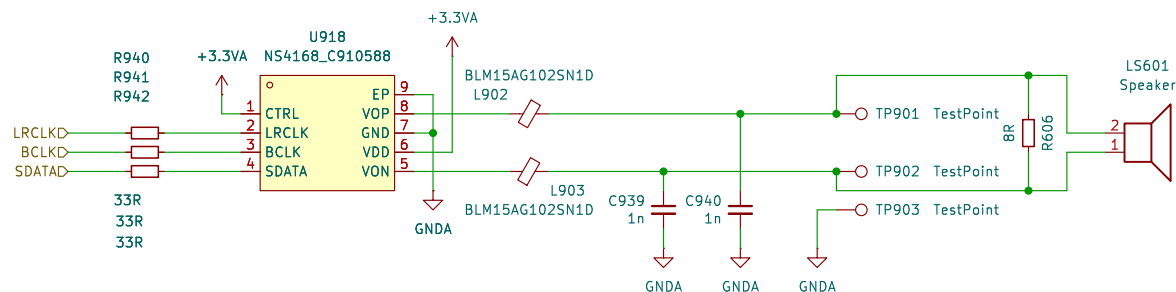
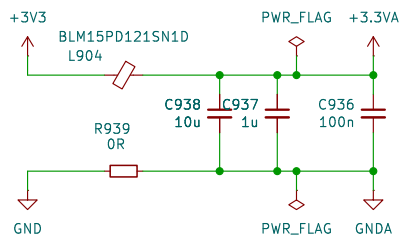


Sheet: /UI_HALL_PREAMP/
File: UI_HALL_PREAMP.kicad_sch

Title:

Size: A4	Date:
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Rev:
Id: 12/22



Sheet: /AUDIO/
File: AUDIO.kicad_sch

Title:

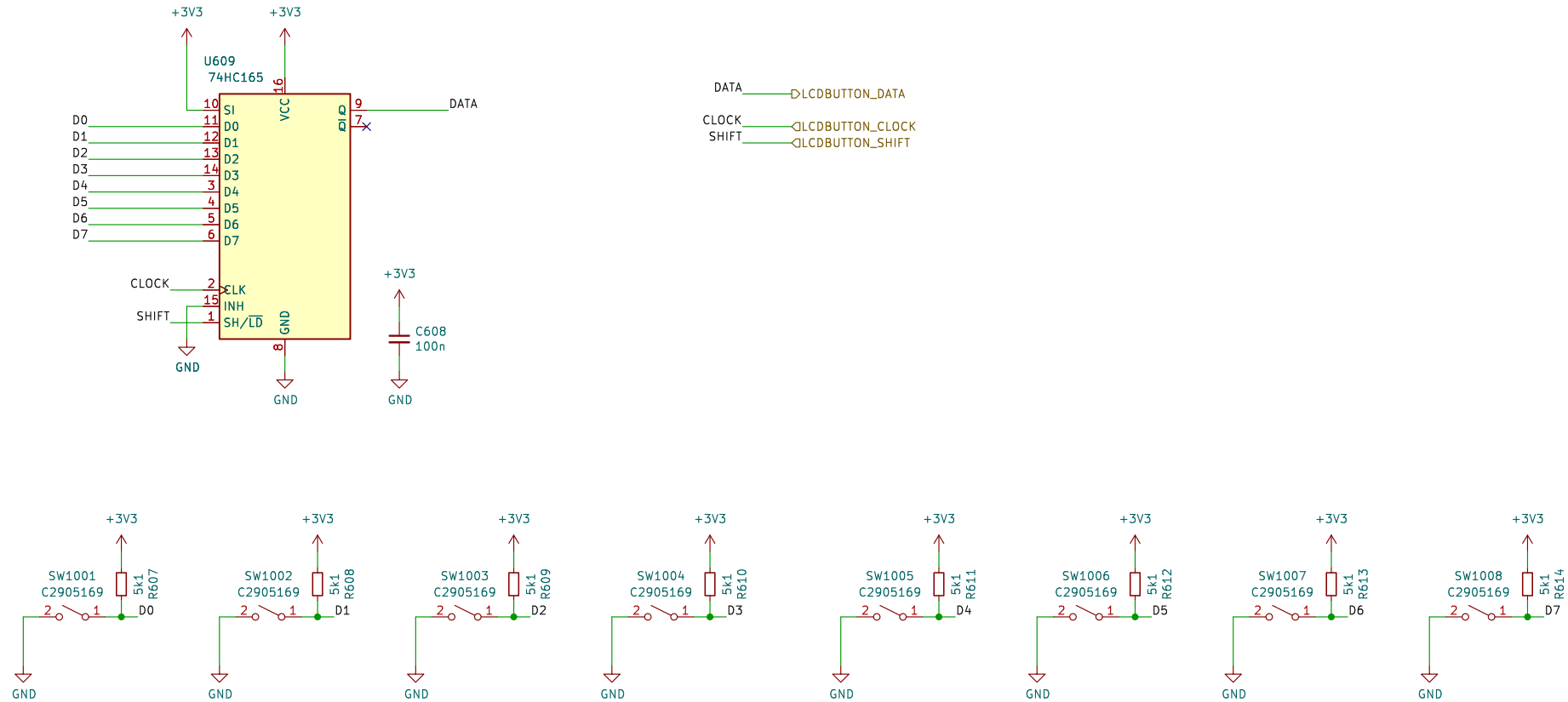
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Date:

KiCad E.D.A. 9.0.1

Rev:

Id: 21/22

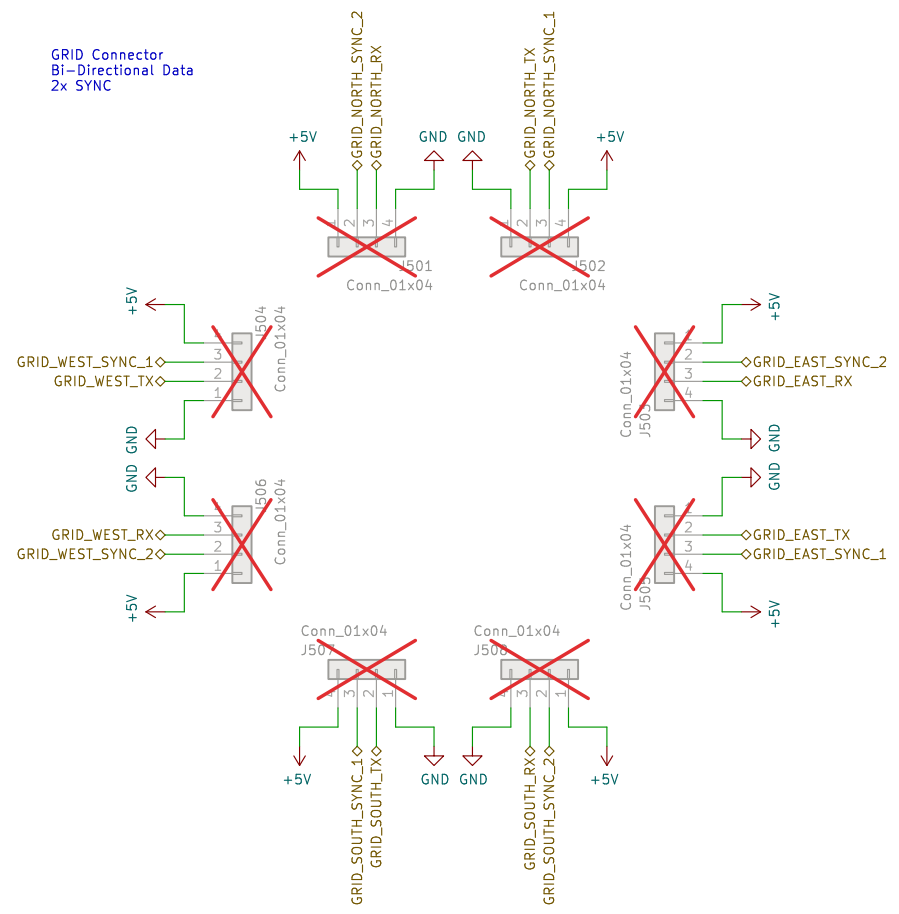


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File: LCDBUTTON.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.1		Id: 22/22

500

GRID Connector
Bi-Directional Data
2x SYNC

Board Mounting Pattern

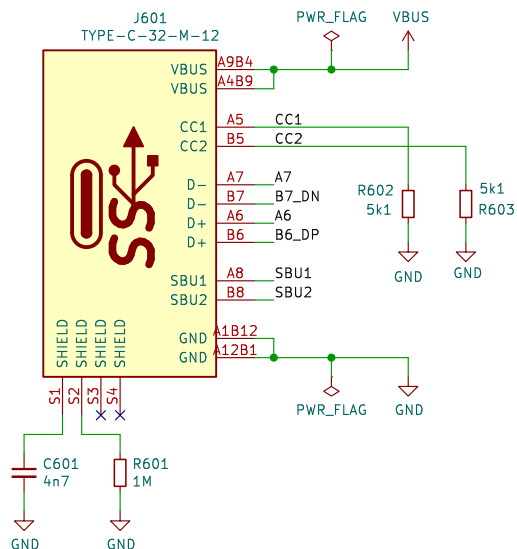


Sheet: /MCU/sheet5D85C9EA/ File: GRID.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.1		Id: 8/22

600

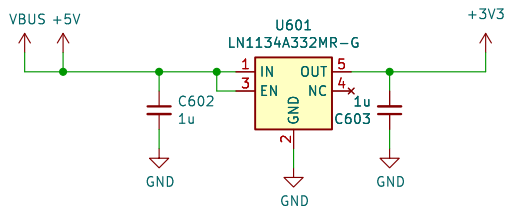
USB Port

USB C upstream facing port configured for 5V 3A power consumption.



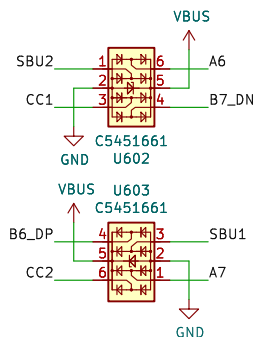
3V3 LDO

LDO regulator for generating the +3V3 power rail for the microcontroller and UI.

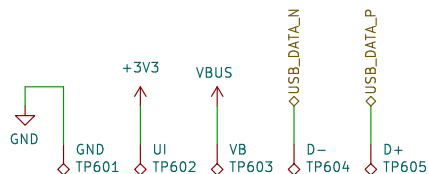


ESD Prot.

ESP protection for all 8 signals externally accessible via the USB C connector.

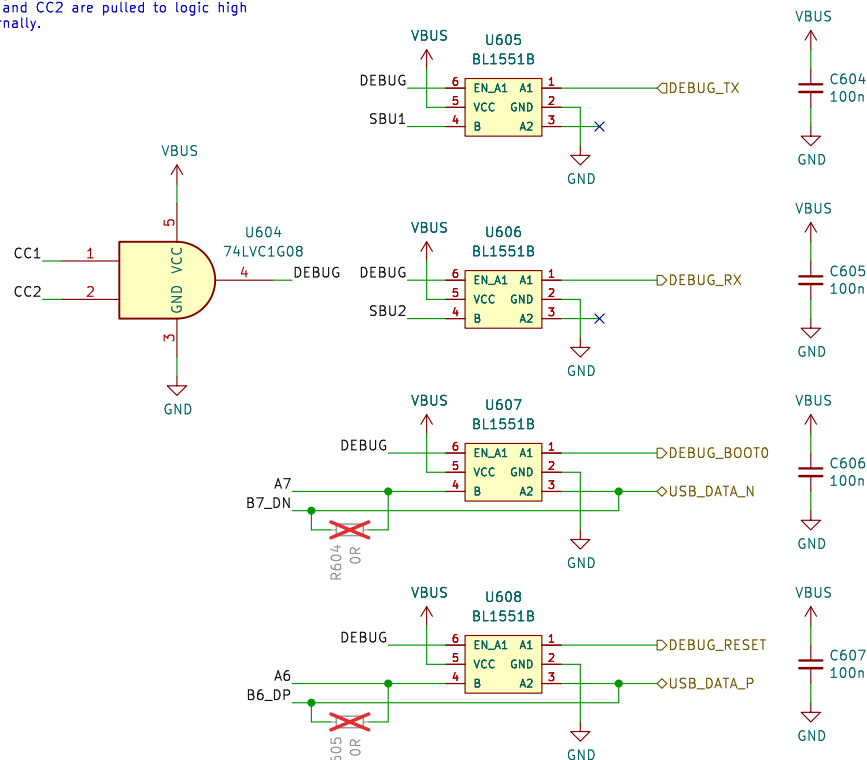


Testpoints



Debug-Mode Multiplexing

Debug.mode is activated when both CC1 and CC2 are pulled to logic high externally.



Sheet: /MCU/USB_POWER/
File: USB_POWER.kicad_sch

Title:

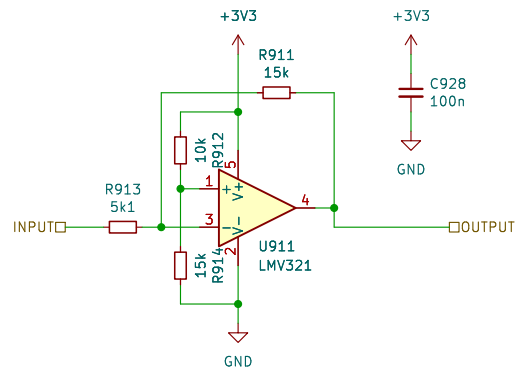
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KiCad E.D.A. 9.0.1

Rev:

Id: 9/22



Sheet: /UI_HALL_PREAMP/INVERT_AND_OFFSET/
File: INVERT_AND_OFFSET.kicad_sch

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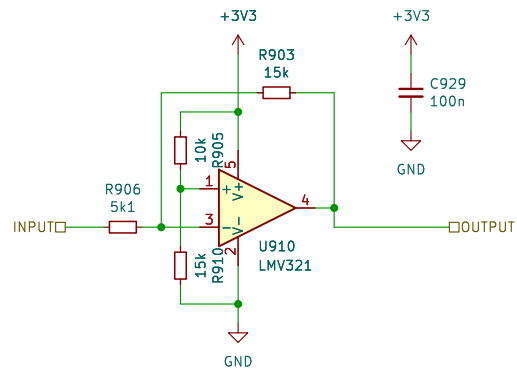
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Date:

Rev:

KiCad E.D.A. 9.0.1

Id: 13/22



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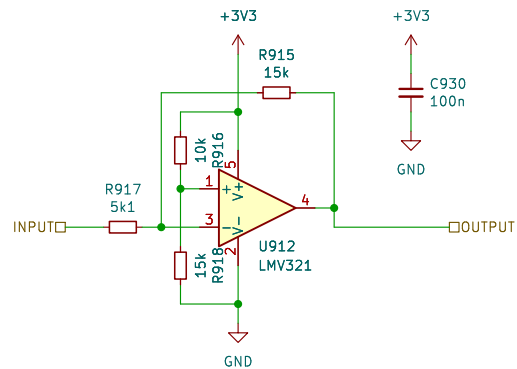
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Date:

Rev:

KiCad E.D.A. 9.0.1

Id: 14/22



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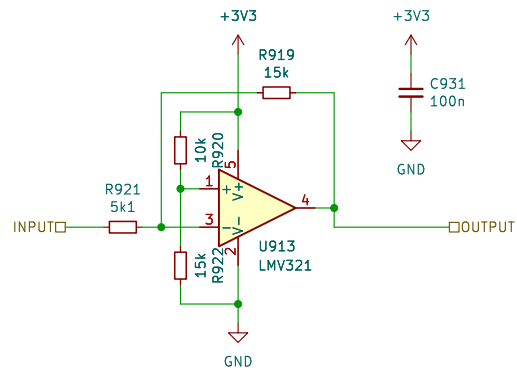
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Date:

Rev:

KiCad E.D.A. 9.0.1

Id: 15/22



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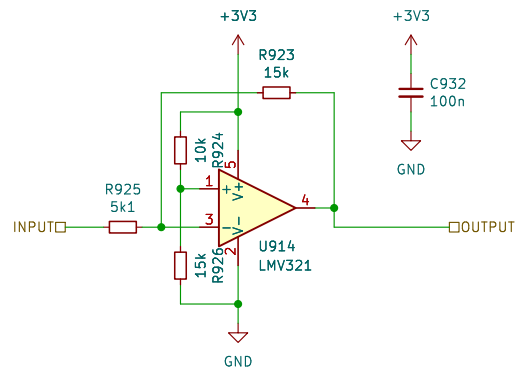
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Date:

Rev:

KiCad E.D.A. 9.0.1

Id: 16/22



Sheet: /UI_HALL_PREAMP/INVERT_AND_OFFSET4/
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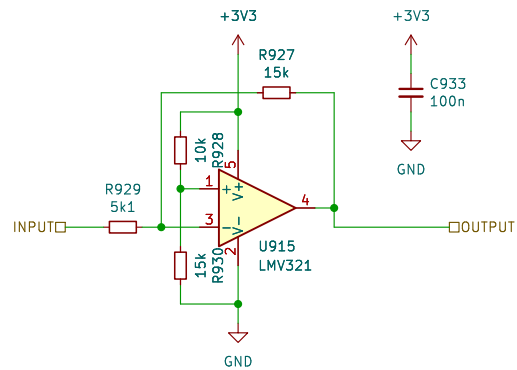
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Date:

Rev:

KiCad E.D.A. 9.0.1

Id: 17/22



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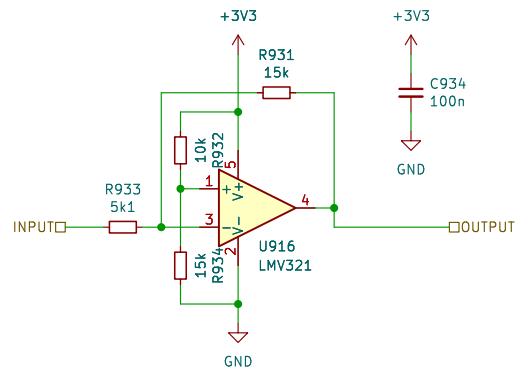
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Rev:

KiCad E.D.A. 9.0.1

Id: 18/22



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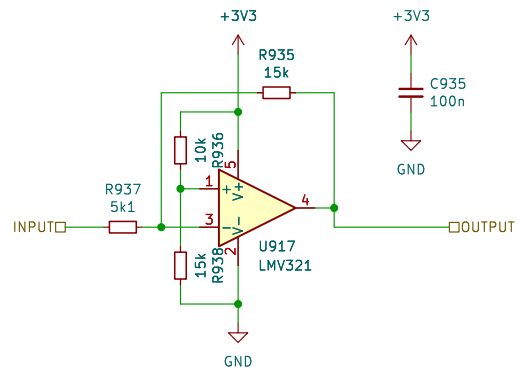
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Rev:

KiCad E.D.A. 9.0.1

Id: 19/22



Sheet: /UI_HALL_PREAMP/INVERT_AND_OFFSET7/
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Title:

Size: A4

Date:

Rev:

KiCad E.D.A. 9.0.1

Id: 20/22