

A

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A

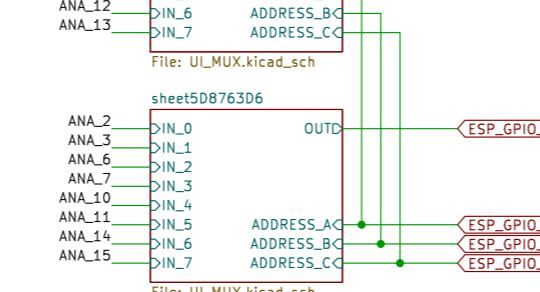
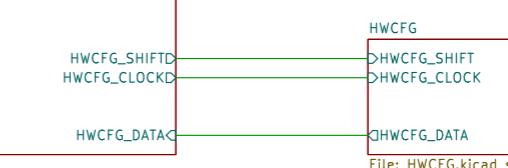
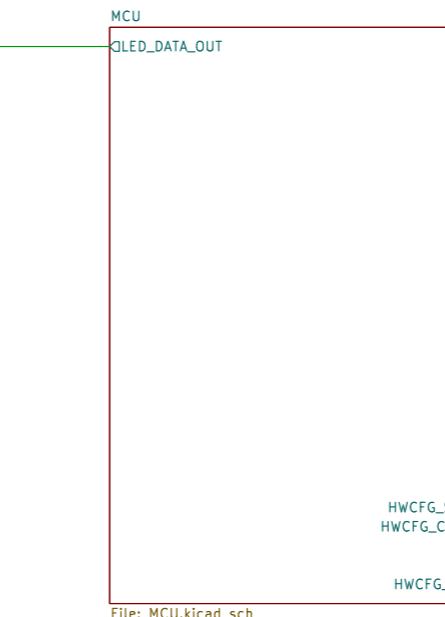
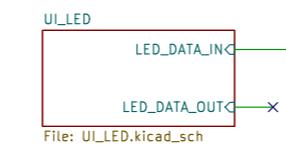
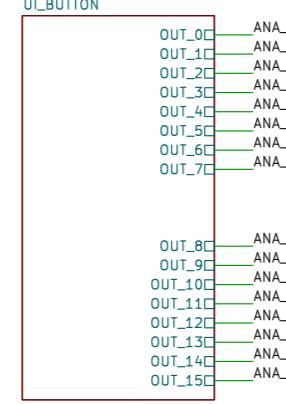
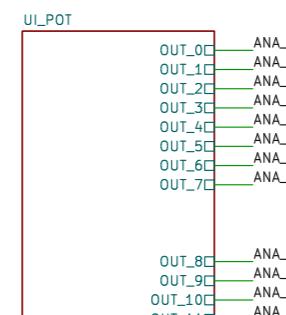
B

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Common Sheets:
500 GRID
600 USB_POWER
700 MCU

Module Specific:
800 HWCFG
900 LED
1000 UI

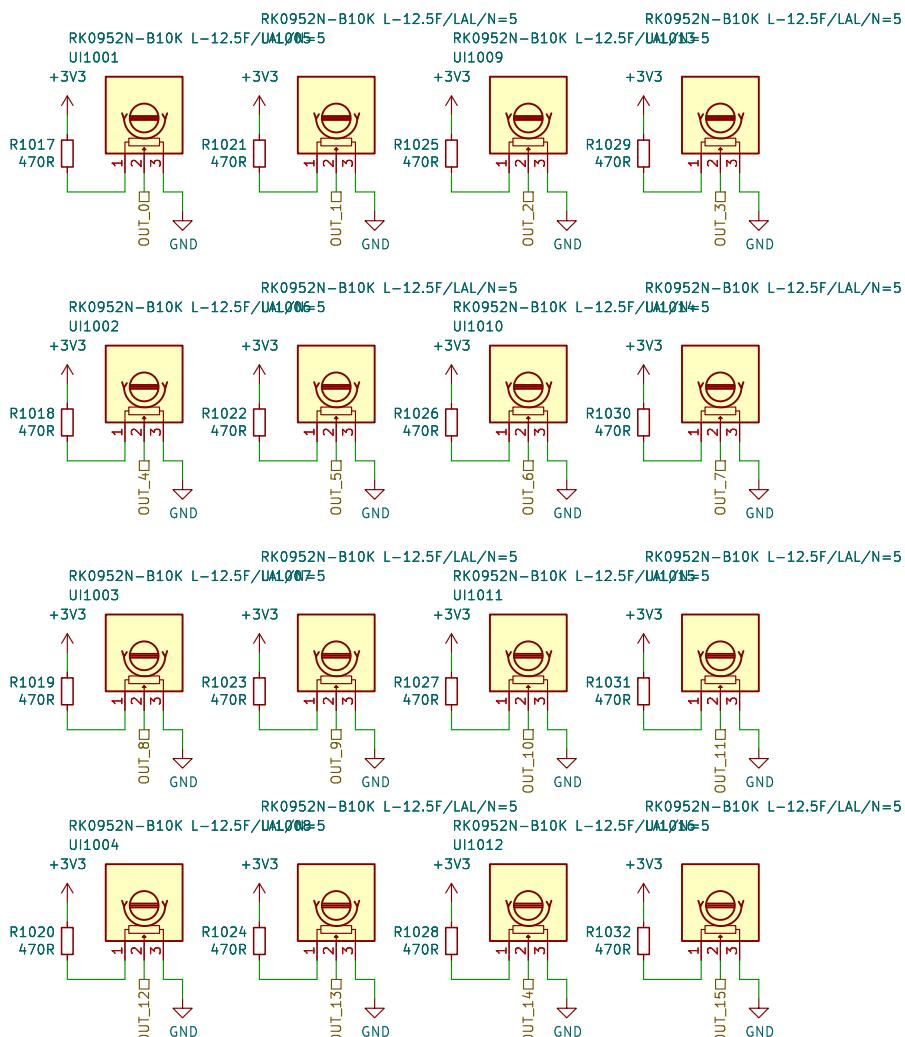
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Size: A3 Date:
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Rev:
Id: 1/10

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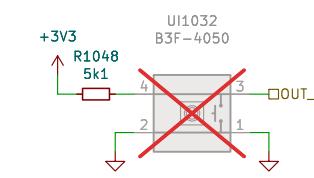
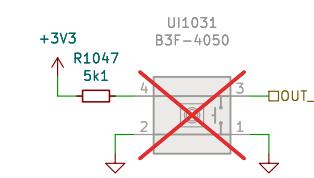
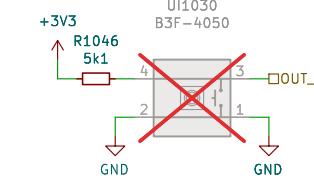
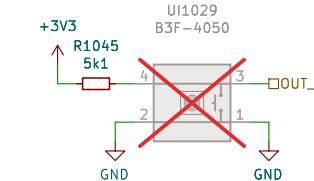
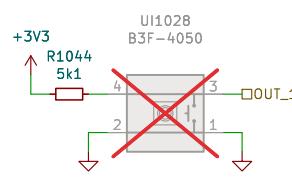
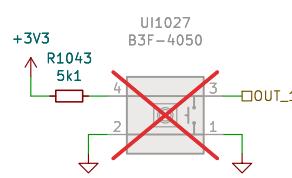
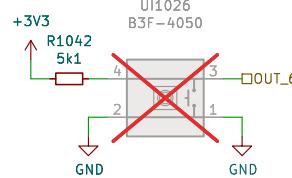
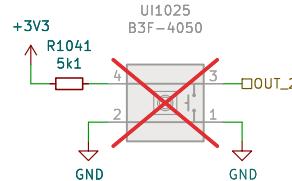
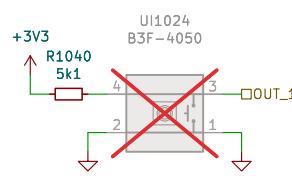
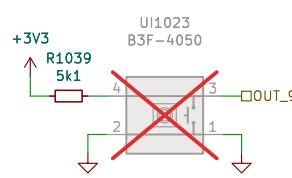
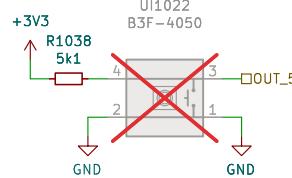
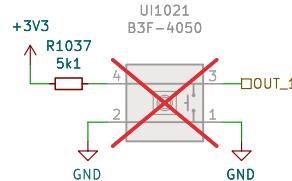
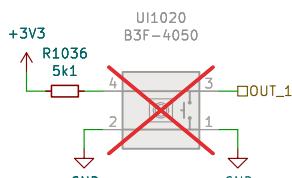
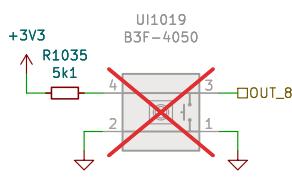
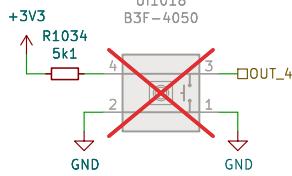
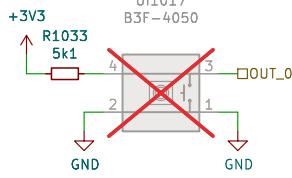
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Simulation:



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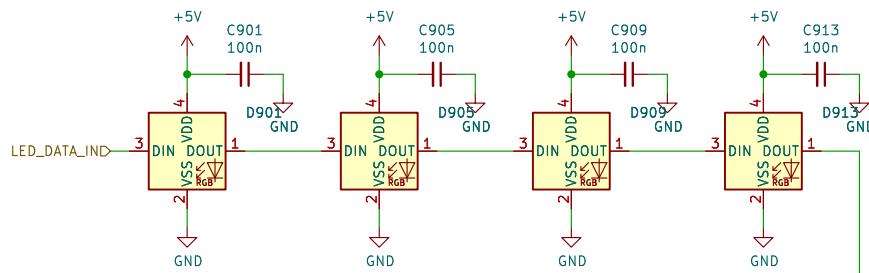
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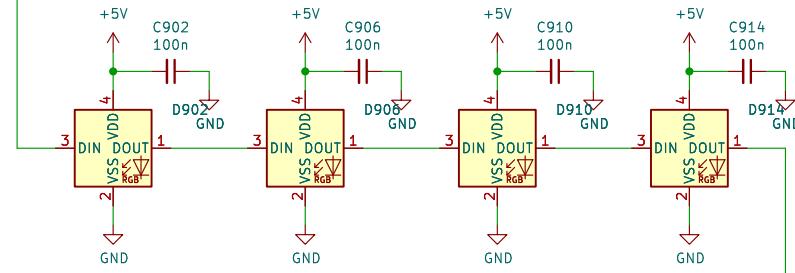
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Id: 3/10

900

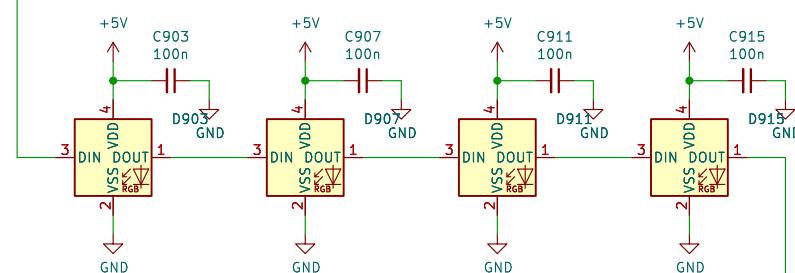
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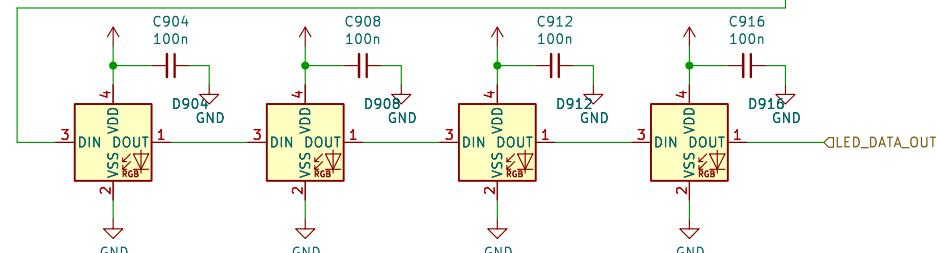
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C



D



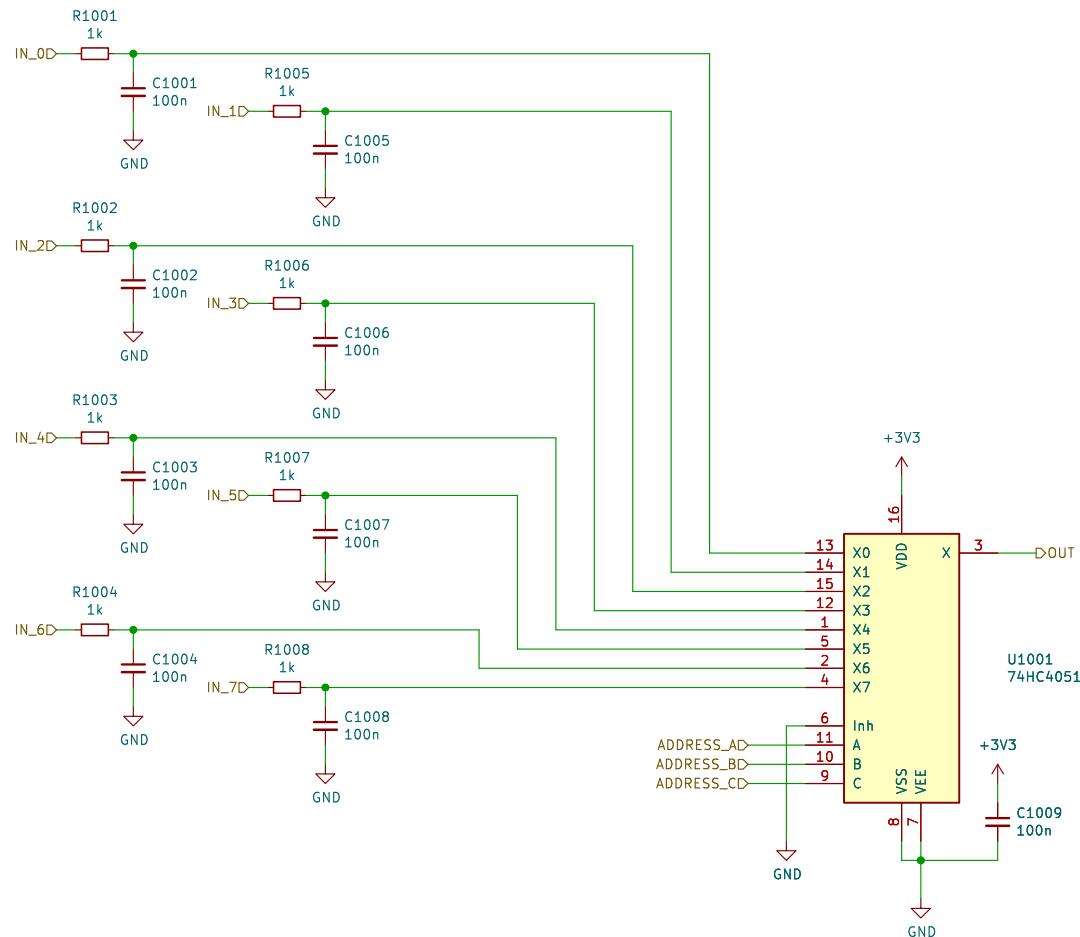
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Rev:
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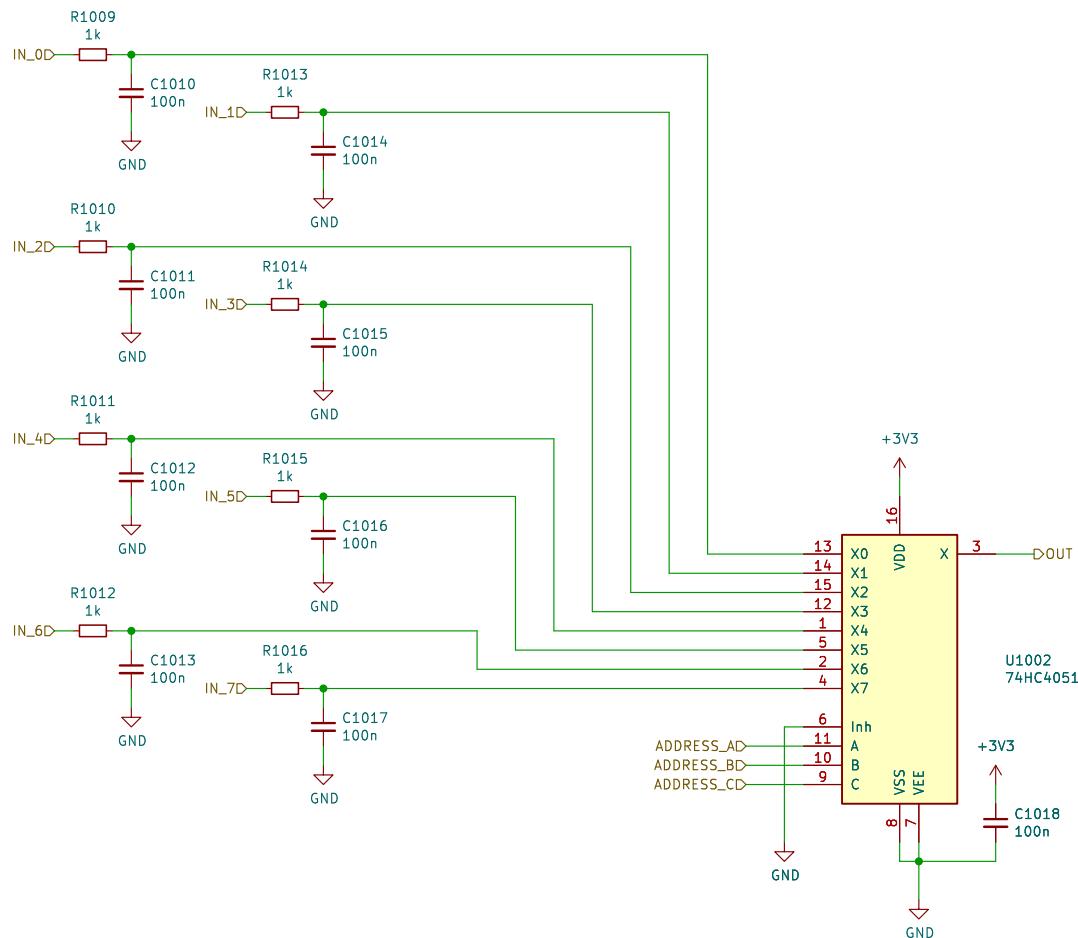
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Rev:
Id: 5/10

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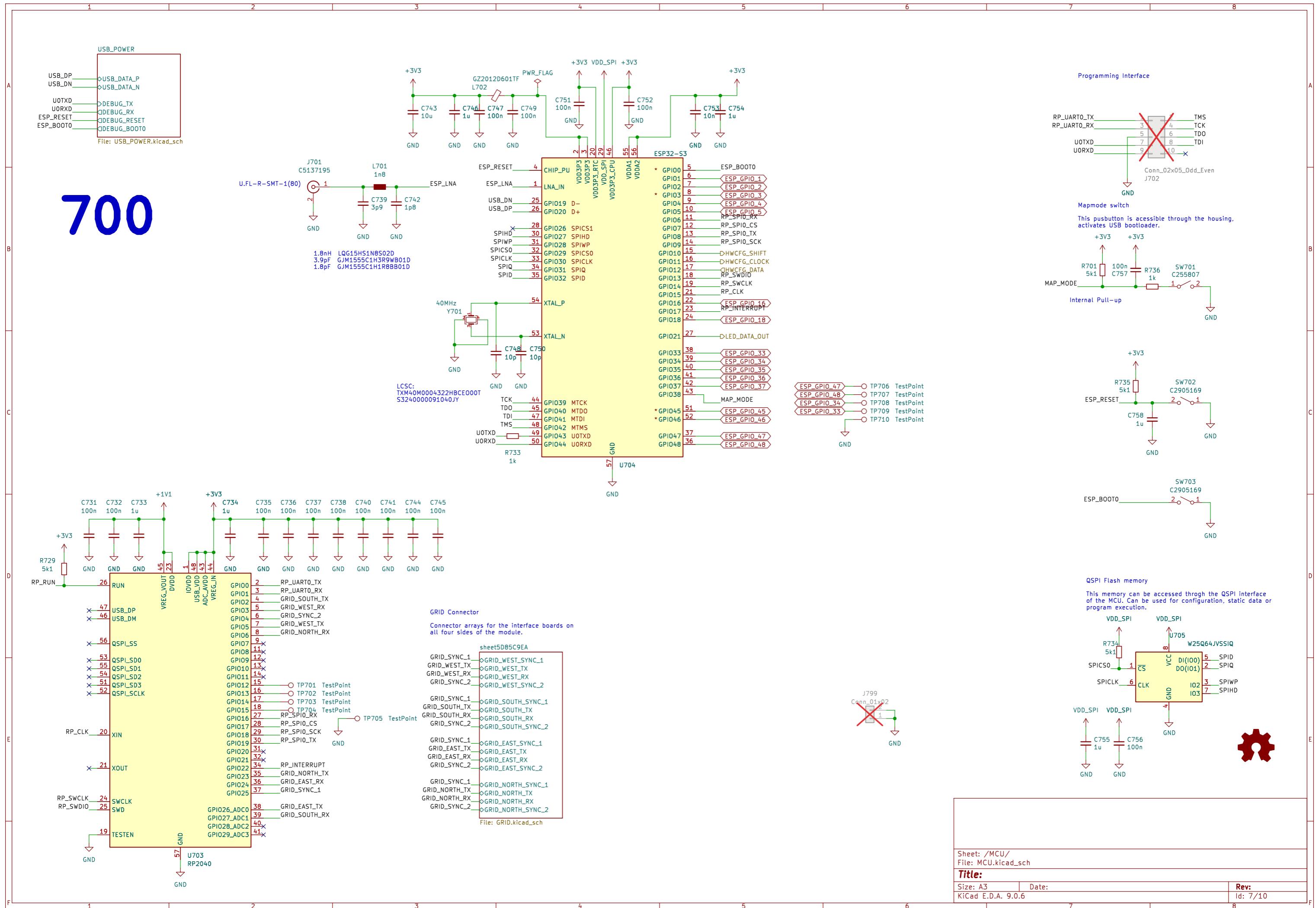


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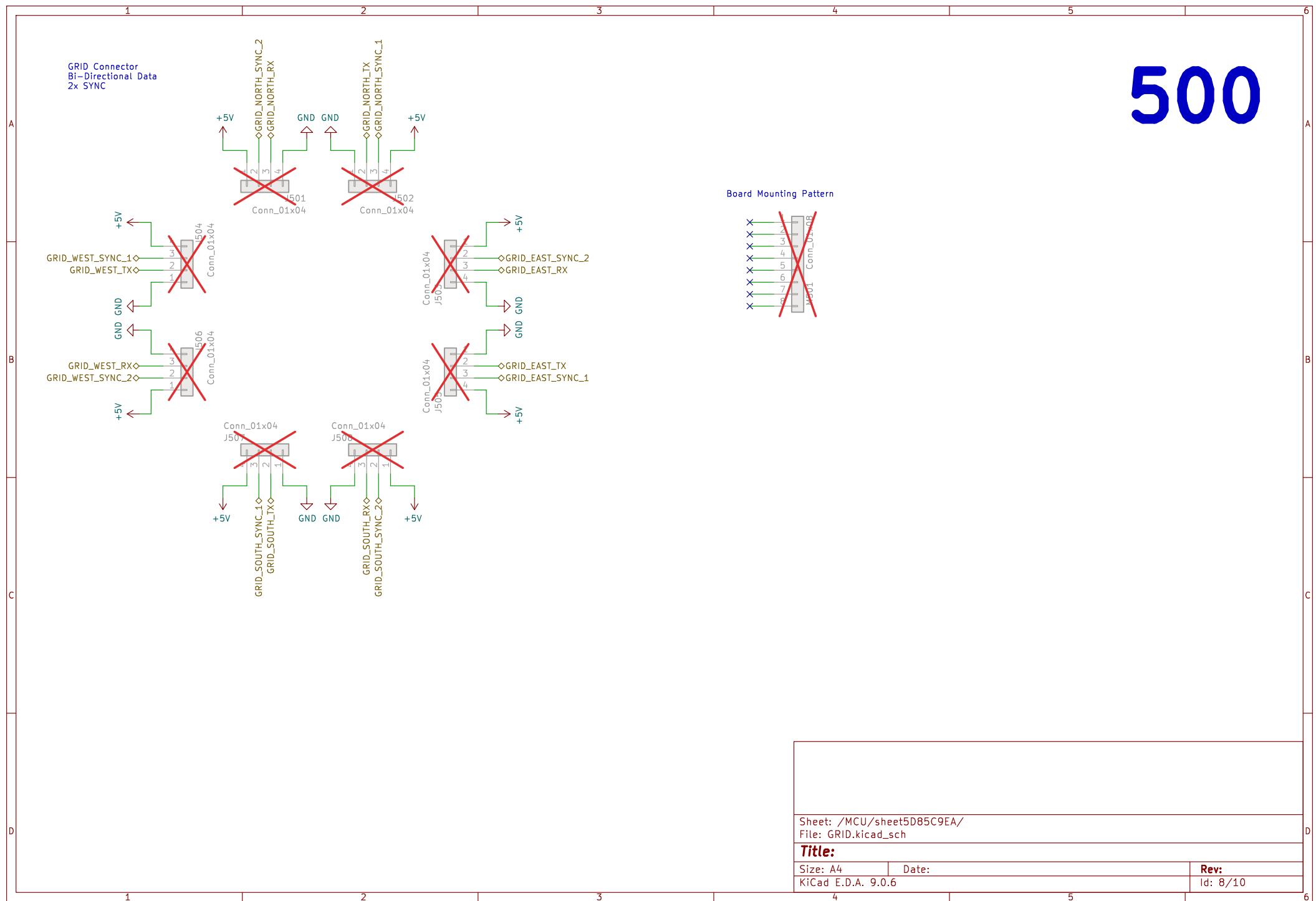
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KiCad E.D.A. 9.0.6

Rev:
Id: 6/10



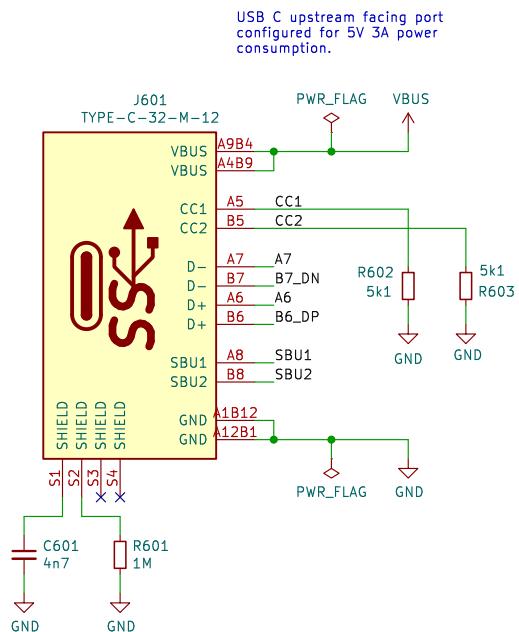
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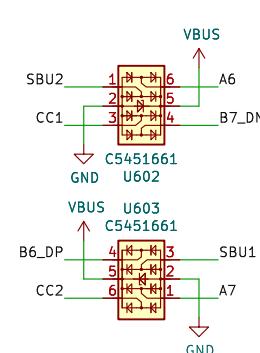
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USB Port

ESD Prot.

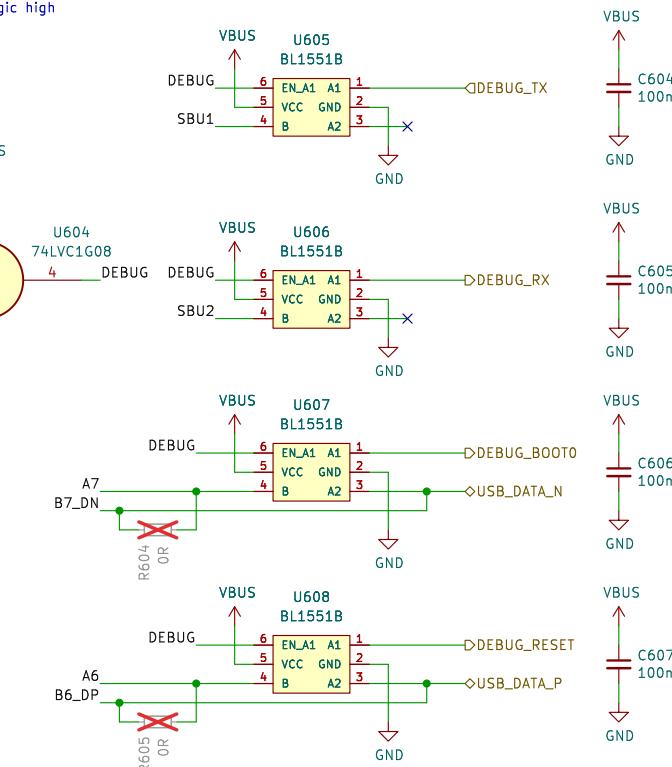


ESD protection for all 8 signals externally accessible via the USB C connector.



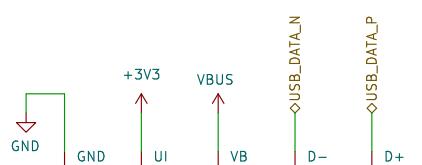
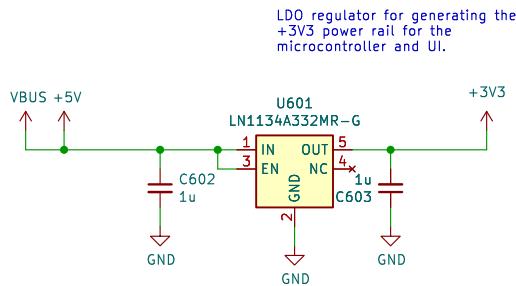
Debug-Mode Multiplexing

Debug.mode is activated when both CC1 and CC2 are pulled to logic high externally.



3V3 LDO

Testpoints



Sheet: /MCU/USB_POWER/
File: USB_POWER.kicad_sch

Title:

Size: A4 Date:
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Rev:
Id: 9/10

800

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A

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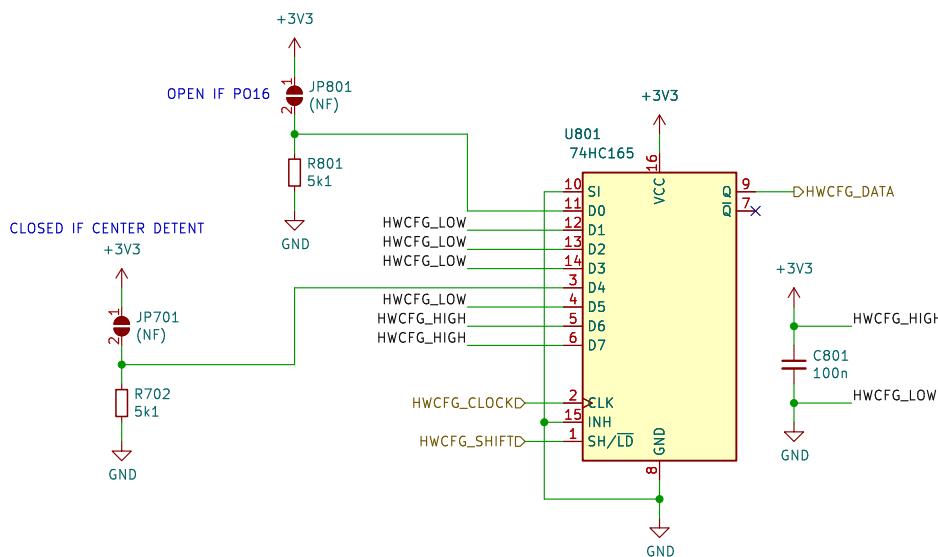
B

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D



Board Identification

Grid firmware can identify the hardware and the board revision through a 3 wire serial interface using one or more shift register as read only memory. The content of the memory is defined by pulling the inputs high or low through pcb traces or solderable configuration jumpers.

4^bModel + 4^bRevision + nb'Reserved (Multiple shift registers)

D0: MODEL (LSB)
 D1: MODEL
 D2: MODEL
 D3: MODEL (MSB)
 D4: REVISION (LSB)
 D5: REVISION
 D6: REVISION
 D7: REVISION (MSB)

Model Codes (D3–D0):

Po16 0000
 Bo16 0001
 PBf4 0010
 EN16 0011
 ...

Revision Codes (D7–D4):

RevA 0000
 RevB 0001
 RevC 0010
 RevD 0011
 ...

Sheet: /HWCFG/
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Title:

Size: A4 | Date:
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Rev:
 Id: 10/10