

FAYE ZHANG

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Please check out my portfolio! <https://intelj711.github.io/allAboutMe/allAboutMe.html>

I'm expecting a Front End Developer position with this portfolio. Experience of JAVA API and C++ API development guides me to an awareness that programming is art and it is the tool that make brilliant ideas to be seen, to be implemented and to be improved. Also programming experience gives me strong confidence that I can improve and enjoy front end developers work rapidly. Please share an opportunity with me to join in and begin my career journey.

PROFICIENCIES

- HTML/CSS/JavaScript, React-Redux, ES6, jQuery, NodeJS, Bootstrap, Google Firebase, AngularJS, Java

EDUCATION

Bachelor of Science Electrical Engineering
Jul, 2010 – May, 2012

UNIVERSITY OF MINNESOTA-TWIN CITIES

Master of Science Electrical Engineering
SEP, 2012 – May, 2014

UNIVERSITY OF MINNESOTA-TWIN CITIES

EXPERIENCE

SENIOR PRODUCT AND APPLICATION ENGINEER (SAN JOSE, CA) Jan, 2015 – Present

- **SK Hynix Memory Solution.** Reliability Software Engineering Team. System Department.
- Design, develop, optimize and execute test plans and regression test cases from functional specs of enterprise SSD product.
- Work with team to implement reliability test by creating **JAVA** API to Neosom chamber:
 - o Implement SATA and NVMe interface standard commands, SKHMS vendor unique commands and different workload loop from JAVA level based on Neosom API.
 - o Support hardware and firmware failure analysis on drive level by collecting all detailed log and tracking data.
 - o Test contents includes: Performance across different workload, SMART health monitor, read disturb, write read compare and other test cases added according to FW and HW debugging purpose.
 - o Optimize test scripts for test automation. Complete measurement with huge efficiency improvement and high accuracy.
 - o Be gate keeper of product release based on the result of reliability test of 1-day baseline, 1-week mix workload test with temperature condition changing and voltage margin, and 6-week stress RDT.
- Be responsible of data collecting and reviewing of the mass quality of Pearl product (SATA) and provided weekly report to customer. Created **Python** and **PowerShell** automation script for data parsing and analyzing. Identifying, analyzing, and documenting any defects discovered during testing. Worked with FW or HW engineer on bugs fixing. Supported further discussion with customer through the whole product mass quality.
- Created **LUA** automation script to measure commands latency and work with FW engineer to get improvement and to meet spec requirement.

VALIDATION ENGINEER INTERN (SAN JOSE, CA) Jan, 2014 – Oct. 2014

- **HGST, a West Digital Company.** Preamp Technology & Characterization Team. Validated multichannel front-end preamp performance and characterization on chip level and HDD level.
- Created **TCL automation script with GUI** to measure chip power consumption while toning through multiple register settings. Worked with validation engineer in Fujisawa head quarter on data review.

SOFTWARE ENGINEER INTERN (PLYMOUTH, MN) MAY, 2013 – Aug. 2013

- **Nonin Medical.** Firmware Team.
- Worked on development of automation of C++ script to support test of firmware functionality and validate the accuracy of hardware prototypes for non-invasive medical devices.