

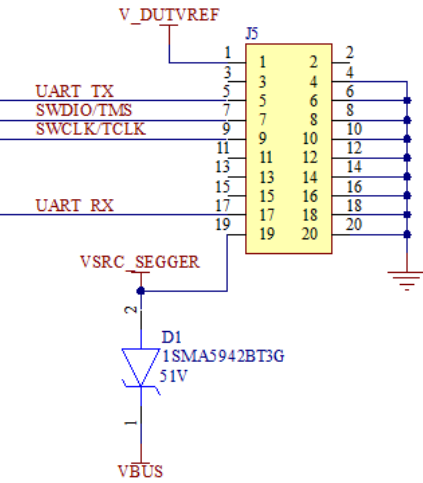
Copyright 2024 Intercreate

Licensed under the Apache License, Version 2.0 (the "License");  
you may not use this file except in compliance with the License.  
You may obtain a copy of the License at

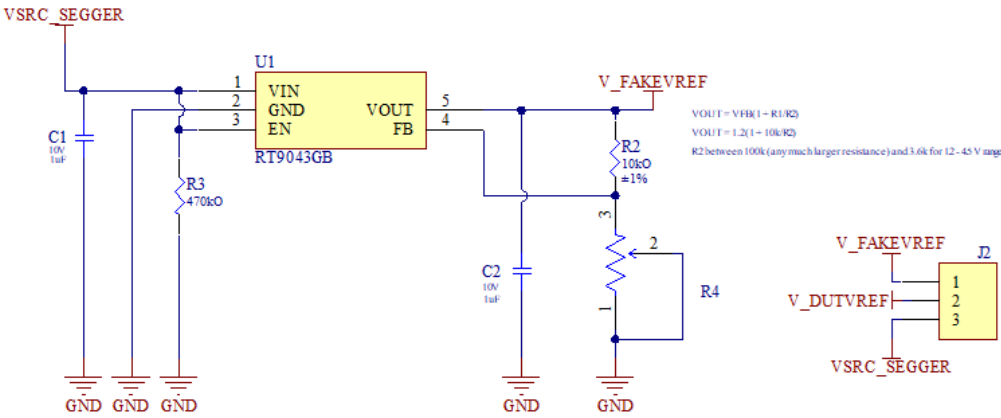
<http://www.apache.org/licenses/LICENSE-2.0>

Unless required by applicable law or agreed to in writing, design  
distributed under the License is distributed on an "AS IS" BASIS,  
WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied.  
See the License for the specific language governing permissions and  
limitations under the License.

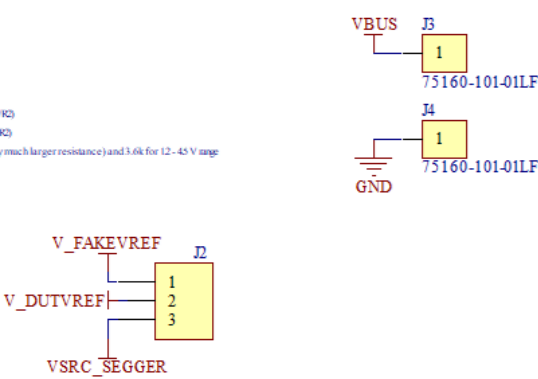
SEGGER



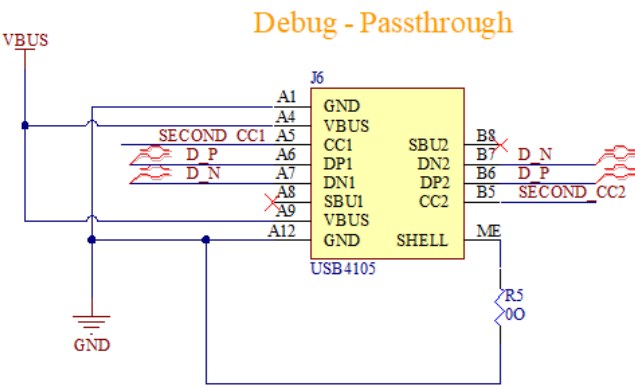
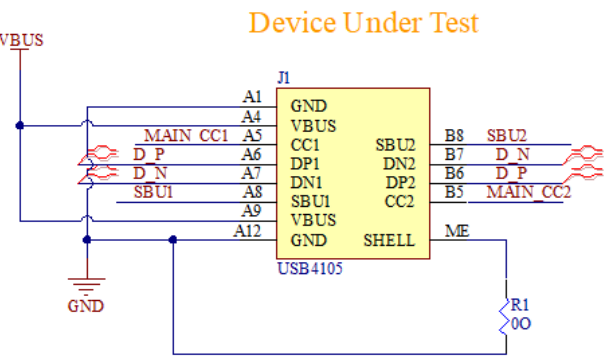
Adjustable LDO for Reference Voltage



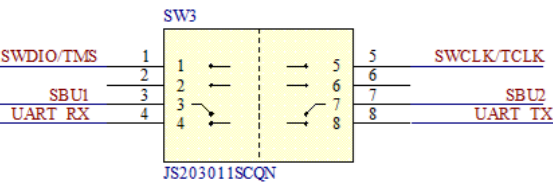
Optional Header



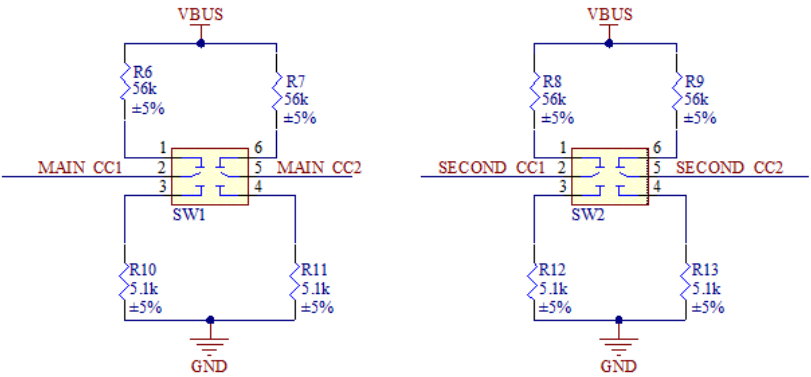
USB-C



SWD-JTAG / UART Passthrough USB-C SBU



USB-C UFP / DFP Selection

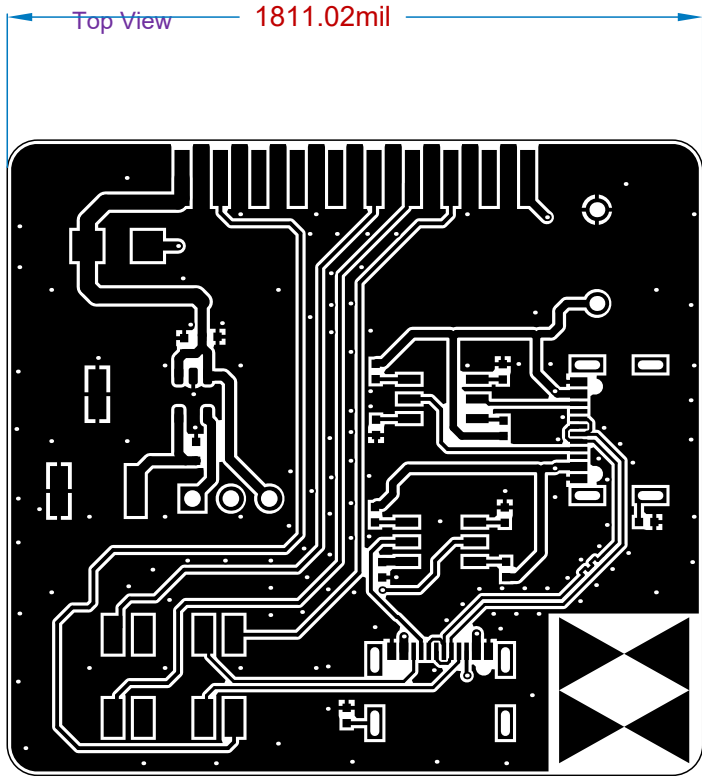


Title <i>Segger Debug Board</i>		
P/N 1		
Name Segger_TypeC_DebugBoard_v1.PcbD	Rev 1	
Engineer jeffrey@intercreate.io		
Layout jeffrey@intercreate.io		
Date 12/5/2024	Time 2:14 PM	Sheet 1 of 2
UNLESS OTHERWISE SPECIFIED, UNITS IN 0.001 INCHES		

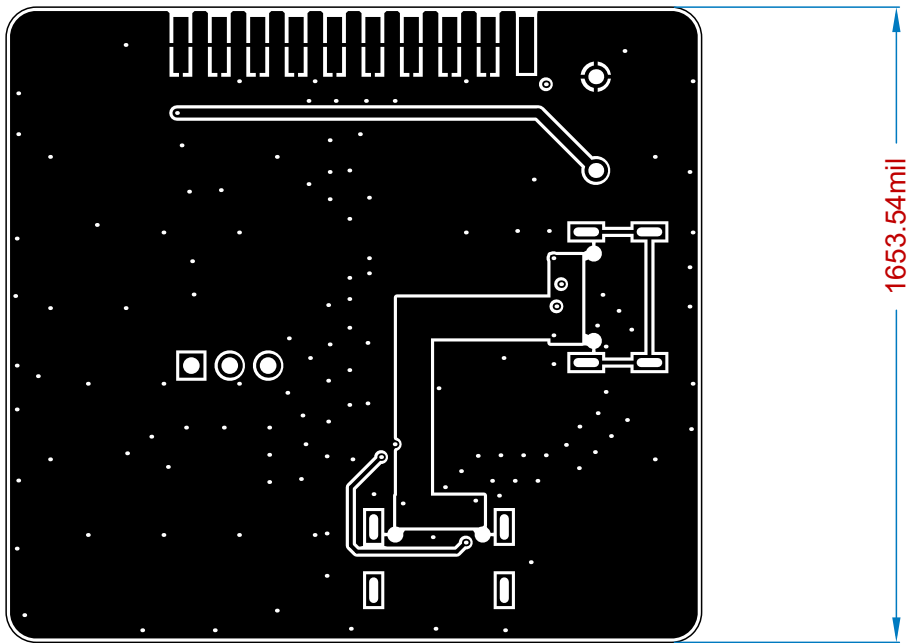
Licensed under the Apache License, Version 2.0 (the "License");  
you may not use this file except in compliance with the License.  
You may obtain a copy of the License at


<http://www.apache.org/licenses/LICENSE-2.0>

Unless required by applicable law or agreed to in writing, design  
distributed under the License is distributed on an "AS IS" BASIS,  
WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied.  
See the License for the specific language governing permissions and  
limitations under the License.



Bottom View



Title			 <b>INTERCREATE</b> 2325 3RD ST. SUITE #204 San Francisco, CA 94107
P/N 1			
Name Segger_TypeC_DebugBoard_v1.PcbD	Rev 1		
Engineer jeffrey@intercreate.io			
Layout jeffrey@intercreate.io			
Date 12/5/2024	Time 2:14 PM	Sheet 2 of 2	
UNLESS OTHERWISE SPECIFIED, UNITS IN 0.001 INCH (MIL)			