

Machine Learning and Data Analysis journal paper template*

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The paper investigates the problem of deep learning model selection. A method can consider different requirements to optimize performance on a wide range of hardware. The method allows to control trade-off between prediction quality and performance on the intermediate stage of model selection without additional model fitting. The method is based on a modification of a differential architecture search algorithm (DARTS) called DARTS-CC. The method can adapt to benefits and weaknesses of hardware, which is especially important for mobile devices with limited computation budget. To evaluate the performance of the proposed algorithm, we conduct experiments on the Fashion-MNIST and CIFAR-10 datasets using/simulating different hardware (e.g. ...) and compare the resulting architecture with architectures obtained by other neural architecture search methods.

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1 Introduction

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Discovering architectures for different problems is a difficult task. If you want to find an architecture that considers hardware properties the task becomes inhumanly hard. Optimizing for mobile devices is crucial because of their limited computation budget. With a wide variety of devices it is important to optimize searching not for one, but many different capabilities and requirements.

There are different approaches to finding architectures: evolutionary, RL (reinforced learning) and gradient-based. According to [7] gradient-based show similar prediction quality to other approaches but mainly with lower search cost.

One of the most popular algorithm is DARTS [5]. Main idea of the algorithm is to represent NN as DAG. Where each connection can be of the “candidate operations (e.g., convolution, max pooling, zero)” with different possibilities.

FBNet [3] uses this algorithm to optimize latency. Model shows great results decreasing FLOPS and CPU latency almost without decreasing accuracy.

DARTS-CC [2] uses this algorithm to optimize model complexity with trade-off control. This modification of DARTS can produce different architectures in one fitting run with control to trade-off between quality and complexity.

We propose a method that will combine approaches of DARTS-CC [2] and FBNet [3] to obtain multiple architectures optimized for different requirements with different trade-offs between prediction quality and performance in one run.

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2 Problem statement

Given a model with structure $\Gamma = (V, E)$, where E is a set of atomic operations. The task is to find a subset of edges for which the pruned model gives comparable quality with significant speed-up on a particular device.

Optimization problem:

$$\mathbb{E}_\lambda \mathbb{E}_\gamma \left[\mathcal{L}_{\text{val}}(\gamma(\lambda)) + \lambda \sum_{e \in E} T(e) \gamma(\lambda)_e \right] \rightarrow \min_{\gamma(\lambda)},$$

where \mathcal{L}_{val} is a loss function for a pruned network, $\lambda \in \mathbb{R}$ is regularization parameter, $\gamma(\lambda) \in \{0, 1\}^{|E|}$, $T(e)$ — time to execute corresponding atomic operation. Since optimization on discrete space is not differentiable we use Gumbel-Softmax approximation [2].

3 Computational experiment

The experiment contains of two parts. In the first part we get pretrained model, fix λ and then optimize γ . In the second part we train hypernetwork to find γ for arbitrary λ . We will use ResNet18 [1] as base model and dataset Cifar-10 to train and validate.

Main purpose of the first part is to approximate Λ . Λ is the max value for λ which keeps positive metrics. But before all of it we measure execution time of each module. Then for each $\lambda = \lambda_1, \lambda_2, \dots, \lambda_n$ we optimize γ for loss with time regularization. Finally, we plot accuracy versus λ . It is expected that plot will have plateau for small λ and then will decrease to worst quality for big λ .

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References

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