

```
call void @llvm.lifetime.start.p0i8(i64 4, i8* nonnull %21) #12, !dbg !381
%22 = call i32 @dpu_alloc(i32 1, i8* null, %struct.dpu_set_t* nonnull %3) #12, !dbg !382
call void @llvm.dbg.value(metadata i32 %22, metadata !296, metadata !DIExpression()), !dbg !383
-----
%209 = call i32 @dpu_free(%struct.dpu_set_t* nonnull byval(%struct.dpu_set_t) align 8 %3) #12, !dbg !553
call void @llvm.dbg.value(metadata i32 %209, metadata !374, metadata !DIExpression()), !dbg !554
%210 = icmp eq i32 %209, 0, !dbg !555
```

Host Code Modification using LLVM Pass

```
call void @llvm.lifetime.start.p0i8(i64 4, i8* nonnull %21) #12, !dbg !381
%22 = call i32 @DynRankAlloc(i32 1, i8* null, %struct.dpu_set_t* %3, i32 %0, i8** %1), !dbg !382
call void @llvm.dbg.value(metadata i32 %22, metadata !296, metadata !DIExpression()), !dbg !383
-----
%209 = call i32 @RankFree(%struct.dpu_set_t* %3), !dbg !553
call void @llvm.dbg.value(metadata i32 %209, metadata !374, metadata !DIExpression()), !dbg !554
%210 = icmp eq i32 %209, 0, !dbg !555
```