**NICOLAS DIKEN**

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Palo Alto, CA 94306

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| **EDUCATION** | |
| **San Jose State University**, College of Engineering  Master of Science, Computer Engineering  GPA: N/A | **January 2019 – Present**  Graduation: Dec 2021 |
| **San Jose State University,** College of Engineering  Bachelor of Science, Electrical Engineering  GPA: 3.45 | **August 2016 – December 2018**  Gradation: Dec 2018 |

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| **PROJECTS** | |
| **IEEE SJSU Micromouse Team**,*Team Leader* | **May 2018 – Present** |

* Designing/building a fully autonomous mouse to learn and solve a maze at high speeds using the ATSAM4SD32B (ARM Cortex-M4) MCU.
* Peripherals include: IR & ultrasonic sensors, H-Bridge motor drivers, optical encoders, and brushed DC motors.

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| **Autonomous Firefighting Robot**,*Co-Leader* | **September 2017 – May 2018** |

* Designed/built a fully autonomous firefighting robot which can navigate a room, detect a flame, and extinguish the fire using the SAMD21J18A (ARM Cortex-M0+) MCU.
* Peripherals included: ultrasonic sensors, H-bridge motor drivers, stepper motors, brushed DC motors, and a fire detection system which utilized an array of IR diodes.

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| **Embedded Control System Design**,*Student Designer* | **August 2017 – December 2017** |

* Wrote drivers for various analog and digital components using GPIO, timer counters, digital filter implementations, PID controllers, interrupts, and I2C. Devices included motors, displays, and I2C controlled devices all in conjunction.

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| **Fully Automated Dormitory,** *Student Designer* | **October 2014 – May 2015** |

* Utilized Raspberry Pi technology to automate a room using python.
* Automated low voltage devices, e.g. LED, speakers, stepper motors, etc. to follow an alarm and virtual timer.

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| **WORK EXPERIENCE** | | |
|  | | **August 2018 – Present** |
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| **Maxim Integrated,** San Jose CA, *EE-Sim Engineering Intern* | **May 2018 – August 2018** | |

* Used python and DataNitro in combination with SIMPLIS/SIMetrix (a SPICE-based power circuit simulator) to create a tool to fully automate testing & data extraction of any SPICE-based power system design.
* This also included stepping of circuit parameters, specs, measurements, and component values to create a seamless tool which allows for infinite expandability & applications.

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| **Nokia ION,** Mountainview CA, *Electrical Engineering* *Intern* | **May 2017 – August 2017** |

* Performed a series of tests analyzing serial communication signals (I2C, SPI) as well as a complete DVT on an array of high power DC/DC converters. Also worked in a team to design an autonomous fan control system using the MAX31740 Fan-Speed Controller.

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| **Sentient Energy,** Burlingame CA, *Electrical Engineering Intern* | **July 2016 – August 2016** |

* Conducted testing on Sentient Energy’s flagship device, MM3, specifically the current measurement system.
* Improved accuracy of the current measurement system through hardware implementations.

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| **SPECIALIZED SKILLS** |

**Equipment:** Soldering (SMD), oscilloscope, function generator, frequency analyzer, programmable electronic loads

**Hardware:** SAMD20J18/21G18 (Cortex-M0+), ATSAM4SD32B (Cortex-M4), Arduino, Raspberry Pi

**Software:** Atmel Studio 7.0, Visual Studio, SIMPLIS/SIMetrix Simulator, LTspice XVII, Allegro PCB Viewer, Mentor Graphics DxDesigner, Linux Command Line

**Languages:** C, python, ARM-Assembly (Cortex-M4), C++, DataNitro (Excel-python)

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| **EXPERIENCES** |

**IEEE,** San Jose State University, *Chair, Vice Chair* **August 2016- Present**

**DIY Engineers,** University of Florida, *Member* **September 2014- May 2015**

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| **RELEVANT COURSEWORK** |

Embedded Control System Design **●** Analog Peripherals for Embedded Systems **●** Microprocessor Based System Design **●** Electric Machines and Drives **●** Circuits and Systems **●** Introduction to Signal Processing **●** Digital Logic Circuit Design **●** Physical Electronics (Semiconductors) **●** Optical and Magnetic Properties of Materials