

Trasarea execuției programului de test pentru MIPS32

Valorile se completează în hexazecimal așa cum trebuie să apară pe SSD. Succesiunea pașilor reprezintă ordinea de execuție în timp la apăsarea butonului ENable. **Pasul 0 corespunde stării inițiale a circuitului (PC = 0), iar pasul N caracterizează starea după apăsarea de N ori a butonului ENable.** Inițial registrele vor avea valoarea 0 (care se atribuie automat în lipsa unei inițializări explicite a RF), iar memoria de date RAM poate fi inițializată cu valori dorite. Tabelul se completează pentru tot programul sau, dacă are buclă, până la finalul primei iterații. *Buclă = revenirea execuției la o instrucțiune care a mai fost executată anterior.*

Pas	SW(7:5)	"000"	"001"	"010"	"011"	"100"	"101"	"110"	"111"	De completat numai pentru instrucțiuni de salt	
	Instr (în asamblare)	Instr (hexa)	PC+4	RD1	RD2	Ext_Imm	ALURes	MemData	WD	BranchAddr	JumpAddr
0	LW \$1, 0(\$zero)	X"8C010000"	X"00000004"	X"00000000"	X"00000000"	X"00000000"	X"00000000"	X"00000040"	X"00000040"	X""	X""
1	LW \$2, 4(\$zero)	X"8C020004"	X"00000008"	X"00000000"	X"00000000"	X"00000004"	X"00000004"	X"00000018"	X"00000018"		
2	ADDI \$3, \$zero, 8	X"20030008"	X"0000000C"	X"00000000"	X"00000000"	X"00000008"	X"00000008"	X"00000000"	X"00000008"		
3	SW \$1, 0(\$3)	X"AC610000"	X"00000010"	X"00000008"	X"00000040"	X"00000000"	X"00000008"	X"00000000"	X"00000008"		
4	ADDI \$3, \$3, 4	X"20630004"	X"00000014"	X"00000008"	X"00000008"	X"00000004"	X"0000000C"	X"00000000"	X"0000000C"		
5	SW \$2, 0(\$3)	X"AC620000"	X"00000018"	X"0000000C"	X"00000018"	X"00000000"	X"0000000C"	X"00000000"	X"0000000C"		
6	ADDI \$3, \$3, 4	X"20630004"	X"0000001C"	X"0000000C"	X"0000000C"	X"00000004"	X"00000010"	X"00000000"	X"00000010"		
7	BEQ \$1, \$2, done	X"1022000B"	X"00000020"	X"00000040"	X"00000018"	X"0000000B"	X"00000028"	X"00000000"	X"00000028"	X"0000000B"	
8	SUB \$4, \$1, \$2	X"00222022"	X"00000024"	X"00000040"	X"00000018"	X"00002022"	X"00000028"	X"00000000"	X"00000028"		
9	SRA \$5, \$4, 31	X"00042FC3"	X"00000028"	X"00000000"	X"00000028"	X"00002FC3"	X"00000000"	X"00000040"	X"00000000"		
10	BEQ \$5, \$zero, update_x	X"10A00004"	X"0000002C"	X"00000000"	X"00000000"	X"00000004"	X"00000000"	X"00000040"	X"00000000"	X"00000004"	
11	SUB \$1, \$1, \$2	X"00220822"	X"00000040"	X"00000040"	X"00000018"	X"00000822"	X"00000028"	X"00000000"	X"00000028"		
12	SW \$1, 0(\$3)	X"AC610000"	X"00000044"	X"00000010"	X"00000028"	X"00000000"	X"00000010"	X"00000000"	X"00000010"		
13	ADDI \$3, \$3, 4	X"20630004"	X"00000048"	X"00000010"	X"00000010"	X"00000004"	X"00000014"	X"00000000"	X"00000014"		
14	J euclid	X"08000007"	X"0000004C"	X"00000000"	X"00000000"	X"00000007"	X"00000000"	X"00000040"	X"00000000"		X"00000007"
15	BEQ \$1, \$2, done	X"1022000B"	X"00000020"	X"00000028"	X"00000018"	X"0000000B"	X"00000010"	X"00000028"	X"00000010"	X"0000000B"	
16	SUB \$4, \$1, \$2	X"00222022"	X"00000024"	X"00000028"	X"00000018"	X"00002022"	X"00000010"	X"00000028"	X"00000010"		
17	SRA \$5, \$4, 31	X"00042FC3"	X"00000028"	X"00000000"	X"00000010"	X"00002FC3"	X"00000000"	X"00000040"	X"00000000"		
18	BEQ \$5, \$zero, update_x	X"10A00004"	X"0000002C"	X"00000000"	X"00000000"	X"00000004"	X"00000000"	X"00000040"	X"00000000"	X"00000004"	
19	SUB \$1, \$1, \$2	X"00220822"	X"00000040"	X"00000028"	X"00000018"	X"00000822"	X"00000010"	X"00000028"	X"00000010"		
20	SW \$1, 0(\$3)	X"AC610000"	X"00000044"	X"00000014"	X"00000010"	X"00000000"	X"00000014"	X"00000000"	X"00000014"		
21	ADDI \$3, \$3, 4	X"20630004"	X"00000048"	X"00000014"	X"00000014"	X"00000004"	X"00000018"	X"00000000"	X"00000018"		
22	J euclid	X"08000007"	X"0000004C"	X"00000000"	X"00000000"	X"00000007"	X"00000000"	X"00000040"	X"00000000"		X"00000007"
24	BEQ \$1, \$2, done	X"1022000B"	X"00000020"	X"00000010"	X"00000018"	X"0000000B"	X"FFFFFFF8"	X"00000000"	X"FFFFFFF8"		
25	SUB \$4, \$1, \$2	X"00222022"	X"00000024"	X"00000010"	X"00000018"	X"00002022"	X"FFFFFFF8"	X"00000000"	X"FFFFFFF8"		
26	SRA \$5, \$4, 31	X"00042FC3"	X"00000028"	X"00000000"	X"FFFFFFF8"	X"00002FC3"	X"FFFFFFF8"	X"00000000"	X"FFFFFFF8"		

27	BEQ \$5, \$zero, update_x	X"10A00004"	X" 0000002C"	X"FFFFFFF"	X" 00000000"	X"00000004"	X"FFFFFFF"	X" 00000000"	X"FFFFFFF"	X"00000004"		
28	SUB \$2, \$2, \$1	X"00411022"	X" 00000030"	X" 00000018"	X" 00000010"	X" 00001022"	X" 00000008"	X" 00000040"	X" 00000008"			
29	SW \$1, 0(\$3)	X"AC620000"	X" 00000034"	X" 00000018"	X" 00000008"	X" 00000000"	X" 00000018"	X" 00000000"	X" 00000018"			
30	ADDI \$3, \$3, 4	X" 20630004"	X" 00000038"	X" 00000018"	X" 00000018"	X" 00000004"	X" 0000001C"	X" 00000000"	X" 0000001C"			
31	J euclid	X" 08000007"	X" 0000003C"	X" 00000000"	X" 00000000"	X" 00000007"	X" 00000000"	X" 00000040"	X" 00000000"		X" 00000007"	
32	BEQ \$1, \$2, done	X"1022000B"	X" 00000020"	X" 00000010"	X" 00000008"	X" 0000000B"	X" 00000008"	X" 00000040"	X" 00000008"	X" 0000000B"		
33	SUB \$4, \$1, \$2	X"00222022"	X" 00000024"	X" 00000010"	X" 00000008"	X" 00002022"	X" 00000008"	X" 00000040"	X" 00000008"			
34	SRA \$5, \$4, 31	X"00042FC3"	X" 00000028"	X" 00000000"	X" 00000008"	X"00002FC3"	X" 00000000"	X" 00000040"	X" 00000000"			
35	BEQ \$5, \$zero, update_x	X"10A00004"	X" 0000002C"	X" 00000000"	X" 00000000"	X" 00000004"	X" 00000000"	X" 00000040"	X" 00000000"	X" 00000004"		
36	SUB \$1, \$1, \$2	X"00220822"	X" 00000040"	X" 00000010"	X" 00000008"	X" 00000822"	X" 00000008"	X" 00000040"	X" 00000008"			
37	SW \$1, 0(\$3)	X"AC610000"	X" 00000044"	X" 0000001C"	X" 00000008"	X" 00000000"	X" 0000001C"	X" 00000000"	X" 0000001C"			
38	ADDI \$3, \$3, 4	X" 20630004"	X" 00000048"	X" 0000001C"	X" 0000001C"	X" 00000004"	X" 00000020"	X" 00000000"	X" 00000020"			
39	J euclid	X" 08000007"	X" 0000004C"	X" 00000000"	X" 00000000"	X" 00000007"	X" 00000000"	X" 00000040"	X" 00000000"		X" 00000007"	
40	BEQ \$1, \$2, done	X"1022000B"	X" 00000020"	X" 00000008"	X" 00000008"	X" 0000000B"	X" 00000000"	X" 00000040"	X" 00000000"			
41	SW \$1, 0(\$3)	X"AC610000"	X" 00000050"	X" 00000020"	X" 00000008"	X" 00000000"	X" 00000020"	X" 00000000"	X" 00000020"			
42	ADDI \$3, \$3, 4	X" 20630004"	X" 00000054"	X" 00000020"	X" 00000020"	X" 00000004"	X" 00000024"	X" 00000000"	X" 00000024"			

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