**Trasarea execuției programului de test pentru MIPS32**

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Pas** | **SW(7:5)** | "000" | "001" | "010" | "011" | "100" | "101" | "110" | "111" | **De completat numai pentru instrucțiuni de salt** | |
| **Instr** (*în asamblare*) | **Instr** (*hexa*) | **PC+4** | **RD1** | **RD2** | **Ext\_Imm** | **ALURes** | **MemData** | **WD** | **BranchAddr** | **JumpAddr** |
| 0 | ADD $1,$0,$0 | X"00000801" | X"00000004" | X"00000000" | X"00000000" | X"00000801" | X"00000000" | X"0000000c" | X"00000000" | - | - |
| 1 | LW $3, 4($0) | X"88030004" | X"00000008" | X"00000000" | X"00000008" | X"00000004" | X"00000004" | X"00000008" | X"00000008" | - | - |
| 2 | ADD $2,$0,$0 | X"00001001" | X"0000000C" | X"00000000" | X"00000000" | X"00001001" | X"00000000" | X"0000000c" | X"00000000" | - | - |
| 3 | ADDI $5,$0, -999 | X"2005FF9D" | X"00000010" | X"00000000" | X"FFFFFF9D" | X" FFFFFF9D " | X"FFFFFF9D" | X"00000000" | X"FFFFFF9D" | - | - |
| 4 | LW $4, 0($0) | X"88040000" | X"00000014" | X"00000000" | X"0000000c" | X"00000000" | X"00000000" | X"0000000c" | X"0000000c" | - | - |
| 5 | BEQ $1,$3 13 | X"1023000D" | X"00000018" | X"00000000" | X"00000008" | X"0000000d" | X" FFFFFFF8" | X"00000000" | X"FFFFFFF8" | X"0000004C" | - |
| 6 | LW $6,0($4) | X"88860000" | X"0000001C" | X"0000000c" | X"0000000e" | X"00000000" | X"0000000c" | X"0000000e" | X"0000000e" | - | - |
| 7 | ADDI $7,$0,1 | X"20070001" | X"00000020" | X"00000000" | X"00000001" | X"00000001" | X"00000001" | X"00000000" | X"00000001" | - | - |
| 8 | AND $8,$7,$6 | X"00E64020" | X"00000024" | X"00000001" | X"0000000e" | X"00003820" | X"00000000" | X"0000000c" | X"00000000" | - | - |
| 9 | ADDI $9,$1,1 | X"20290001" | X"00000028" | X"00000000" | X"00000001" | X"00000001" | X"00000001" | X"00000000" | X"00000001" | - | - |
| 10 | ADD $1,$9,$0 | X"01200801" | X"0000002C" | X"00000001" | X"00000000" | X"00000801" | X"00000001" | X"00000000" | X"00000001" | - | - |
| 11 | ADDI $10,$4,1 | X"208A0001" | X"00000030" | X"0000000C" | X"0000000d" | X"00000001" | X"0000000D" | X"00000007" | X"0000000D" | - | - |
| 12 | ADD $4,$10,$0 | X"01402001" | X"00000034" | X"0000000D" | X"00000000" | X"00002001" | X"0000000D" | X"00000007" | X"0000000D" | - | - |
| 13 | BEQ $7,$0,1 | X"11000001" | X"00000038" | X"00000000" | X"00000000" | X"00000001" | X"00000000" | X"0000000c" | X"00000000" | X"0000003C" | - |
| Se sare | J 5 | X"08000005" | X"0000003c" | X"00000000" | X"00000000" | X"00000005" | X"00000000" | X"0000000c" | X"00000000" | - | X"00000005" |
| 14 | SLT $7,$5,$6 | X"00A6380A" | X"00000040" | X"0000FF9D "  FFFFFF9D | X"0000000e" | X"0000380A" | X"00000001" | X"00000000" | X"00000001" | - | - |
| 15 | BEQ $7,$0,1 | X"10E00001" | X"00000044" | X"00000001" | X"00000000" | X"00000001" | X"0000001" | X"0000000c" | X"00000001" | X"00000048" | - |
| 16 | ADD $5,$6,$0 | X"00C02801" | X"00000048" | X"0000000e" | X"00000000" | X"00002801" | X"0000000e " | X"00000006" | X"0000000e " | - | - |
| 17 | J 5 | X"08000005" | X"0000004c" | X"00000000" | X"00000000" | X"00000005" | X"00000000" | X"0000000c" | X"00000000" | - | X"00000005" |
|  | Finalul primei iteratii |  |  |  |  |  |  |  |  |  |  |
| la final | SW $5,8($0) | X"8C050008" | X"00000050" | X"00000000" | X"00000014" | X"00000008" | X"00000008" | X"00000014" | X"00000008" | - | - |
|  |  |  |  |  |  |  |  |  |  |  |  |