

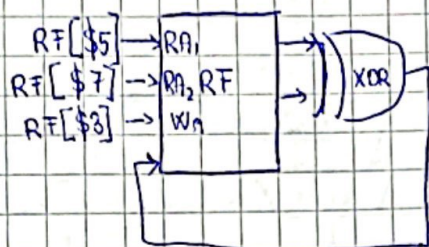
• Instrucțiunea XOR

XOR \$rd \$rs \$rt

ex: XOR \$3 \$5 \$7

 $RF[\$3] \leftarrow RF[\$5] \wedge RF[\$7]$ 

B" 000 - 101 - 111 - 011 - 0 - 110 "

• Instrucțiunea SLT - dacă  $\$rs < \$rt$ , \$rd e inițializat cu 1, altfel cu 0

SLT \$rd \$rs \$rt

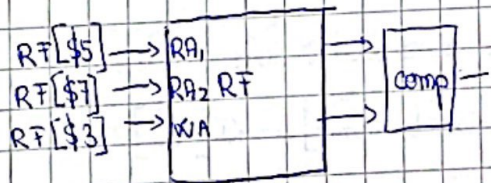
ex: SLT \$3 \$5 \$7

if  $RF[\$rs] < RF[\$rt]$  $RF[\$rd] \leftarrow 1;$ 

else

 $RF[\$rd] \leftarrow 0;$ 

B" 000 - 101 - 111 - 011 - 0 - 111 "





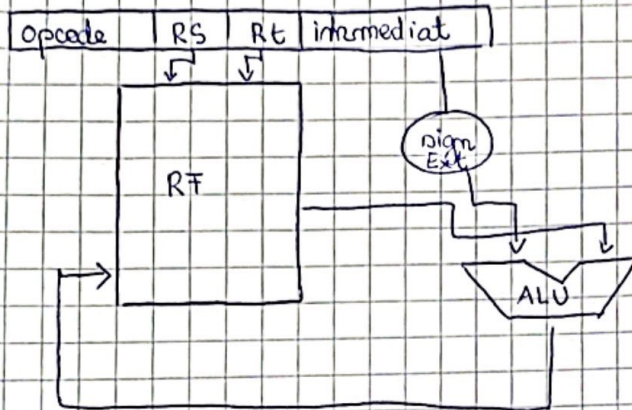
- Instrucțiunea ANDi - logical AND unsigned constant

andi \$rt \$rs imm

$RF[rt] \leftarrow RF[rs] \& Z\_Ext(imm)$

Ex: andi \$2 \$3 1

B" 101-011-010-0000001"



- Instrucțiunea bltz - branch on less than zero

• bltz \$rs, imm

• if ( $RF[rs] < 0$ ) then

$PC \leftarrow PC + 1 + S\_Ext(imm) \ll 2$

ex: bltz \$7, 5

• B" 110-111-000-0000101"

opcode	rs	rt	imm
110	111	000	0000101

