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DISCLAIMER

Raspberry Pi is a trademark of the Raspberry Pi Foundation

This is not a replacement for the official schematic and it was created for information and educational purposes only.

The information inside this schematic was created by using a best effort approach and making many assumptions (which may or may not be correct), therefore, there is no warranty about it's accuracy.

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Probably contains errors.
Minimal or no component values
Created by reverse engineering the PCB
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File: DISCLAIMER.sch

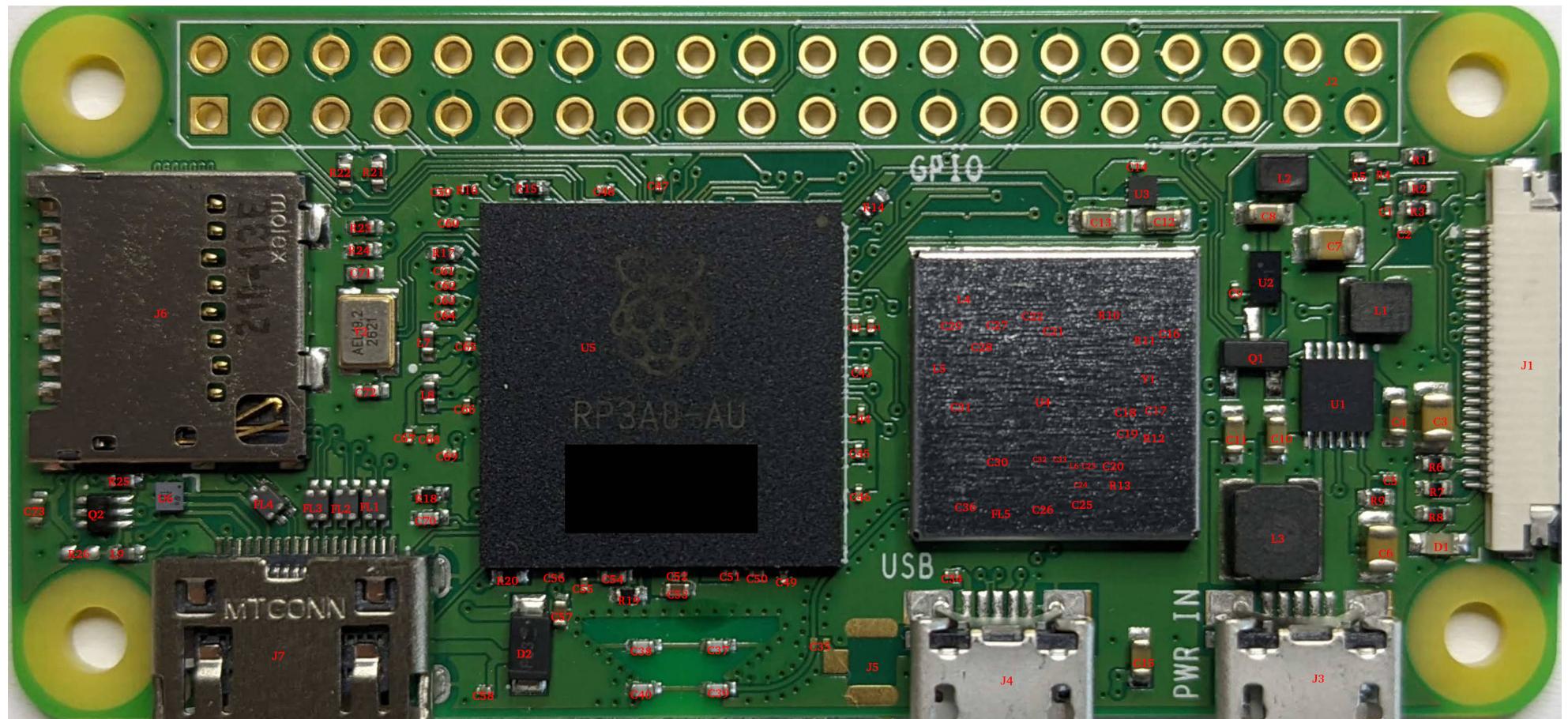
Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4 Date: 2022-01-15

KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1

Rev: V1.0

Id: 2/15



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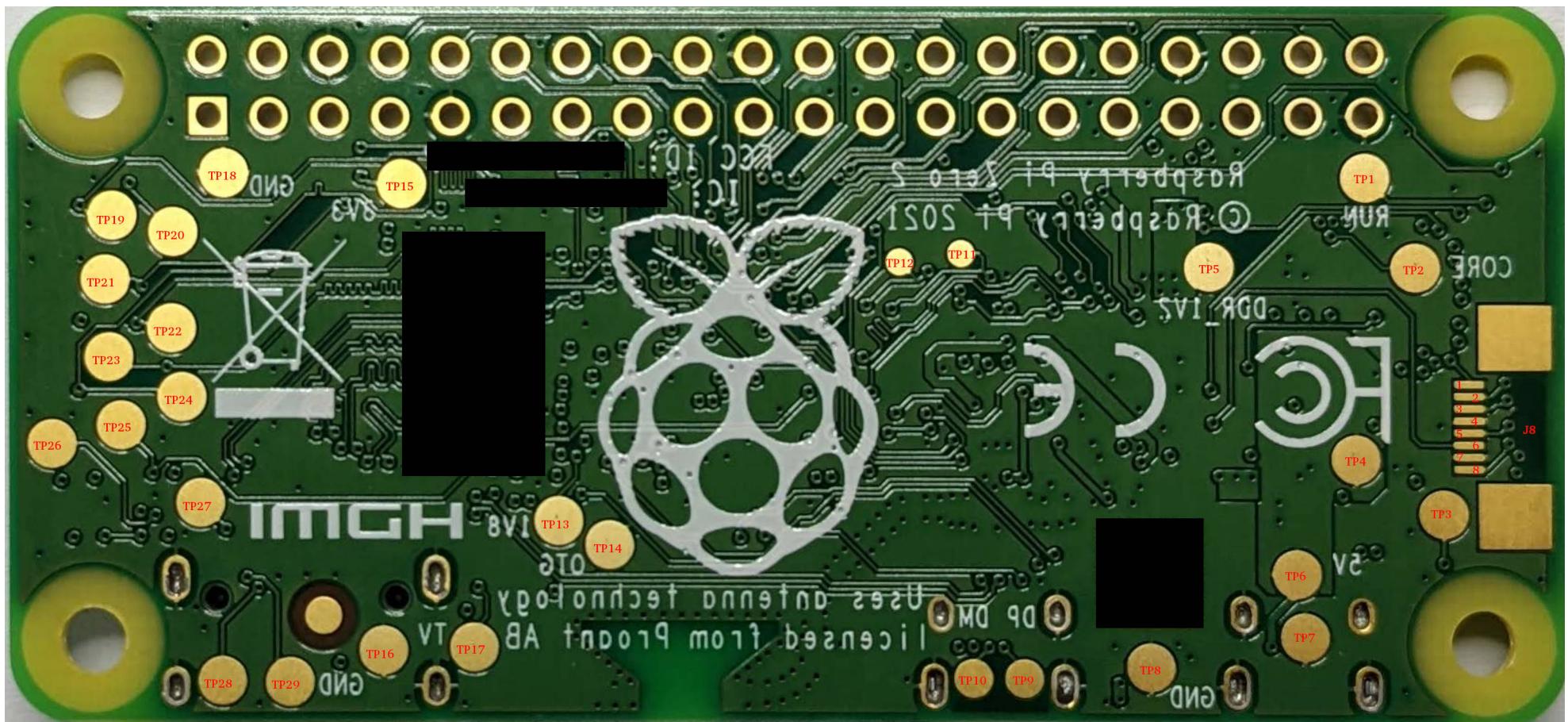
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File: BOTTOM_DESIGNATORS.sch

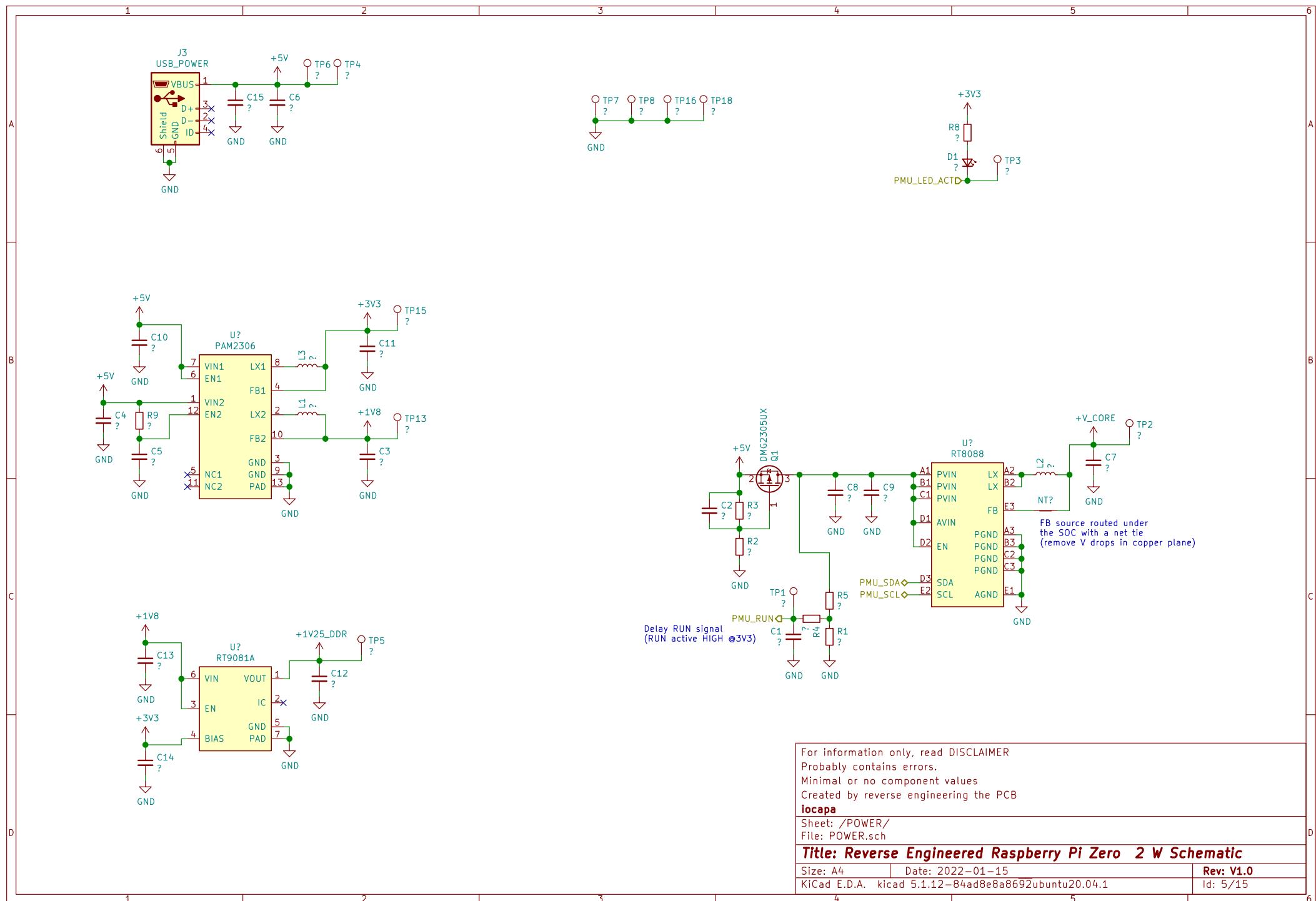
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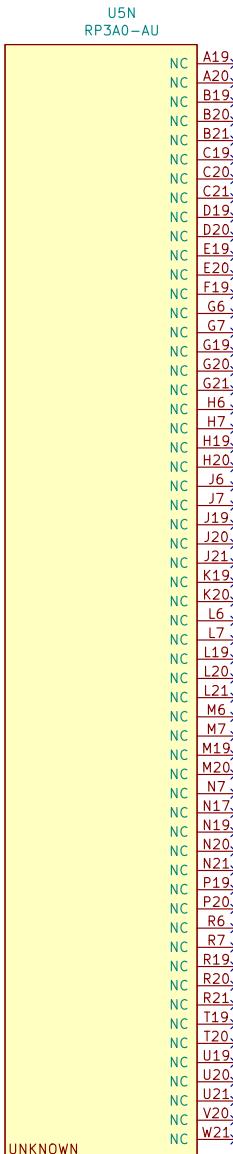
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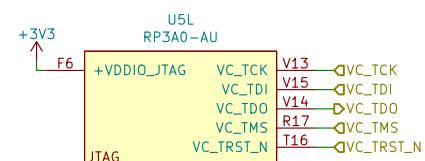
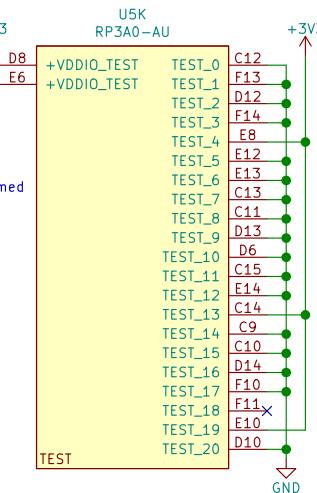
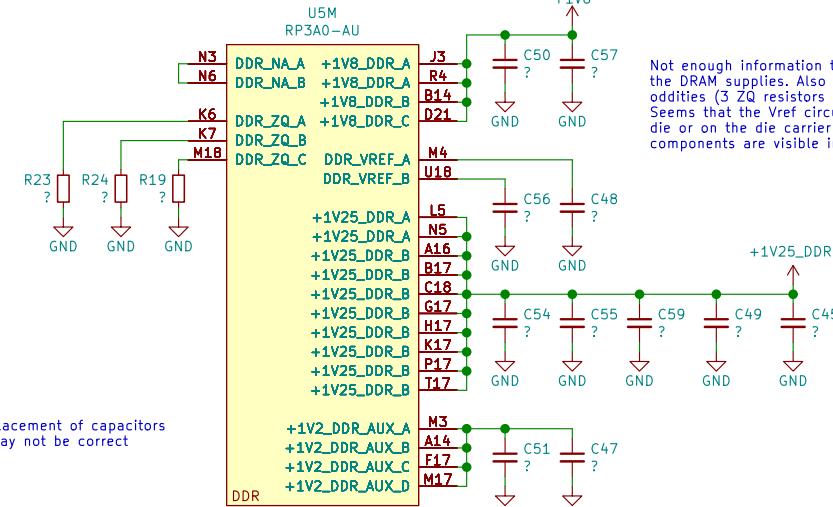
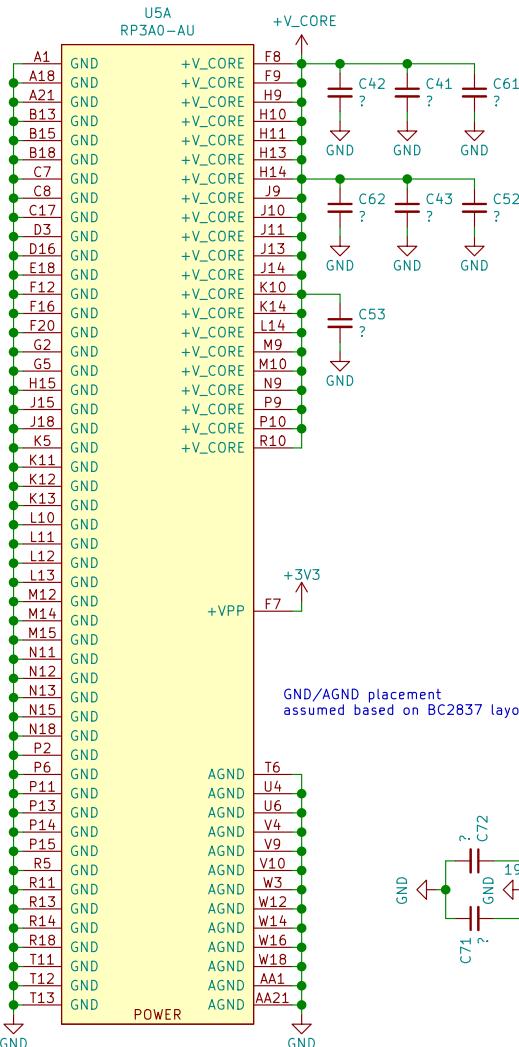
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1 2 3 4 5 6



These are probably DRAM related
High probability that the SOC
has provisioning for an extra 512M
DRAM die



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Sheet: /SOC_SYS/
File: SOC_SYS.sch

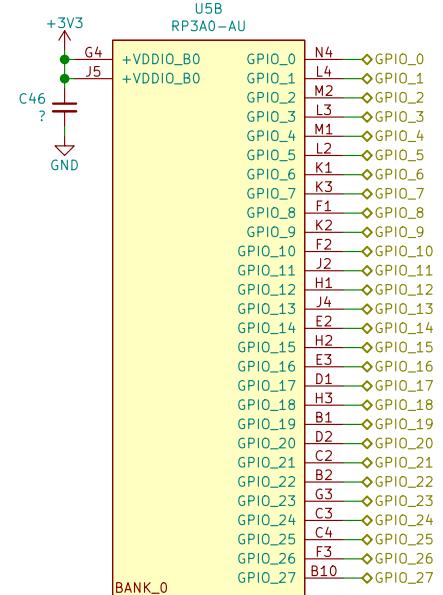
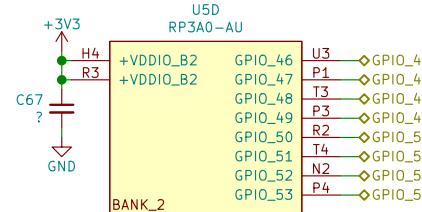
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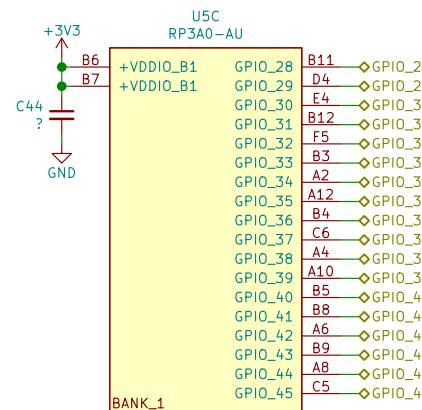
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Sheet: /SOC_GPIO/
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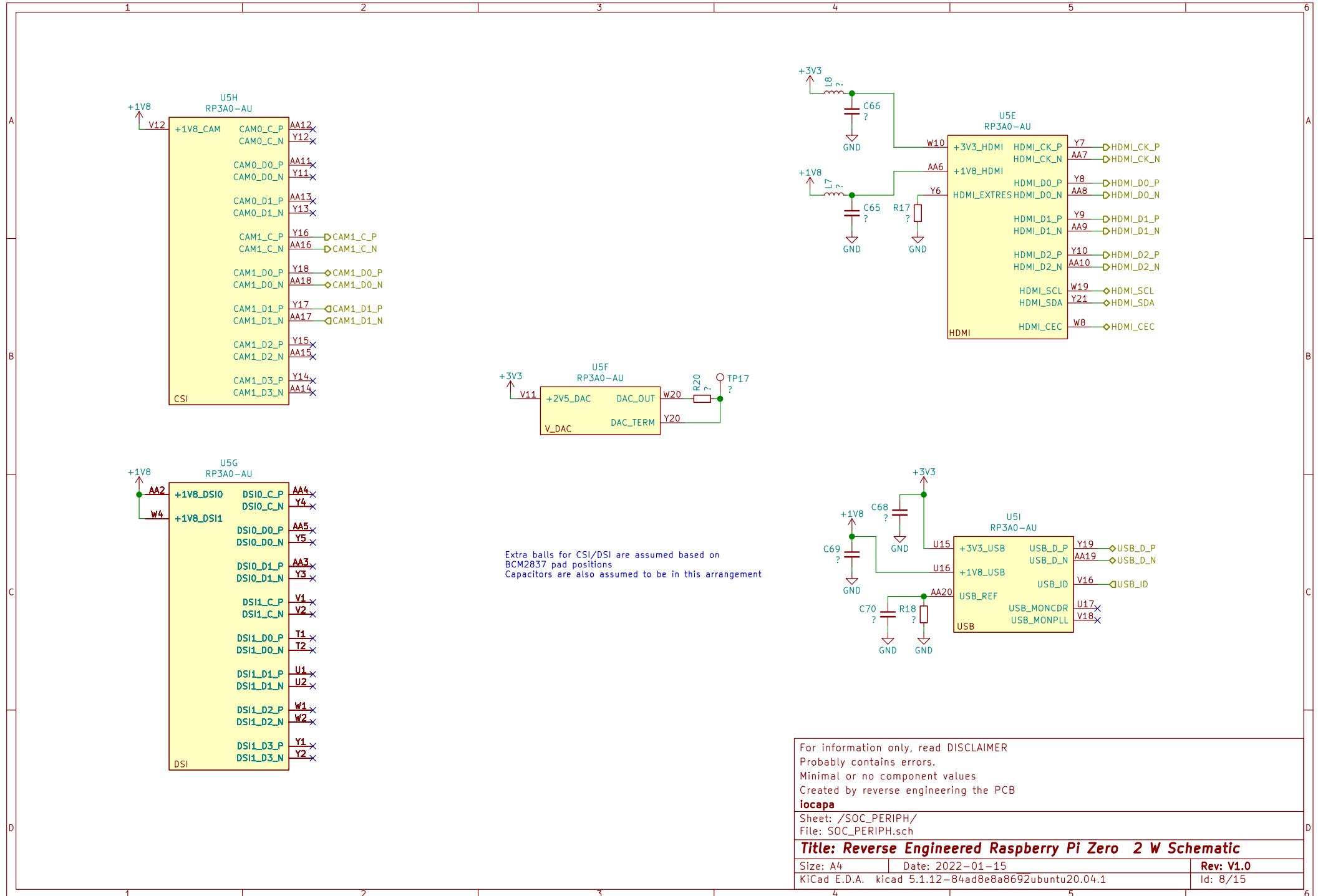
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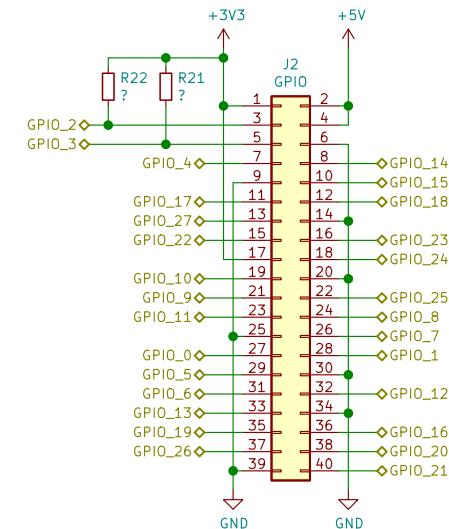
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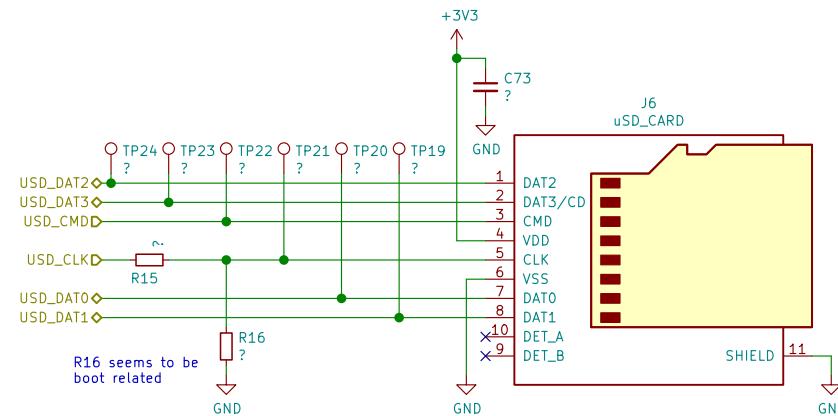


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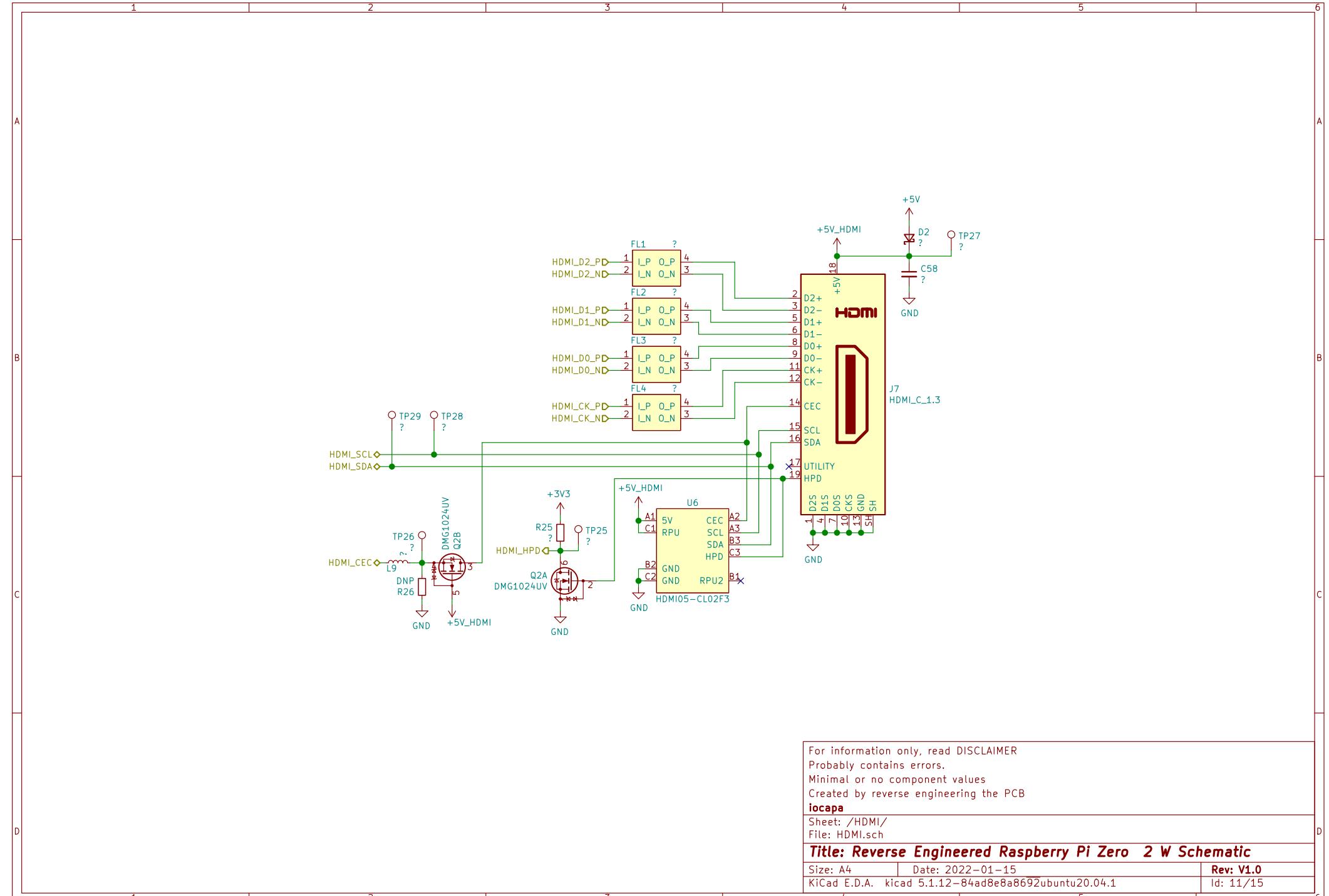
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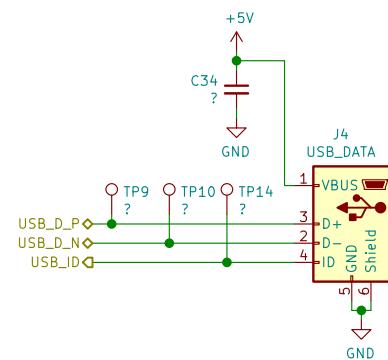
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Sheet: /USB/

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Rev: V1.0

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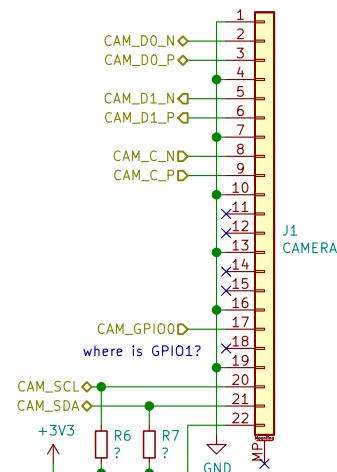
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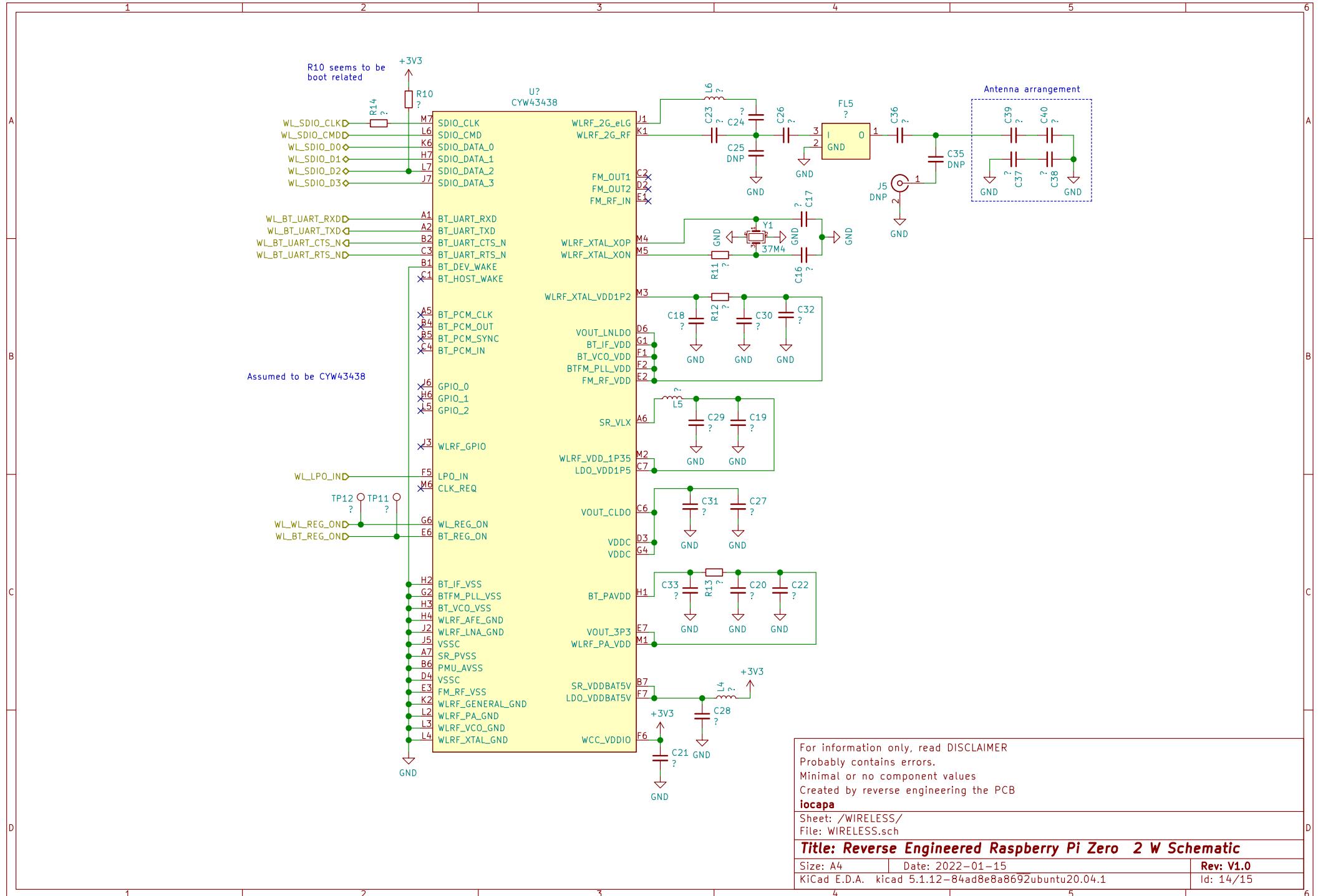
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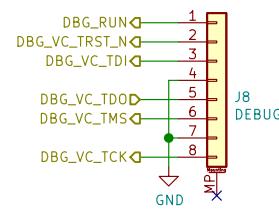
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Sheet: /DEBUG/
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