CS MISIB HW3 Junhong Wang 1504941113)

4- bit CLA

$$\begin{array}{c|c}
C_2 \\
\downarrow \\
A_2 \rightarrow \downarrow \\
b_2 \rightarrow \downarrow \\
D_2 \rightarrow D_2 \rightarrow D_2$$

$$S_0 = a_0 \oplus b_0 \oplus c_0 \oplus \overline{a}$$

$$C_1 = G_0 + P_0 \cdot C_0$$
 $\frac{27}{27} = \frac{27}{67}$

$$C_2 = G_1 + G_0 \cdot P_1 + C_0 \cdot P_0 \cdot P_1$$

$$\frac{27}{47} \quad \frac{27}{57} \quad \frac{27}{57}$$

$$S_2 = \underbrace{a_2 \Theta}_{0} \underbrace{b_2 \Theta}_{87} \underbrace{C_2}_{0}$$

$$C_{3} = \frac{G_{2} + G_{1} \cdot P_{2} + G_{0} \cdot P_{1} \cdot P_{2} + C_{0} \cdot P_{0} \cdot P_{1} \cdot P_{2}}{27 \quad 27 \quad 27 \quad 27 \quad 27}$$

$$\frac{27}{47} \quad \frac{27}{57} \quad \frac{27}{77} \quad \frac{27}{77$$

(12T)

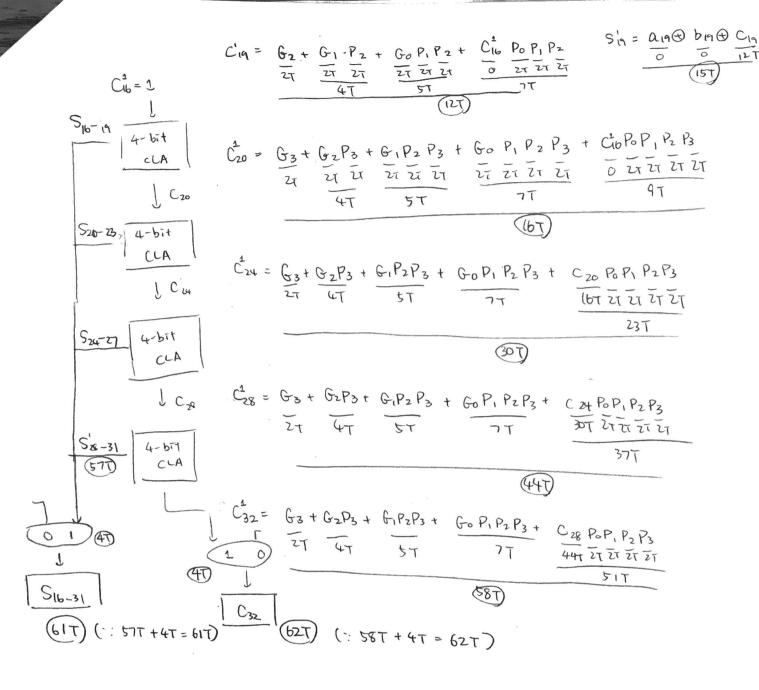
$$S_3 = \underbrace{a_3 \oplus b_3 \oplus C_3}_{0}$$

$$C_4 = G_3 + P_3 \cdot C_3$$

$$\underbrace{C_5}_{0}$$

$$\begin{cases} C_{R} \\ J \\ -S_{R} = 19 \\ \longrightarrow G_{S} \\ \longrightarrow P_{S} \end{cases}$$

Q4T)



$$S_{31}^{1} = \underbrace{\begin{array}{ccc} \Omega_{31} \oplus & b_{31} \oplus & C_{31} \\ \hline 0 & \hline & & & \\ \hline \end{array}}_{577}$$

Therefore,

Out put	pelay
GO	2+
ро	2.T
6-2	127
Pa	77
CIZ	207
C 15	30 T
CIB	247
515	7 25
C 20	167
SI9	157
cst	(31)
31	30 T
	547
C32 (after mux)	627
S31 (after mux)	617

Maximal Delay: 62T

DISCLAIMER: Delays are computed as sum of products. For example, delay of attazas + atasao + is computed as the sum of the delay of a, delay of azas as a whole, delay of agasab as a whole.