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Compact radio-frequency resonator for cryogenic ion traps

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We report on the investigation and implementation of a lumped-component, radio-frequency resonator used in a cryogenic vacuum environment to drive an ion trap. The resonator was required to achieve the voltages necessary to trap (~ 100 V), while dissipating very little power. Ultimately, for an input voltage of 1.35 V, a voltage gain of 100 was measured at 5.7 K, using a design which dissipated only 18 mW. The resonator operated at a frequency of 7.64 MHz and had a Q of 700. Single $^{40}\text{Ca}^+$ ions were confined in a trap driven by this device, providing proof of successful resonator operation at low temperature. © 2012 American Institute of Physics. [<http://dx.doi.org/10.1063/1.4737889>]

I. INTRODUCTION

In the last decade, trapped ions have proved to be a suitable system to perform quantum computation and quantum information processing.¹ Proof-of-principle demonstrations of several algorithms were performed,^{2–5} though the complete control of a large number of qubits is still an outstanding challenge.^{6,7} One proposed way to scale-up the number of qubits, is the use of a multiplexed array of small traps, with segmented dc electrodes used to shuttle ions from the memory region to the processing region.^{8,9} A reasonable way to build these arrays of traps is using planar surface traps,^{10,11} which can benefit from photolithographic techniques to achieve miniaturization to micrometer feature sizes. However, as the ion-electrode distance is reduced, ion heating becomes significant.¹² This can be mitigated to some extent by operating at cryogenic temperatures.¹³

Despite great progress in the miniaturization of ion-traps, little has changed regarding the trap-drive electronics. In order to limit Joule dissipation and RF crosstalk, the relatively high radio-frequency voltages necessary for trapping, typically around a hundred volts, cannot be applied through long cables in the cryostat (see Fig. 1(a)). The ideal solution is to send a small amount of RF power through the (long) cables, and amplify the voltage with a resonator as close as possible to the trap. The common choice adopted for ion trapping is the use of helical resonators,¹⁴ which can also be operated at cryogenic temperatures.¹⁵

In this work, the resonators are made using lumped components, namely, series RLC resonators. These circuits are naturally compact, and the small volume makes them suitable for the use in a cryostat, or when several resonators should be used to drive different RF-electrodes.¹⁶ In this article, we show that these versatile resonators can be effectively used to trap ions, while dissipating a negligible amount of RF-power. The resonator described here, operating with a resonant fre-

quency of 7.64 MHz, reached a voltage step up as high as 100 at low temperature.

II. OVERVIEW OF THE EXPERIMENT

An overview of the experiment is shown in Fig. 1. The RF voltage source is a commercial function generator (Thurlby Thandar Instruments TG 4001), with an output impedance of 50 Ω . The RF signal is applied, via coaxial cables, to the resonator – and then to the trap – inside the cryostat (ARS CS210S-GMX-20). The transmission line is composed of three different types of coaxial cables and two types of RF connector. The first cable is a standard copper-core RG-58. The second cable (Lakeshore SS-32) is made from steel for thermal decoupling from the room-temperature part of the setup to the cryogenic-temperature part. The heat transfer of a single cable is about 0.5 mW, which is small compared to the 500 mW cooling power of the cryostat. The steel's relatively high resistance accounts for a measured attenuation of 3.2 dB at room temperature for frequencies of about 10 MHz. Moreover, the cable characteristic impedance is 40 Ω and it is not well matched to the rest of the transmission line. However, the calculated return loss due to this mismatch is negligible ($\sim 0.1\%$). The last cable is a very thin and flexible coaxial cable (Samtec MH081). The core material is silver-plated copper and the characteristic impedance is 50 Ω . The first connector is a coaxial feedthrough (Accu-Glass 25D-5CX2-450), mounted on the outer shroud of the cryostat, while the second is an array of microdot straight connectors (Tyco Electronics 141-0001-0001 and 142-0000-0001), anchored on the 5 K copper shield. The microdot connectors showed excellent performance at low temperature, even after several cooling cycles. After this coaxial transmission line, the RF voltage is applied – through soldered connections – to a printed circuit board (PCB), where gold-plated pogo-pins are soldered (Mill-Max 0929-0-15-20-75-14-11-0). It was decided to use these connectors so that it would be possible to easily exchange the last part of the circuit, where the resonator and the trap are, without major changes to the setup.

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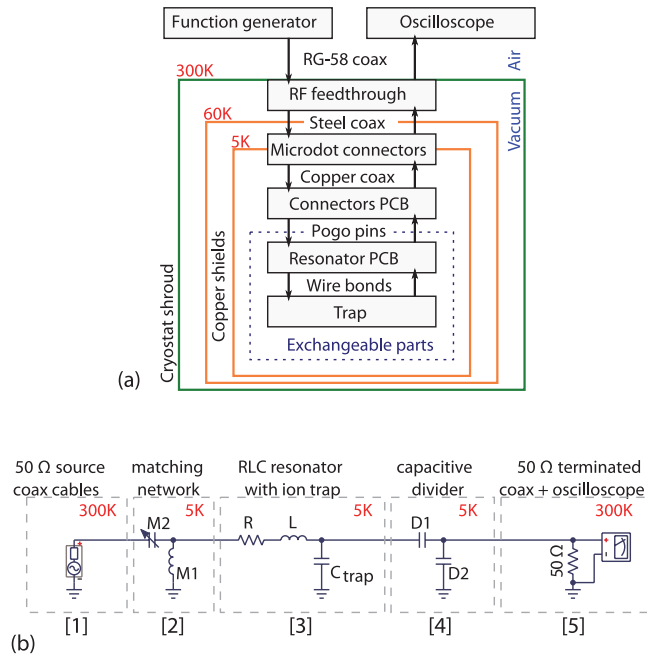


FIG. 1. (a) Complete schematic of the trap-driving electronics. The approximate temperatures in each stage are also shown. (b) Detailed schematic of the resonator and of the auxiliary electronics. The radio-frequency source is a commercial function generator. The matched RLC resonator provides the necessary voltage step up, while the voltage divider and the oscilloscope are used for the measurement and the voltage control.

The exchangeable part of the circuit is the matched RLC resonator with the ion trap. The matching circuit (part [2] in Fig. 1(b)) is a simple “L-section” network,¹⁷ because the resonator is meant to be driven within a narrow range of frequencies. The series RLC resonator (Fig. 1(b), [3]) is actually composed of only an inductor and the trap capacitance, but an effective resistance must be used in the model to take into account any dissipation in the resonator. The value of this effective resistance influences the quality of the resonator, so keeping it as low as possible is of significant importance. The trap is a planar surface trap, with an ion-electrode distance of $454\ \mu\text{m}$ and a distance between electrodes of $20\ \mu\text{m}$. It had been photolithographically patterned, using e-beam evaporation to deposit 2 nm of Ti (adhesive layer) and 300 nm of Au on a $500\ \mu\text{m}$ -thick SiO_2 substrate.

The last two blocks in the schematic (Fig. 1(b), [4] and [5]) are the capacitive divider and an oscilloscope (Tektronix TDS 2004B). These are used to measure and control the voltage at the resonator’s output. The purpose of the capacitive divider is to increase the input impedance of the measurement device (in this case the oscilloscope plus the transmission line) and to make the capacitance of this “probe” independent of the length of the coaxial cable. The divider is connected in parallel with the trap; for this reason its capacitance has to be taken into account when calculating the resonator’s characteristics.

III. CIRCUIT ANALYSIS

In this section, the non-commercial parts (matching network, resonator, and capacitive divider) are described; the

motivations for their necessity and the equations for their characterization are discussed.

A. Matching network

Since the impedance of the resonator at resonance is, in general, different from the impedance of the transmission line, in order to reduce reflections of power it is necessary to add a matching network to the circuit. As for the voltage-amplifying circuit, low power consumption and cryogenic-compatible components constrain the matching network to be passive.

The obvious choice for an easily customizable single-frequency passive matching network is the “L-section” (a voltage divider built with two reactive components). At resonance the impedance of the resonator is purely real and, usually, smaller than $50\ \Omega$. This means that the first matching reactance – seen from the resonator’s point of view – has to be connected in series. The second component has to be an opposite reactance (a capacitor, if the first is an inductor, or vice-versa) connected in parallel. In both of these two possible configurations, the resonator and the trap do not have a dc path to ground because of the capacitor. In order to remove any dc bias voltage and reduce the low-frequency noise on the RF trap electrodes, which could perturb the ions, a dc path to ground should be provided.

On a closer analysis, the role of the first (series) reactance is simply to shift the resonance frequency until the impedance of the resonator (which is frequency-dependent) can be matched with just a parallel component. An alternative matching, therefore, would be achieved operating the resonator at a frequency – usually different from the resonance – where its impedance can be matched with only one parallel reactance. The two frequencies where matching with one parallel reactance is possible are denoted here as ω_L (low) and ω_H (high). The matching at these two frequencies can be accomplished with an inductor or a capacitor, respectively. If this reactance ($M1$ in Fig. 1(b)) is inductive, then the dc path to ground is ensured. A second impedance can then be added ($M2$) in series for fine-tuning the matching, without interrupting the dc path.

The aim of this tuning is to modify the resonator’s impedance (by adding series or parallel reactances) until it exactly matches that of the source. Adding parallel reactance causes the resonator’s impedance at the frequencies ω_L and ω_H to become equal to the source impedance. The matching-component reactance is inductive or capacitive, respectively. Considering the circuit in Fig. 2, it can be shown analytically that the frequency ω_L and the matching reactance X_m can be expressed as

$$\omega_L = \omega_0 \sqrt{1 + \frac{\alpha}{4Q^2}} - \frac{\omega_0}{2Q} \sqrt{\alpha}, \quad (1)$$

$$X_m(\omega_L) = \frac{Z_s}{\sqrt{\alpha}}, \quad (2)$$

where Z_s is the RF source’s impedance, ω_0 is the resonator’s resonant frequency, $Q = \omega_0 L/R$ is the resonator’s quality

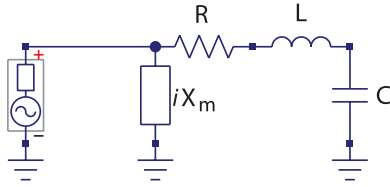


FIG. 2. Circuit diagram of resonator including matching element with reactance X_m which allows one to match the resonator to the source impedance at ω_L (low) and ω_H (high). The matching component is then an inductor or a capacitor, respectively.

factor, and $\alpha = \frac{Z_s}{R} - 1$. From an experimental point of view, it is easier to find ω_L and $X_m(\omega_L)$ by measuring the resonator's impedance with a network analyzer.

It is important to understand that matching away from resonance does not compromise the voltage gain given by the resonant enhancement. However, there is an important, unavoidable, drawback connected to the matching: if only reactive components are used in the network, and power conservation holds (which means that the quality factor of the matching circuit $Q_M \gg Q$), these circuits have to satisfy a strict relation between voltage transformation and impedance transformation. Since we want to match the resonator with an inductor, the matching quality factor Q_M has the same order of magnitude as Q . This means that the overall quality factor will be reduced, by as much as a factor of 2. Nonetheless the following analysis still holds.

Treating the matching network as a two-port device, power conservation requires

$$|V_{in}|^2/Z_{in} = |P_{in}| = |P_{out}| = |V_{out}|^2/Z_{out} \quad (3)$$

with $V_{in/out}$ being the voltage and $Z_{in/out}$ the impedance seen from their respective port. Equivalently stated

$$k := |V_{out}/V_{in}| = \sqrt{|Z_{out}/Z_{in}|}. \quad (4)$$

Typically in the arrangement used here, $|Z_{out}| \sim 5 \Omega$, though this strongly depends on the exact details of the resonator coil. In any event, in all the relevant cases for resonators driving ion traps, $|Z_{out}| < |Z_{in}| = 50 \Omega$. Consequently $k < 1$. This means that, when the resonator is matched, the voltage at its input is reduced by a factor proportional to the square root of its impedance. This factor reduces the overall voltage gain given by the resonator; for this reason the matching network must be taken into account in the resonator design.

B. RLC resonator

1. Resonator analysis

The RLC resonator is the part of the circuit where the voltage step up takes place. It is a voltage divider made of an inductor and a capacitor. The resistance is an effective one and it is used to model the power dissipations of realistic components. The input impedance is easy to calculate and can be written as

$$Z(\omega) = R + i\omega L - \frac{i}{\omega C} = R \left[1 + iQ \left(\frac{\omega}{\omega_0} - \frac{\omega_0}{\omega} \right) \right], \quad (5)$$

where $\omega_0 = 1/\sqrt{LC}$ is the resonator's resonant frequency and $Q = \omega_0 L/R$ is the resonator's quality factor. On resonance Z is purely real and equal to R , which, for the relevant cases, is less than 50Ω .

At resonance, an unmatched resonator has a voltage gain (the value of the transfer function on resonance) which is equal to the quality factor, Q . However, when a matching network is added, the overall (matching and resonator) voltage gain changes by a factor k , as defined in Eq. (4). The voltage gain near the resonance is

$$G_V(\omega) \simeq \frac{kQ}{\left| 1 + \frac{iQ}{2} \left(\frac{\omega}{\omega_L} - \frac{\omega_L}{\omega} \right) \right|}. \quad (6)$$

The above equation holds if the frequency dependence of the matching network is negligible with respect to the frequency dependence of the resonator's impedance. Matching with a parallel reactance satisfies this condition. The peak gain appears on resonance, and it is given by

$$G_V(\omega_0) \simeq kQ = \sqrt{\frac{\omega_0 L Q}{Z_s}}. \quad (7)$$

2. Quality-factor measurement

To fully characterize a resonator, it is important to measure its quality factor, Q . Measuring ω_0 is also important, but trivial. Thinking of the matched resonator as a circuit with one input port and one output port, it is possible to measure the quality factor from either port, independently. From Eq. (6), it is possible to show that Q can be measured, at the output port, as

$$Q = \frac{\omega_0}{\Delta\omega} = 2 \frac{\omega_0}{\Delta\omega_V}. \quad (8)$$

Here, $\Delta\omega_V$ is the measured -3 dB full-width voltage-gain bandwidth (referenced to the function-generator voltage source), which is twice the voltage-gain bandwidth, $\Delta\omega$, referenced from the input of the bare RLC resonator.

On the other hand, using a network analyzer at the input port, it is possible to measure the scattering parameter, S , as a function of frequency.¹⁷ If the resonator were to be matched with a transformer, its impedance would become

$$Z_M(\omega) = Z_s \left[1 + iQ \left(\frac{\omega}{\omega_0} - \frac{\omega_0}{\omega} \right) \right] \quad (9)$$

and the scattering parameter could be calculated as

$$|S| = \left| \frac{Z_M(\omega) - Z_s}{Z_M(\omega) + Z_s} \right| = \frac{\frac{Q}{2} \left(\frac{\omega}{\omega_0} - \frac{\omega_0}{\omega} \right)}{\left| 1 + i \frac{Q}{2} \left(\frac{\omega}{\omega_0} - \frac{\omega_0}{\omega} \right) \right|}. \quad (10)$$

From this equation, it is possible to find that the $1/\sqrt{2}$ full-width S -bandwidth, $\Delta\omega_S$, can be used to measure the quality factor, using the formula

$$Q = 2 \frac{\omega_0}{\Delta\omega_S}. \quad (11)$$

If different kinds of matching are used, $Z_M(\omega)$ takes different shapes, and it is not possible to find a generally valid

expression. However, as long as the frequency dependence of the matching components is negligible, Eqs. (10) and (11) continue to be valid (replacing ω_0 with ω_L).

3. Voltage-gain optimization

As mentioned in Sec. I, ion traps need relatively high RF voltages. To achieve this goal without dissipating too much RF power it is necessary to have a high voltage gain in the resonator. If the transmission line's characteristic impedance, Z_s , and the operating frequency, ω_0 , are fixed, it is obvious from Eq. (7) that the gain can be improved by increasing the values of L and Q .

Note that, in the chosen model, the effective resistance is completely attributed to the inductor. This is justified by the fact that the quality factors of commercial capacitors are one or two orders of magnitude higher than the quality factors of commercial inductors. In this situation, the general equation for the quality factor of two series component $Q^{-1} = Q_L^{-1} + Q_C^{-1}$ reduces to $Q \simeq Q_L$.

Both of the important characteristics to enhance, Q_L and L , are usually reported on inductors' datasheets, and are limited as follows:

- Q_L is limited by the resistance of the conducting material. This resistance is increased by several effects, like the *skin effect*, the *proximity effect*¹⁷ and the induced eddy currents in dissipative materials coupled to the inductor. A way to increase Q_L is to use good conducting materials and big wire diameters;
- L is limited mainly by the parasitic capacitance of the inductor, which introduces a lower bound for the operating frequency. This problem can be reduced by using bigger coils with larger spacing between the windings.

C. Capacitive divider

A capacitive divider is a voltage divider made with two capacitors. In this circuit its purpose is to increase the input impedance of the measuring apparatus, including the transmission line, in order not to affect the resonator during the measurements. Choosing $D1 \ll D2$ and $(\omega D1)^{-1} \gg 50 \Omega$ (with reference to Fig. 1(b)), the impedance seen from the resonator is dominated by the impedance of the first capacitor. In this way it does not depend on the length of the transmission line or the input impedance of the oscilloscope, and the measurement instrumentation can be disconnected without affecting the resonator. The transmission line is connected in parallel with $D2$. The transmission-line impedance adds a frequency dependence to the divider's voltage transformation if the relation $(\omega D2)^{-1} \ll 50 \Omega$ is not fulfilled (as in our experiment).

IV. EXPERIMENTAL RESULTS

The circuit testing was done in two steps. The first was the calibration and testing of the capacitive divider. Here, the choice for a temperature-stable and high quality-factor capacitor led to the use of surface-mounted device (SMD) capacitors

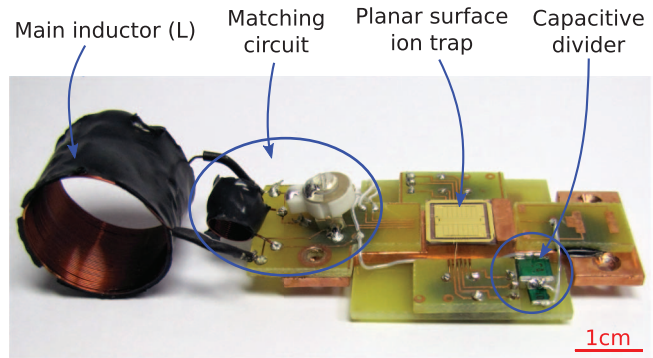


FIG. 3. Final trap-driving circuit prototype. The matched resonator is on the left, the gold-plated planar surface trap is in the center and the capacitive divider on the right. The trap is bound with $25 \mu\text{m}$ -gold wires. The prototyping circuit was built on a stack of three FR-4 PCB layers.

itors with mica dielectric, manufactured by Cornell Dubilier Electronics. With reference to Fig. 1(b), $D1$ was chosen to be 5 pF (part no. MC08CD050D-F), while $D2$ was chosen to be 1 nF (part no. MC22FD102J-F), so that the voltage transformation ratio was roughly 200. The divider was calibrated at room and cryogenic temperatures, proving in this way the capacitors' reliability at low temperature. The measurement of the voltage transformation ratio did not show any significant change with the temperature; however, the measured uncertainty between different realizations of the divider was 6% (due to the uncertainty in the capacitances), leading to a systematic error of the same value in the measured voltage after the divider.

The second part of the testing was done with the complete circuit. Figure 3 shows the resonator prototype, with the matching network, the capacitive divider and the ion trap in the center. The trap was bonded using $25 \mu\text{m}$ -thick gold wires and an ultrasonic wire-bonder. The circuit was built on a stack of three layers of FR-4 PCB. This material was used for quick development: at low temperature the outgassing rate was low enough to allow trapping of ions. In the circuit, the RF line and the DC lines (for the segmented electrodes) were kept apart, in order to reduce the RF coupling and noise. The resonator and the capacitive divider were placed on two opposite sides of the trap's RF electrode, to measure the trap voltage and to check the continuity of the line at the same time.

The trap capacitance, calculated with finite element software, is $C_{\text{trap}} \simeq 1 \text{ pF}$. The actual value was not measured. Several inductors for the resonator were tested. Inductors with cores other than air were limited by dissipation in the core or by cryogenic compatibility issues. The best results for high quality factors were obtained with an air-core inductor manually wound using a copper-coated NbTi wire (Supercon Inc. 54S43 insulated diameter 0.279 mm , superconducting critical temperature $T_C = 9.2 \text{ K}$). As stated before, high L values are limited by the parasitic capacitance, in conflict with the required small volume. The physical dimensioning of the inductor can be carried out using inductance-calculator software¹⁸ or formulas.^{19–21} In the final design, the inductor had a measured inductance of $45 \mu\text{H}$ at 7.56 MHz . The inductor's diameter and length were 20 mm each. The coil was mechanically stabilized with an epoxy matrix (Emerson & Cuming

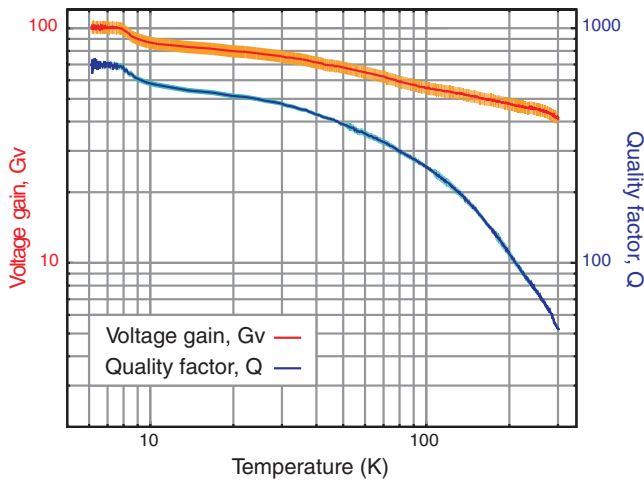


FIG. 4. Temperature dependence of the voltage gain, G_V , and quality factor, Q , of the trap-drive resonator. The error bounds for the voltage gain show a systematic uncertainty due to the capacitive divider uncertainty, while the instrumental uncertainty is smaller than the line thickness. The error bounds for the quality factor depend on the network analyzer's frequency resolution. The temperature is measured on the copper of the trap carrier.

Stycast 2850 FT). Since the superconducting material was copper-coated NbTi, the superconducting critical temperature was 9.2 K, above the usual minimum working temperature of the cryostat of this experiment (<6 K). The same superconducting cable was used to wind the matching inductor $M1$. $M2$ was a ceramic variable capacitor with range 12 – 100 pF (Johanson Manufacturing 9328).

With the circuit mounted inside the cryostat, at room temperature, the matching was tuned by changing $M2$ while checking the scattering parameter S on the input port of the circuit. On closing the vacuum chamber the impedances changed slightly, resulting in a residual reflection $S = 0.027$. The measured resonance frequency was $\omega_0 = 2\pi \cdot 7.43$ MHz, with a voltage gain $G_V = 41 \pm 2$ and a measured quality factor $Q = 52 \pm 1$ at room temperature.

For the cryogenic testing, the temperature was measured on the copper of the trap carrier and in the charcoal of the cryo-pump using two silicon diode temperature sensors (Lakeshore DT-670-SD-1.4L). The two measurements agreed within 0.5 K. Supported by subsequent measurements, we estimate that when the temperature sensors read 6 K, the coil temperature was around 9 K. Both the gain and the quality factor were measured as a function of the trap carrier's temperature. The results are reported in Fig. 4.

The plots of the quality factor, Q , and voltage gain, G_V , increase with decreasing temperature, showing an improvement of the resonator at low temperature. At $T = 5.7$ K (measured at the sensor), the quality factor was $Q = 700 \pm 30$, one order of magnitude higher than at room temperature. Similarly, the gain was $G_V = 101 \pm 6$ at 5.7 K, more than twice the gain at room temperature. The voltage gain did not show any evident enhancement when the superconducting-transition temperature is passed. Moreover, a similar realization of the RLC resonator with an inductor built winding an enameled copper wire showed comparable G_V and Q . During the test the resonance frequency showed a small change, moving from 7.43 MHz at 300 K to 7.64 MHz at 5.7 K. The

scattering parameter, S , changed from 0.027 to 0.57. This is due to the small effective resistance, which at low temperature was 10 times lower than at room temperature. The matching which was achieved at 300 K was no longer good at 5.7 K. Possible solutions to this are discussed in Sec. V.

After characterization, this circuit was used to successfully trap single $^{40}\text{Ca}^+$ ions. The output voltage from the function generator was 1.35 V, meaning that the ions were trapped using only 18 mW of RF power.

V. OUTLOOK

As ion trap experiments are scaled up to more ions and reduced ion-electrode distance, the frequency of the trap drive is typically increased. The simple model outlined above breaks down at higher frequencies due to small inductances distributed around the circuit. However, the essential design is expected work at frequencies up to hundreds of MHz.

To investigate such possibilities a resonator was made which operates at the lower end of the VHF band (very high frequency, 30–300 MHz). The design is similar to the circuit in Fig. 1(b), except that the matching component $M1$ is provided by a capacitor. A 1.1 μH inductor (Micrometals T94-6 core with 11 turns of 0.6 mm diameter Cu wire) was used to drive a room-temperature linear Paul trap at 30 MHz. The quality factor, Q , of the inductor was ~ 180 . The measured capacitance of the trap and wiring was 27 pF, the parallel matching reactance ($M1$) was a 12–100 pF variable ceramic capacitor (Johnson Manufacturing 9328). The matching capacitance required was much less than that computed by a simple model of the circuit, due to series inductance of the PCB circuit traces. The PCB had initially been developed for resonators at lower frequencies and the traces were as long as 6 cm. Even at 30 MHz this was acceptable but, for resonators at still higher frequencies, care would need to be taken to minimize the length of conductors between components. A 12 μH inductor (API Delevan 5022-123J) was placed in parallel with the matching capacitor, $M1$, which provided a low-impedance path to ground at low frequency in order to dissipate any charge built up on the trap electrodes. Tuning the 5–45 pF capacitor, $M2$, (Sprague-Goodman GXE45000) reduced the reflection to less than 1%. The computed gain for this configuration was $G_V \sim 20$. A capacitive probe was used to verify the gain at the output of the resonator and ions were then loaded in the trap at room temperature without issue. With attention to the wiring and component selection there should be little difficulty in using these high-voltage resonators into the regime of hundreds of MHz at either cryogenic or room temperatures.

An additional consideration for future work involves methods of matching or tuning the resonance at low temperature. When varying the resonant frequency the best method of matching depends on the temperature at which the resonator is to be operated. At cryogenic temperatures the Q of air-core inductors is extremely high because the eddy currents that limit most spiral-wound inductors have very little resistance in copper wires at low temperature. Inductors are, therefore, often the preferred matching components under these conditions. At room temperature, however, ceramic and mica capacitors

are preferred as they usually have better quality factors. In either event, these resonators, matched by electrical components, may present an attractive alternative to conventional helical resonators, where matching is done by adjusting the tap point on a helical conductor. Critically, it may be useful to tune components *in situ*, while they are at cryogenic temperatures. It may be possible to tune, for example, the value of $M2$ using varicap diodes. However, given to the unknown performance of varicap diodes at cryogenic temperatures, these were not considered in this instance. Using trimmers or movable parts^{22,23} seems to be a more sure approach. However, it should be noted that this is not a scalable solution and that it also requires a dedicated design of the vacuum chamber and of the cryostat's shrouds. MEMS capacitors²⁴ seem to be a reasonable option for the future. The main advantage of this solution will be the possibility of integrating the whole resonant circuit on the same chip as the trap, leading in the end to a truly scalable ion-trap topology.

VI. CONCLUSIONS

In conclusion, this article reports the investigation and experimental realization of a lumped-component radio-frequency resonator used to drive a planar surface ion trap. In contrast to traditional helical resonators, this class of circuits is compact and easily reproducible. Moreover, using tunable components it is possible to change the resonant frequency without major changes in the circuit.

With a carefully built inductor, voltage gains as high as 100 are achievable at low temperature, and trapping of ions can be achieved with a very small amount of RF power. In the case considered here, 18 mW was dissipated, using a trap with an ion-electrode distance of 454 μm .

ACKNOWLEDGMENTS

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