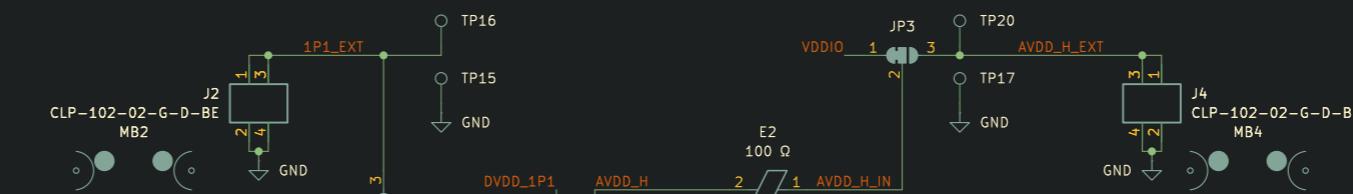


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J1 CLP-114-02-G-D-BE

1	RESET
2	LINK_ST
3	MISO
4	TS_CAPT
5	SCK
6	VDDIO
7	SPI_CFG1
8	SWPD_EN
9	TX2P4_EN
10	TS_TIMER_MS_SEL
11	CS
12	INT
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TP2 TP9 TP11 TP12 TP8 TP1 TP13 TP14 TP4 TP5 TP3 TP10

CS SCK MOSI MISO SDI SDO/SPI\_CFG0 SPI\_CFG1 INT RESET LINK\_ST TX2P4\_EN TXP TXN RXP RXN TX2P4\_EN SWPD\_EN TS\_TIMER\_MS\_SEL TS\_CAPT CLK25\_REF XTAL\_I/CLK\_IN XTAL\_O



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