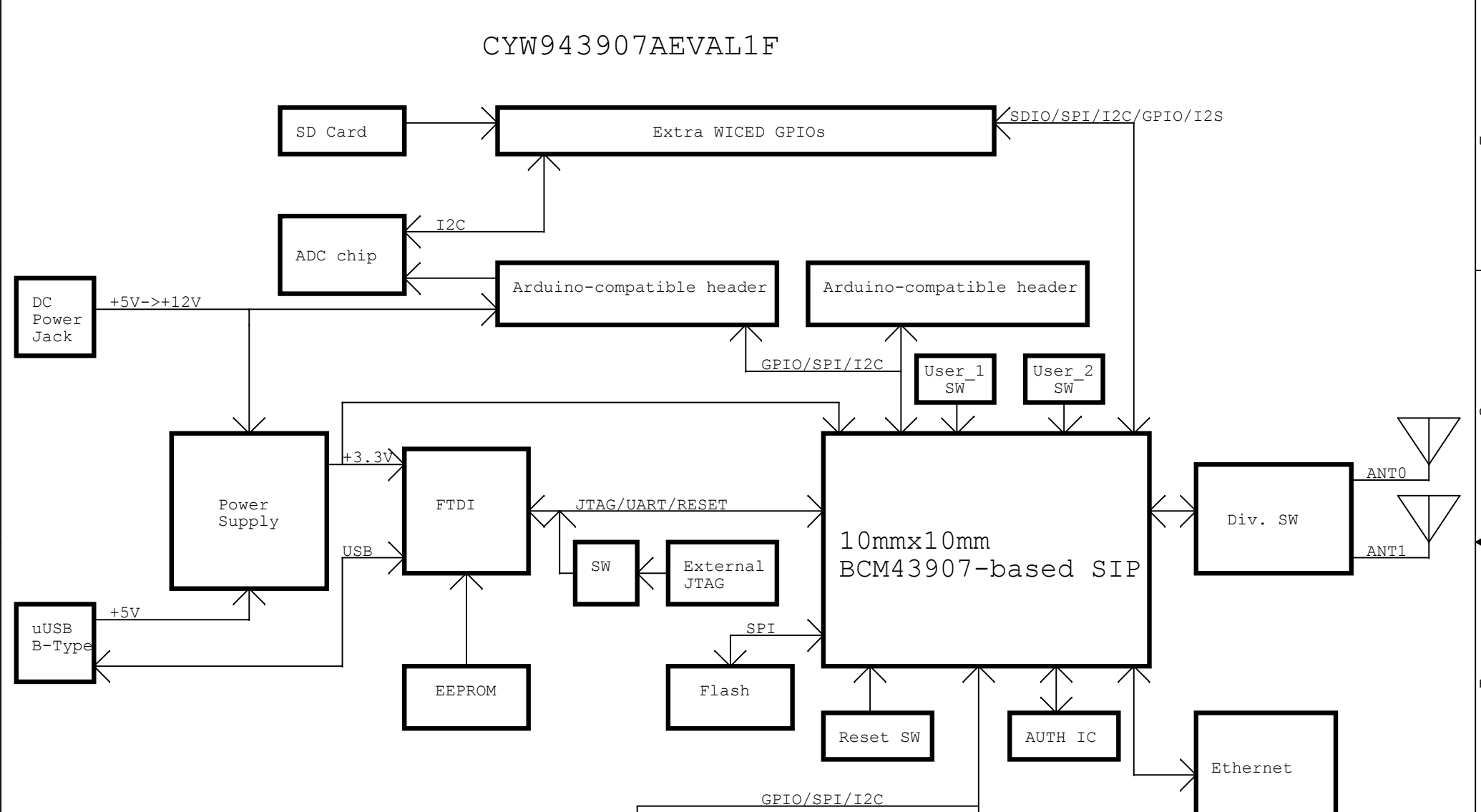


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SYSTEM BLOCK DIAGRAM

CYPRESS SEMICONDUCTOR TECHNOLOGY CO., LTD.

| | |
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| Title | CYW943907AEVAL1F |
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| Size A | Document Number 630-60379-01 |
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| Designer | Deval Dave | Rev 1.0 |
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Date: Thursday, April 27, 2017

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D

C

B

A

D

C

B

A

| BCM43907-based SiP Pin Name | WICED header net name |
|--|------------------------|
| PWM_3 | PWM_3 |
| PWM_4 | PWM_4 |
| PWM_5 | PWM_5 |
| I2S0_MCK | I2S0_MCK |
| I2S0_SD_OUT | I2S0_SD_OUT |
| I2S0_SCK_BCLK | I2S0_SCK_BCLK |
| I2S0_WS_LRCLK | I2S0_WS_LRCLK |
| GND | GND |
| SPI_1_CLK | SPI_1_CLK |
| I2S1_SD_OUT | I2S1_SD_OUT |
| SPI_1_MISO | SPI_1_MISO |
| SPI_0_CLK | SPI_0_CLK |
| SPI_1_MOSI | SPI_1_MOSI |
| SPI_0_MOSI | SPI_0_MOSI |
| SPI_1_CS | SPI_1_CS |
| SPI_0_CS | SPI_0_CS |
| SPI_0_MISO | SPI_0_MISO |
| UART0_RXD_IN | UART0_RXD_IN |
| GND | GND |
| UART0_TXD_OUT | UART0_TXD_OUT |
| USB2_HOST_DEV_SEL | USB2_HOST_DEV_SEL |
| UART0_CTS_IN | UART0_CTS_IN |
| I2C_0_SCL | I2C_0_SCL |
| UART0_RTS_OUT | UART0_RTS_OUT |
| I2C_0_SDA | I2C_0_SDA |
| I2S1_MCK | I2S1_MCK |
| I2S1_WS_LRCLK | I2S1_WS_LRCLK |
| GND | GND |
| I2S1_SCK_BCLK | I2S1_SCK_BCLK |
| SDIO_DATA1 | SDIO_DATA_1 |
| SDIO_DATA0 | SDIO_DATA_0 |
| SDIO_CLK | SDIO_CLK |
| SDIO_CMD | SDIO_CMD |
| SDIO_DATA3 | SDIO_DATA_3 |
| SDIO_DATA2 | SDIO_DATA_2 |
| RF_SW_CTRL_6_UART1_RX_IN | RF_SW_CTRL_6_UART1_RXD |
| RF_SW_CTRL_7_RSRC_INIT_MODE_UART1_TX_OUT | UART1_TXD |
| RF_SW_CTRL_8_UART2_RX | RF_SW_CTRL_8_UART2_RXD |
| RF_SW_CTRL_9_HIB_LPO_SEL_UART2_TX | UART2_TXD |
| HIB_WAKE | HIB_WAKE |
| HIB_LPO_SEL | HIB_LPO_SEL |
| HIB_REG_ON_IN | HIB_REG_ON_IN |
| USB2_DN | USB2_DN |
| USB2_DP | USB2_DP |

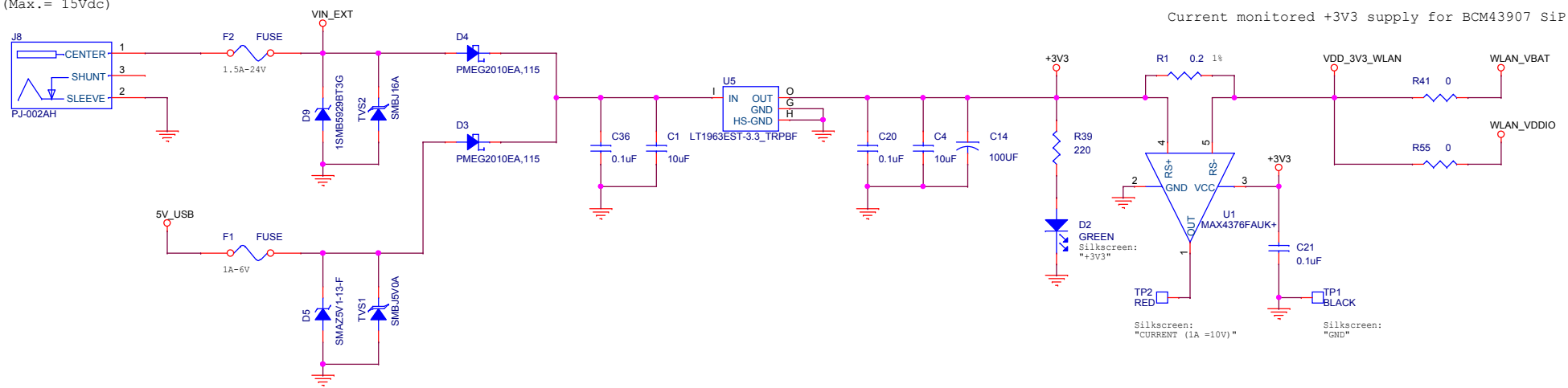
| BCM43907-based SiP Pin Name | BCM43907-based SiP Pin Function | Bootstrap option | Arduino header net name | Arduino functions |
|-------------------------------|---------------------------------|------------------------------------|-------------------------|-------------------|
| GPIO_0 | UART0_RX | GSPI MODE SDIO MOD WLAN BOOT | ARD_GPIO0 | UART_RX |
| GPIO_1_GSPI_MODE | UART0_TX | | ARD_GPIO1 | UART_TX |
| GPIO_13_SDIO_MODE | GPIO_13 | | ARD_GPIO2 | GPIO |
| GPIO_7_WCPU_BOOT_MODE | PWM5 | | ARD_GPIO3 | PWM |
| GPIO_14 | GPIO_14 | | ARD_GPIO4 | GPIO |
| GPIO_16 | PWM2 | | ARD_GPIO5 | PWM |
| GPIO_15 | PWM3 | | ARD_GPIO6 | PWM |
| I2S0_SD_IN | GPIO_26 | | ARD_GPIO7 | GPIO |
| N/A(uses external ADC chip) | ADC | | ARD_AD0 | AD0 |
| N/A(uses external ADC chip) | ADC | | ARD_AD1 | AD1 |
| N/A(uses external ADC chip) | ADC | APPS BOOT | ARD_AD2 | AD2 |
| N/A(uses external ADC chip) | ADC | | ARD_AD3 | AD3 |
| N/A(uses external ADC chip) | ADC | | ARD_AD4_SDA | AD4, SDA |
| N/A(uses external ADC chip) | ADC | | ARD_AD5_SCL | AD5, SCL |
| I2S1_SD_IN | GPIO_29 | | ARD_GPIO8 | GPIO |
| PWM_4 | PWM4 | | ARD_GPIO9 | PWM |
| GPIO_11_ACPU_BOOT_MODE | SPI1_CS, PWM1 | | ARD_SS | PWM, SPI_CS |
| GPIO_10 | SPI1_MOSI, PWM0 | | ARD_MOSI | PWM, SPI_MOSI |
| GPIO_12 | SPI1_MISO | | ARD_MISO | SPI_MISO |
| GPIO_9_USB_SEL | SPI1_CLK | | ARD_SCK | SPI_SCK |
| GND | GND | | GND | GND |
| N/A (supplied off eval board) | N/A (supplied off eval board) | | ARD_AREF | POWER |
| I2C_1_SDA | I2C_SDA | N/A (supplied off eval board) | ARD_AD4_SDA | AD4, SDA |
| I2C_1_SCL | I2C_SCL | | ARD_AD5_SCL | AD5, SCL |
| NC | NC | | ARD_NC | NC |
| N/A (supplied off eval board) | N/A (supplied off eval board) | | ARD_IOREF | POWER |
| RESET_L | RESET | | ARD_RESET | RESET |
| N/A (supplied off eval board) | N/A (supplied off eval board) | | +3V3 | POWER |
| N/A (supplied off eval board) | N/A (supplied off eval board) | | NC | POWER |
| GND | GND | | GND | GND |
| GND | GND | | GND | GND |
| N/A (supplied off eval board) | N/A (supplied off eval board) | | VIN_EXT | POWER |

| BCM43907-based SiP Pin Name | On Board Debug Interface |
|--|--------------------------|
| GPIO2_JTAG_TCK | GPIO2_JTAG_TCK |
| GPIO3_JTAG_TMS | GPIO3_JTAG_TMS |
| GPIO4_JTAG_TDI | GPIO4_JTAG_TDI |
| GPIO5_JTAG_TDO | GPIO5_JTAG_TDO |
| GPIO6_JTAG_TRST_N | GPIO6_JTAG_TRST_N |
| RF_SW_CTRL_7_RSRC_INIT_MODE_UART1_TX_OUT | UART1_TXD |
| RF_SW_CTRL_6_UART1_RX_IN | RF_SW_CTRL_6_UART1_RXD |

WICED / ARDUINO HEADER PAIRS

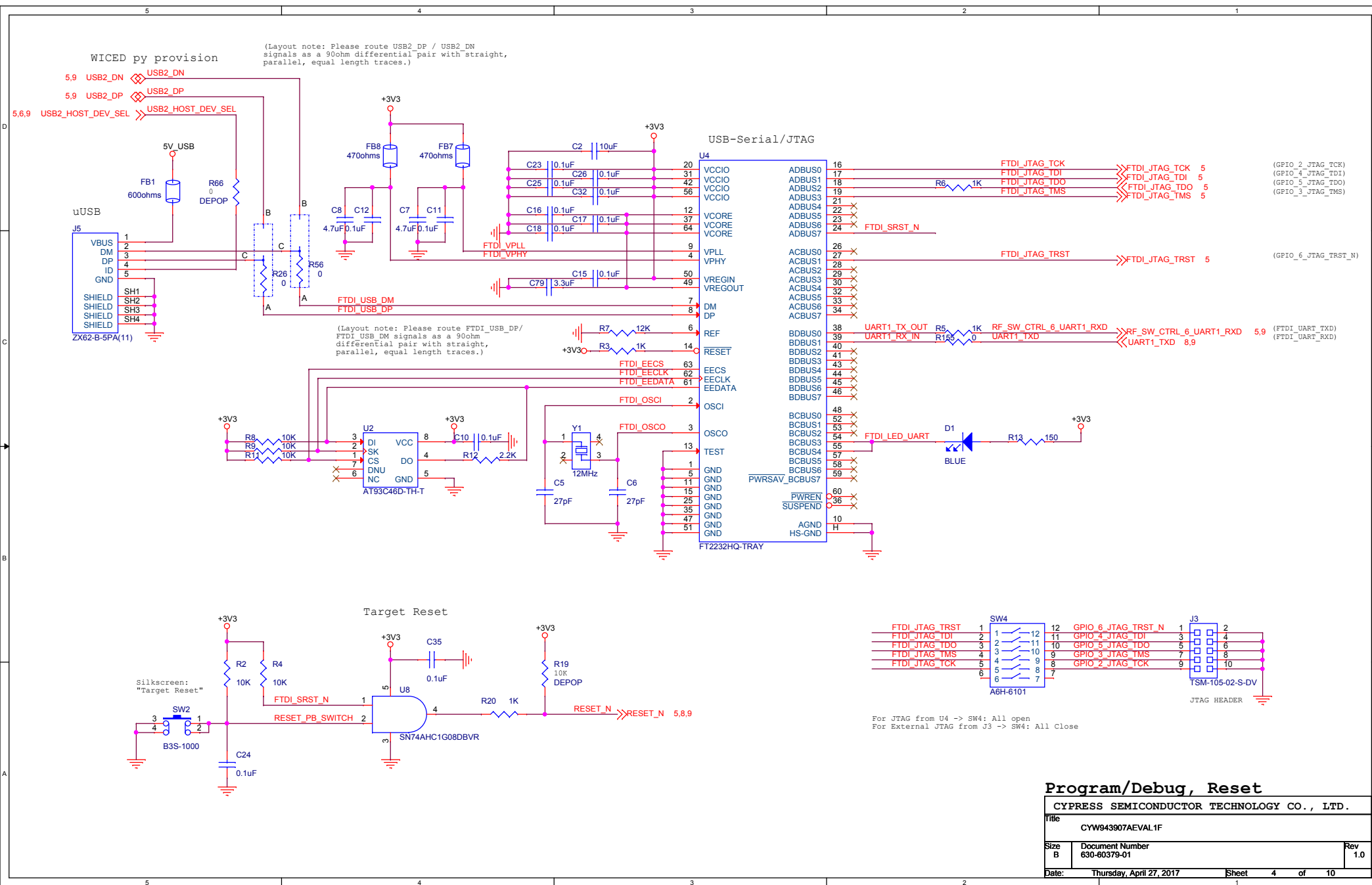
| CYPRESS SEMICONDUCTOR TECHNOLOGY CO., LTD. | | |
|--|------------------------|---------------|
| Title | | |
| CYW943907AEVAL1F | | |
| Size | Document Number | Rev |
| A | 630-60379-01 | 1.0 |
| Date: | Friday, April 28, 2017 | Sheet 2 of 10 |

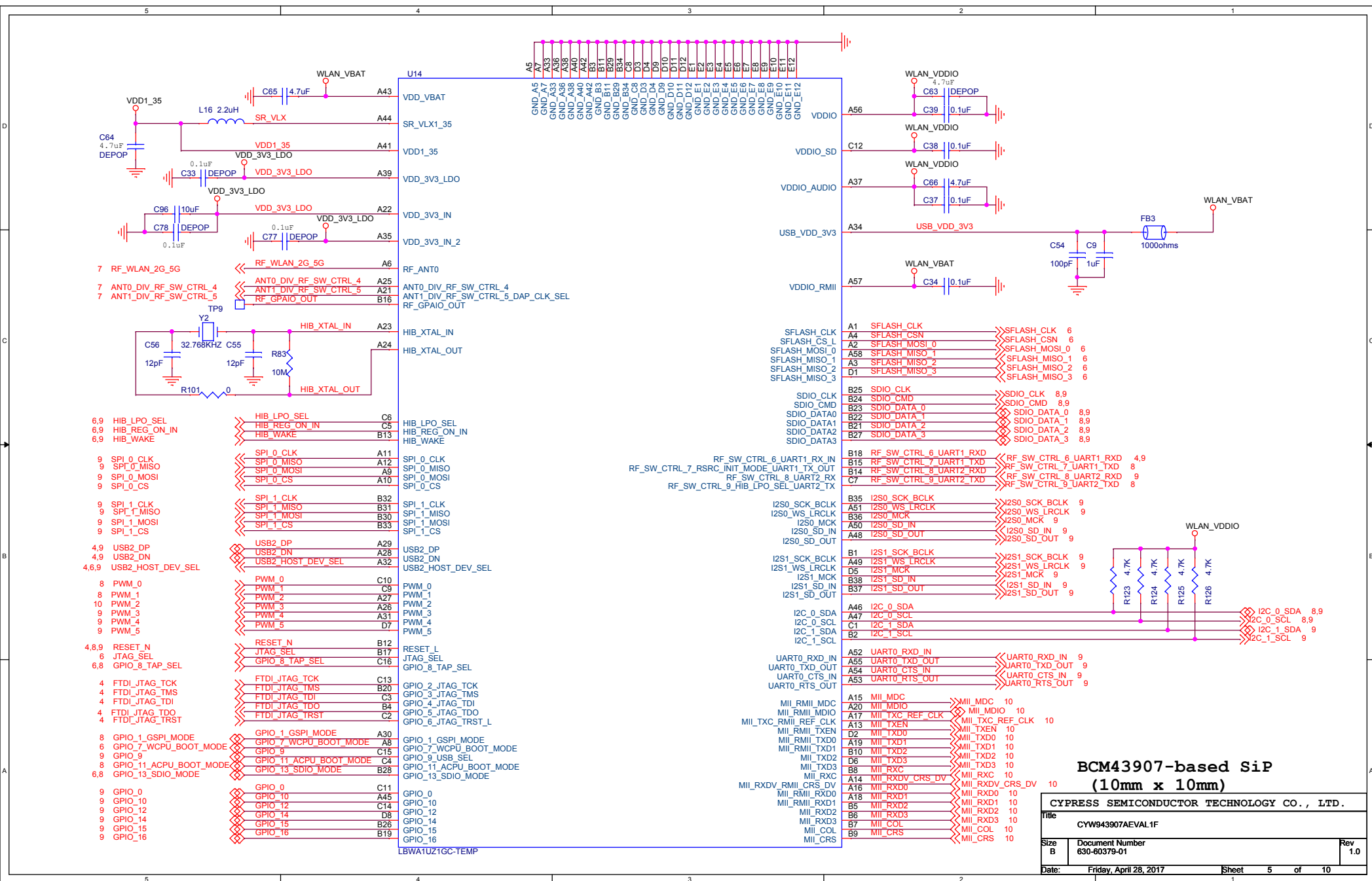
2.1mm DC Jack (centre pin positive)
 Typ.= 5Vdc to 12Vdc
 (Min.= 4.5Vdc)
 (Max.= 15Vdc)

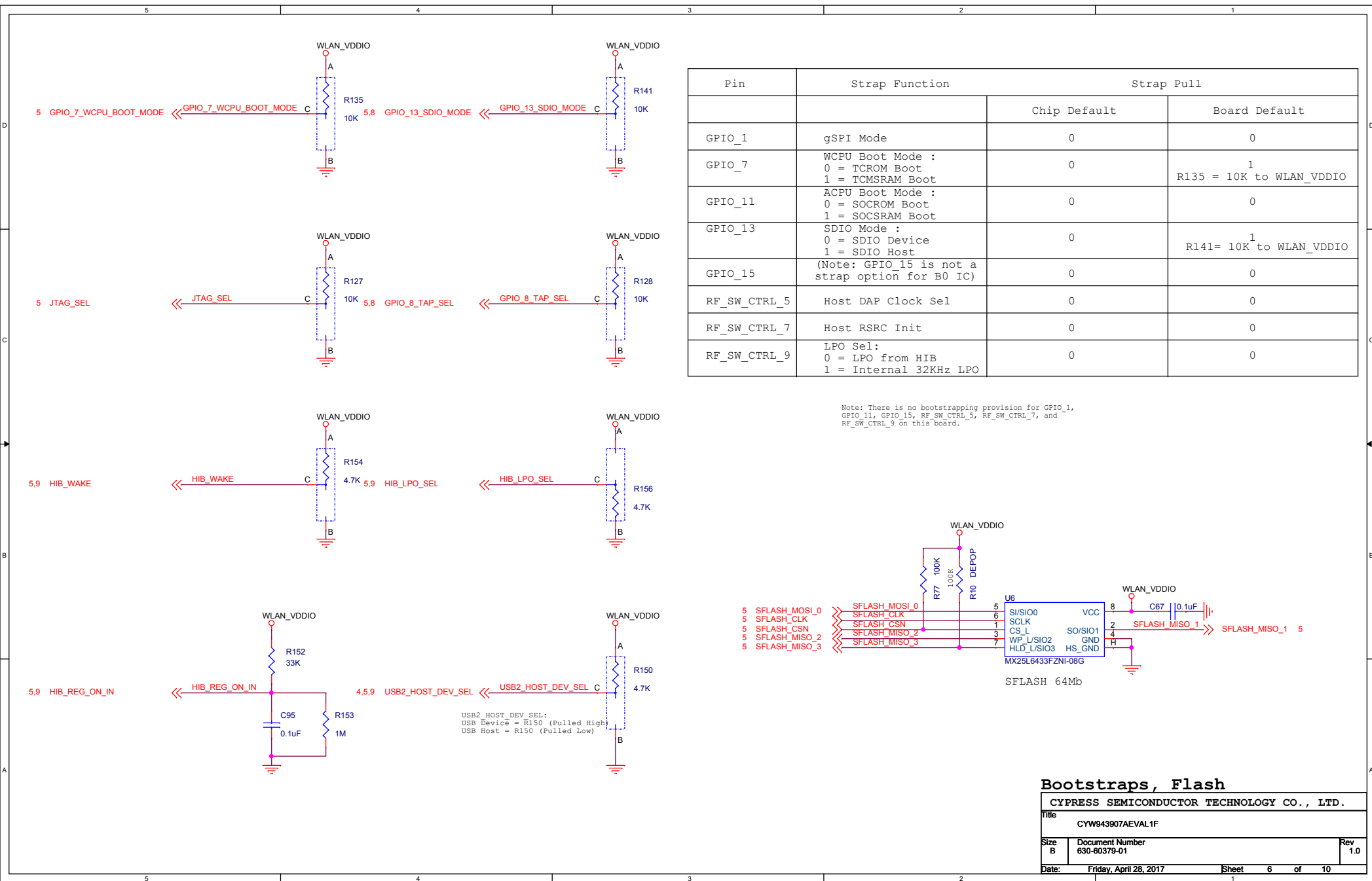


POWER

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| CYW943907AEVAL1F | | |
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| B | 630-60379-01 | 1.0 |
| Date: | Monday, April 24, 2017 | Sheet 3 of 10 |

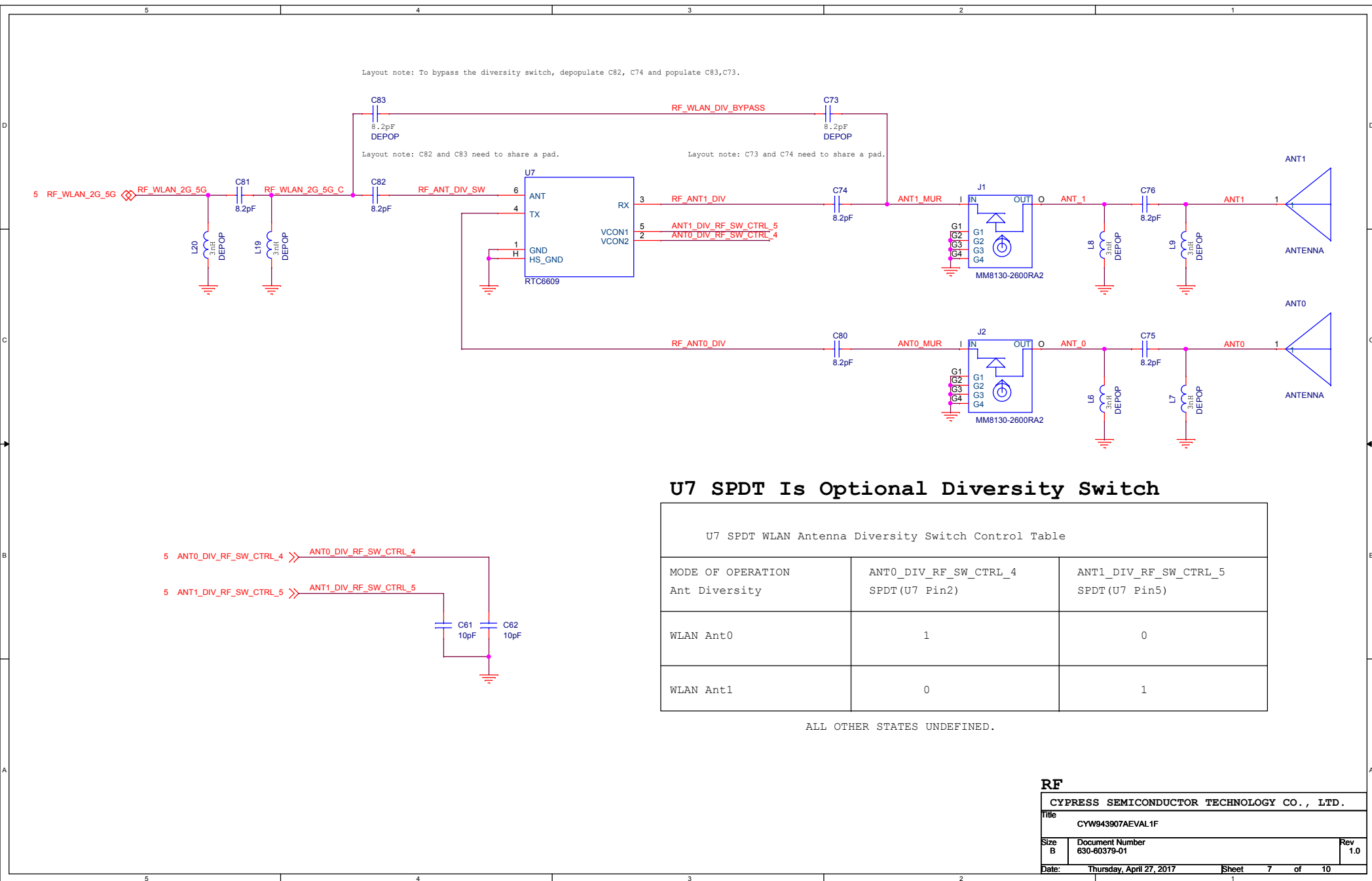


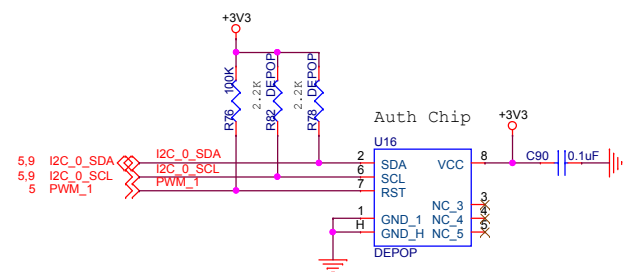
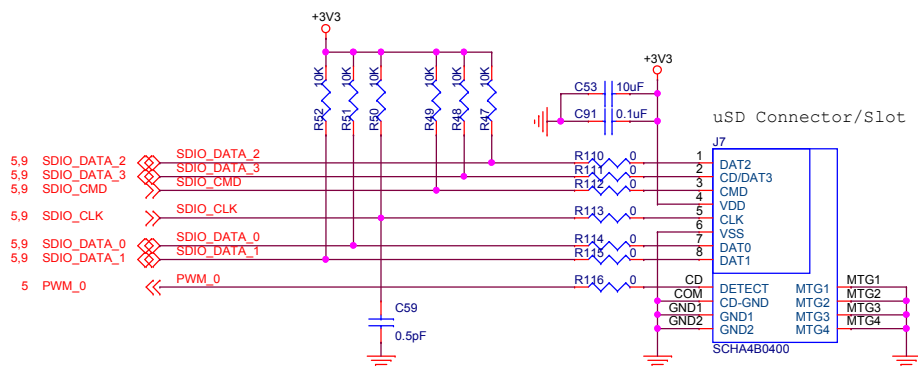
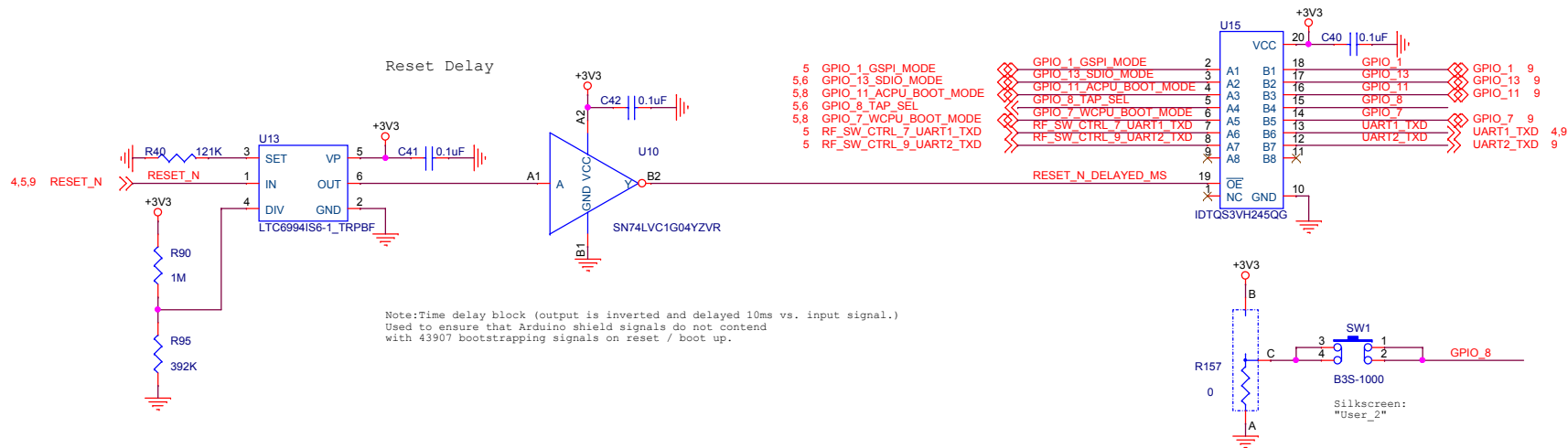




Bootstraps, Flash

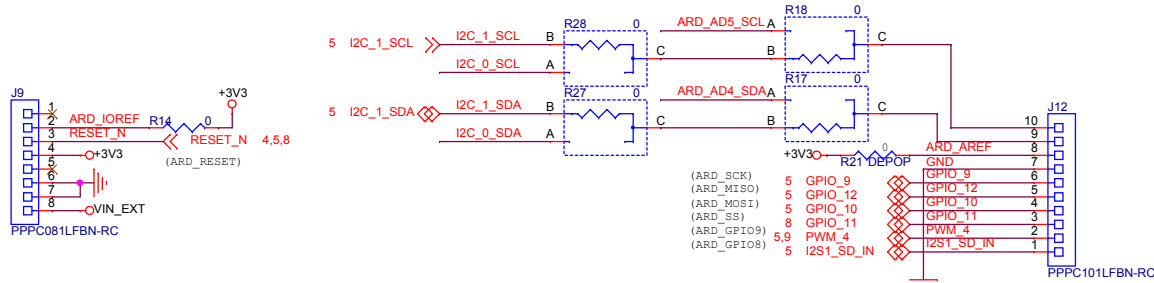
| | | |
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| CYPRESS SEMICONDUCTOR TECHNOLOGY CO., LTD. | | |
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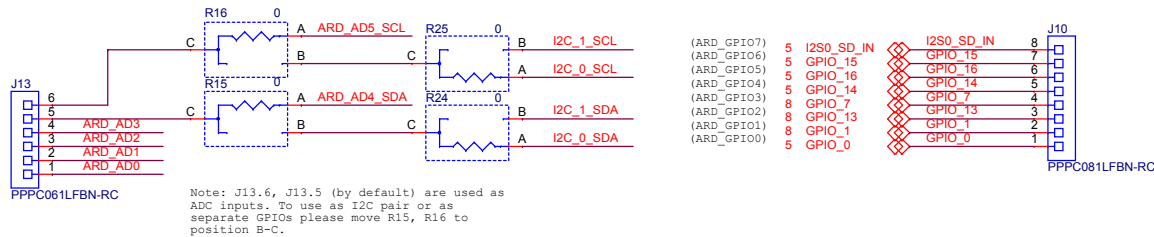
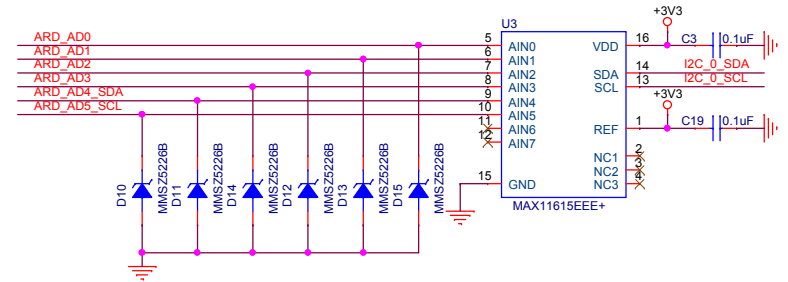


| Reset Delay, Auth IC, uSD | | |
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| Sheet 8 of 10 | | |

Arduino-compatible Headers (+3V3 I/O only)



External ADC



WICED Header (+3V3 I/O only)

Silkscreen:
 "WICED HEADER (+3V3 ONLY)"

