


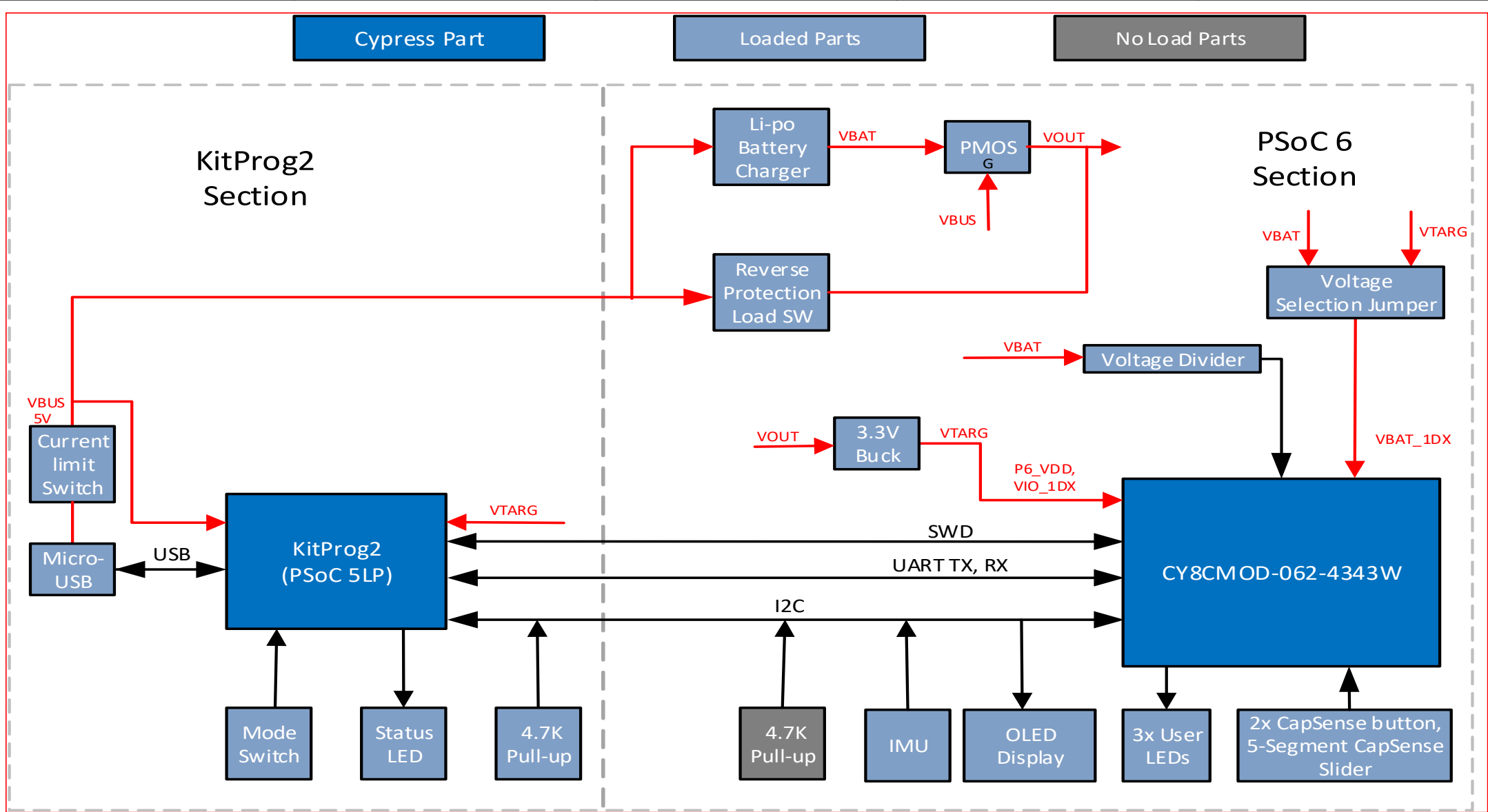
## PSoC 6 WiFi-BT Remote Control Board

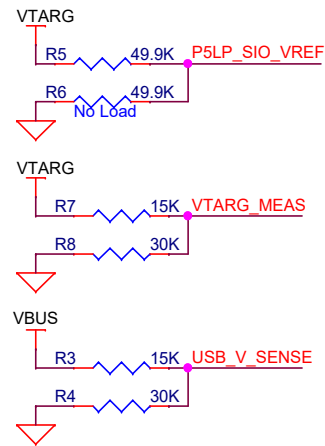
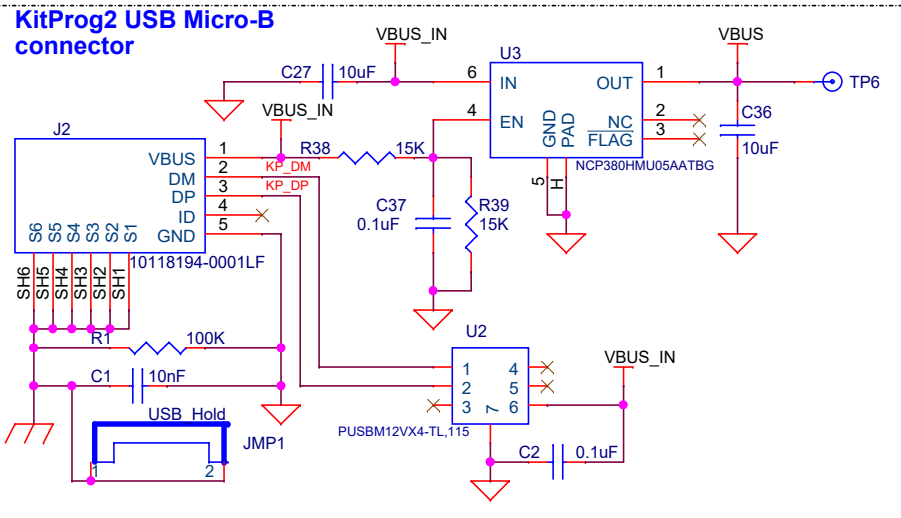
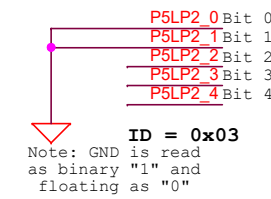
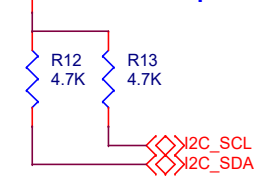
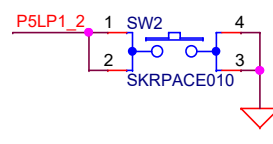
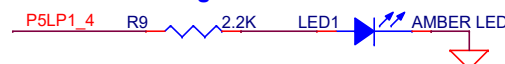
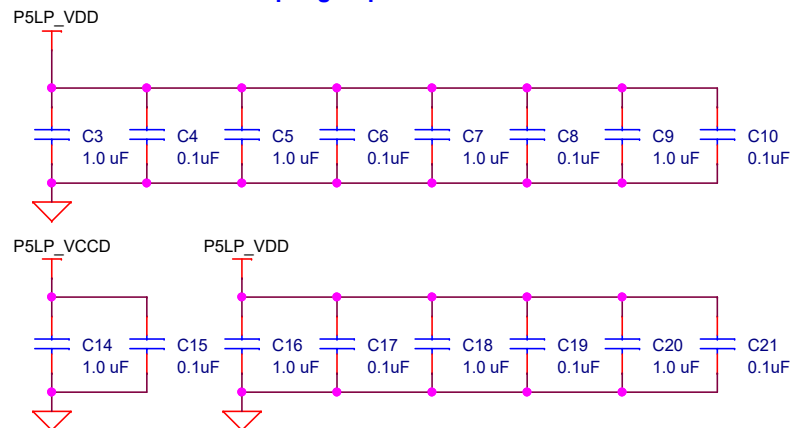
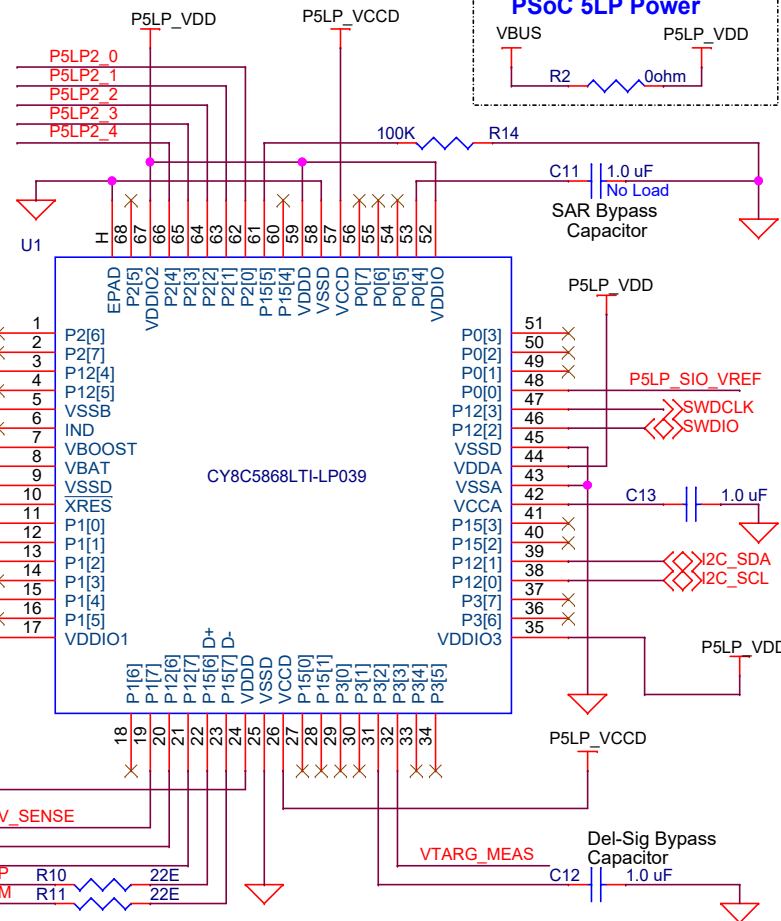
CONTENTS	
PAGE	DESCRIPTION
01	Title & Table of Contents
02	Block Diagram
03	KitProg2
04	PSoC 6 Module Power
05	PSoC 6 Module Signals
06	IMU and Accessories
07	Revision History

### Drawing Numbers

PCBA	121-INT0061-01
PCB	600-INT0061-01
FAB DRW	610-INT0061-01
ASSY DRW	620-INT0061-01
SCH DRW	630-INT0061-01

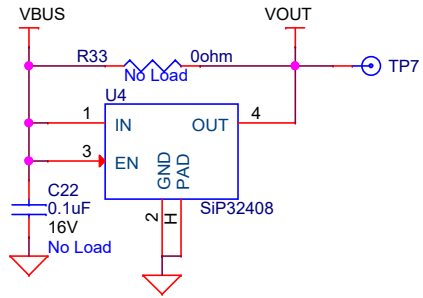
		<b>CYPRESS SEMICONDUCTOR</b> 198 CHAMPION COURT SAN JOSE, CA 95134 (408) 943-2600					
<b>CYPRESS SEMICONDUCTOR © 2018</b>							
<b>SCH Title : PSoC 6 WiFi-BT remote control board</b>							
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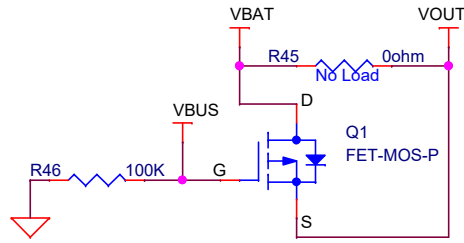


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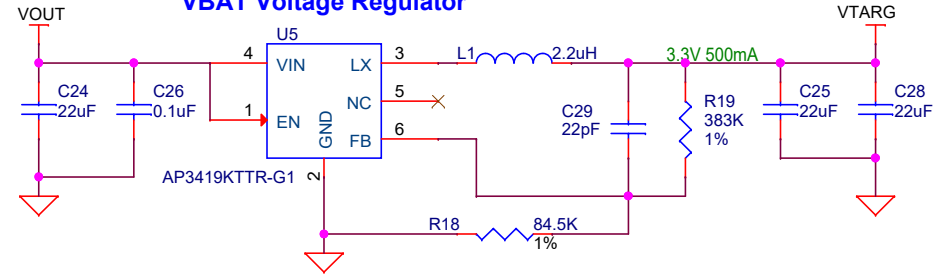
### VBUS Reverse Voltage Protection



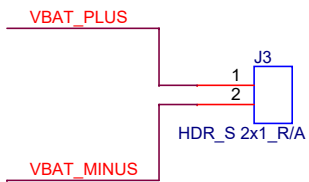
### VBAT Reverse Current Protection



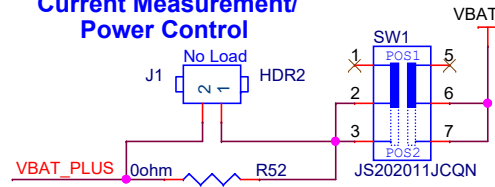
### VBAT Voltage Regulator



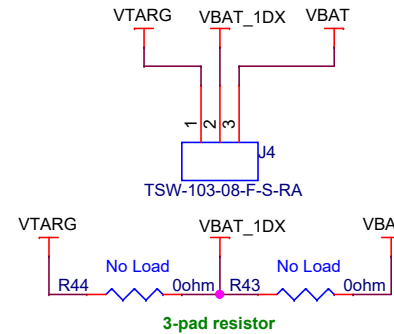
### Battery Connector



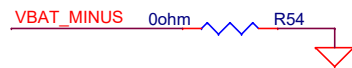
### Current Measurement/Power Control



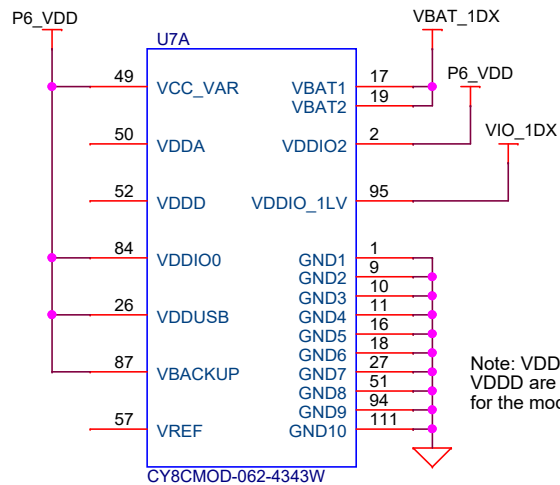
### VBAT\_1DX Selection Jumper



### Battery Protection By-pass

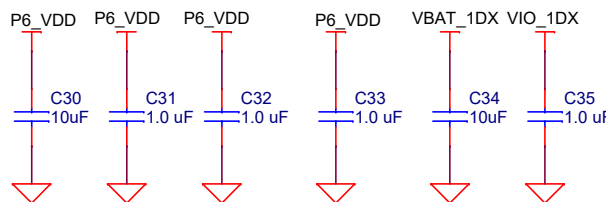


### Module Power

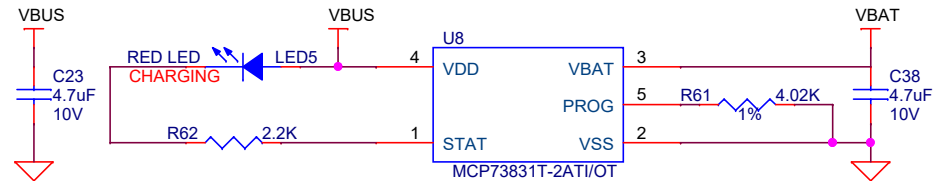


Note: VDDA and VDDD are optional for the module.

### Decoupling Capacitors



### Battery Charger



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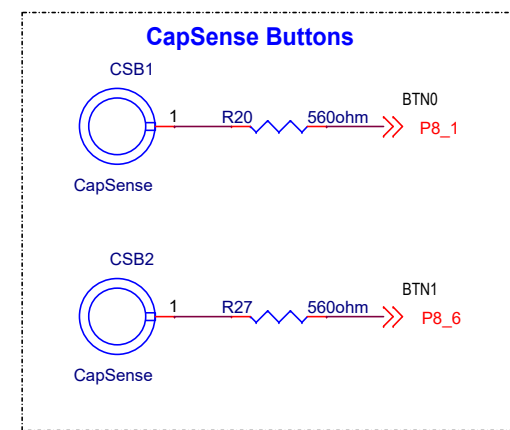
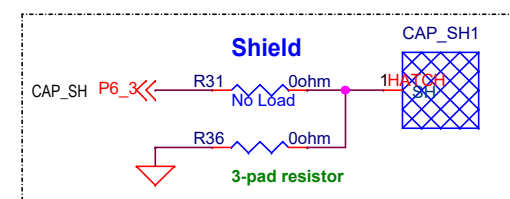
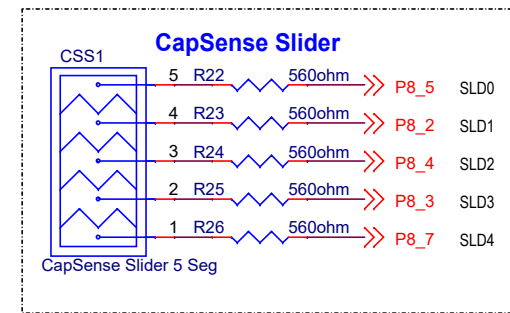
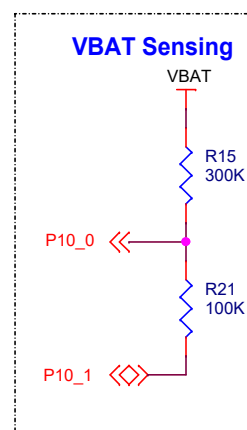
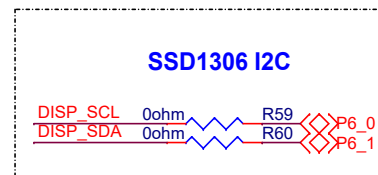
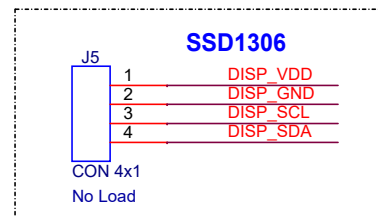
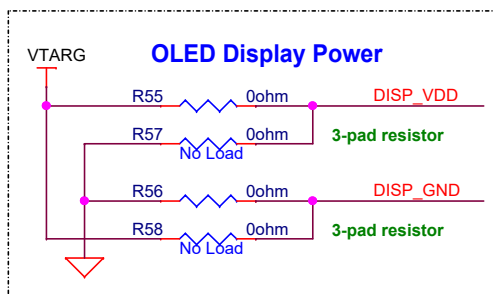
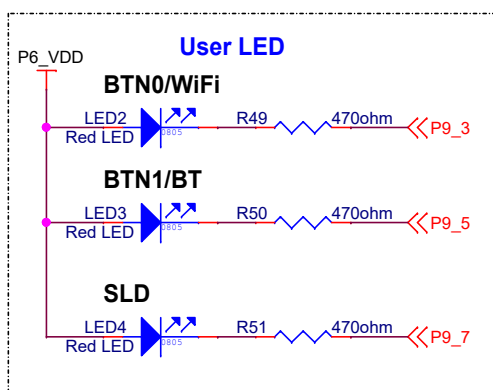
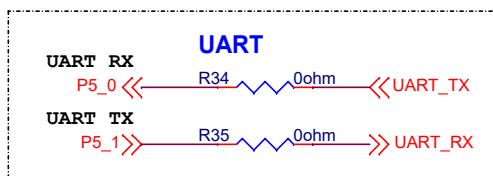
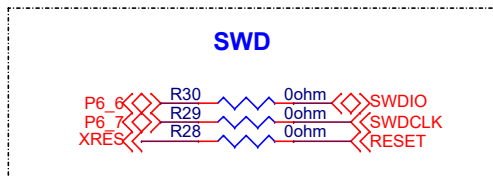
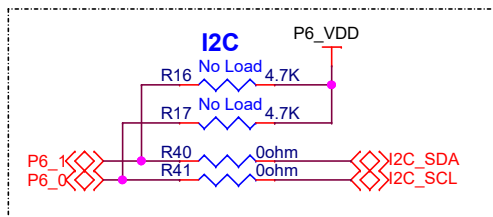
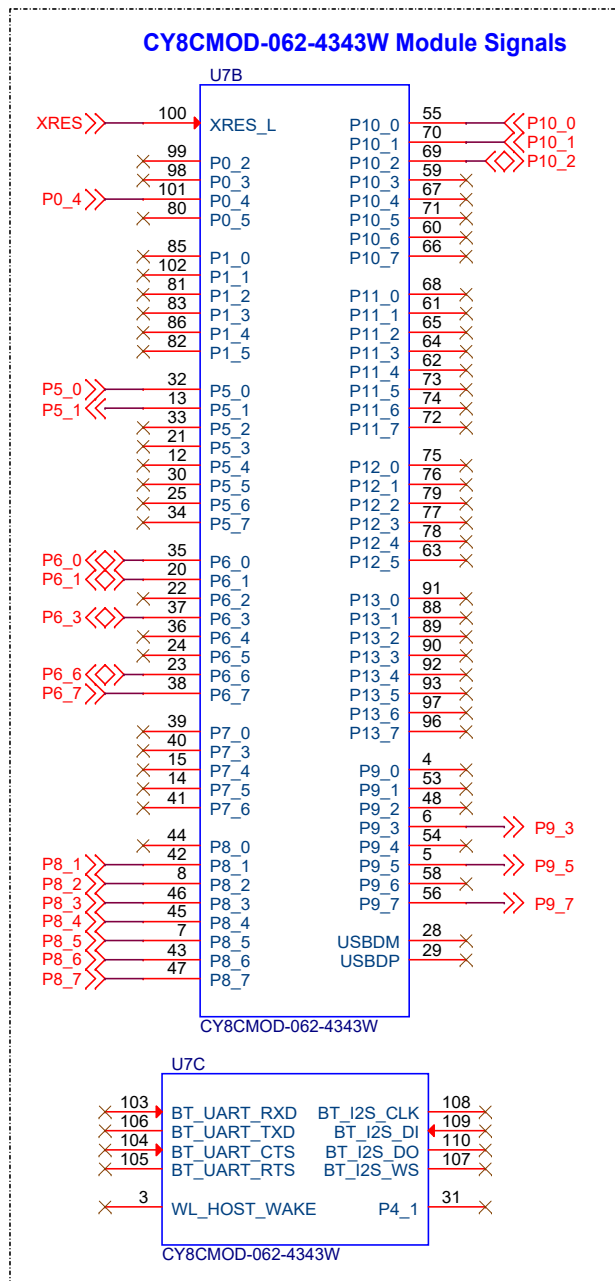
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**Page Title : PSoC 6 Module Power**

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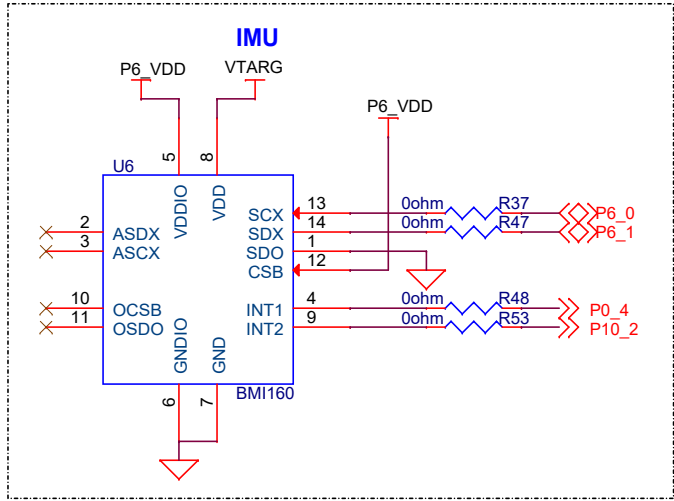
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**Page Title : PSoC 6 Module Signals**

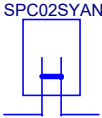
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**Accessories**

**Jumper Shunt**



**Acrylic Overlay**

Acrylic Overlay

Color: Clear, Transparent  
Finish: Matt

Dimensions TBD

**OLED Display and Tape**

SSD1306

Color: Blue

Dimensions :  
27mm x 27mm x  
4mm

Adhesive Tape

Dimensions :  
15mm x 15mm



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**Page Title : IMU and Accessories**

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## REVISION HISTORY

REV	DESCRIPTION OF CHANGE	DATE
00.1	Initial draft	2018/08/29
00.2	Added battery charger	2018/08/30
00.3	1. Added slide switch for power 2. added IMU in page 6 3. added TPs for VBUS, VOUT and P5LP programming	2018/08/31
00.3	1. Added OLED Display to accessories 2. changed LED pins to port 9 3. changed VBAT sense GND to	2018/09/03
00.4	1. Removed Coin cell holder and protection circuit 2. Removed 2 user LEDs	2018/09/06
00.5	1. Added Adhesive tape to accessories 2. Removed Reset switch and associated RC circuit	2018/09/07
00.6	1. Changed CapSense silder to self-cap as used in CY8CKIT-042 2. changed CapSense buttons to self-cap as used in CY8CKIt-050 3. Added block diagram	2018/09/10
00.7	Changed CapSense silder and buttons to optimize routing	2018/09/10



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