

|       |   |    |             |
|-------|---|----|-------------|
| AND   | R | 11 | 10001010000 |
| ANDI  | I | 10 | 1001001000  |
| ANDS  | R | 11 | 11101010000 |
| ANDIS | I | 10 | 1111001000  |

opcode[5:1]

|       |   |    |             |
|-------|---|----|-------------|
| ADD   | R | 11 | 10001011000 |
| ADDI  | I | 10 | 1001000100  |
| ADDS  | R | 11 | 10101011000 |
| ADDIS | I | 10 | 1011000100  |

opcode[9] & opcode[5] & opcode[3:1]

|      |   |    |             |
|------|---|----|-------------|
| ORR  | R | 11 | 10101010000 |
| ORRI | I | 10 | 1011001000  |

opcode[9:8] & opcode[5:1]

|      |   |    |             |
|------|---|----|-------------|
| EOR  | R | 11 | 11001010000 |
| EORI | I | 10 | 1101001000  |

opcode[9:8] & opcode[5:1]

|       |   |    |             |
|-------|---|----|-------------|
| SUB   | R | 11 | 11001011000 |
| SUBI  | I | 10 | 1101000100  |
| SUBS  | R | 11 | 11101011000 |
| SUBIS | I | 10 | 1111000100  |

opcode[9] & opcode[5] & opcode[3:1]

|     |   |    |             |
|-----|---|----|-------------|
| LSR | R | 11 | 11010011010 |
| LSL | R | 11 | 11010011011 |

op code[9:1]

→ LS?  
→ SUB || ADD  
→