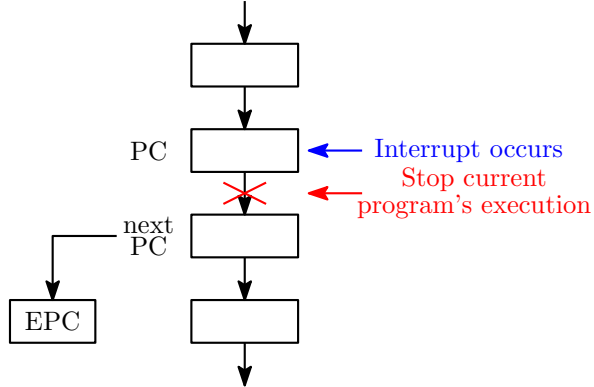


Return address for exceptions



Return address for interrupts