# **GSM3612P**

# **30V N-Channel MOSFETs**

## **Product Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

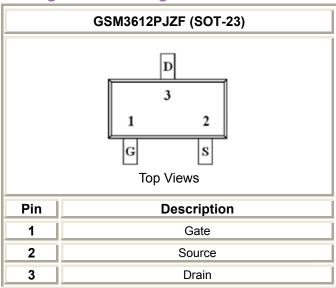
#### **Features**

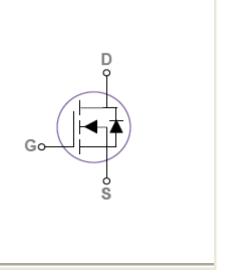
- 30V, 5.3A,  $R_{DS(ON)}$ =32m $\Omega$ @ $V_{GS}$ =4.5V
- Improved dv/dt capability
- Fast switching
- Suit for 2.5V Gate Drive Applications
- Green Device Available
- SOT-23 package design

### **Applications**

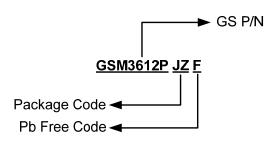
- Notebook
- Load Switch
- LED applications

## **Packages & Pin Assignments**



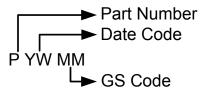


## **Ordering Information**





## **Marking Information**



Part Number	Package	Part Marking	Quantity
GSM3612PJZF	SOT-23	PYWMM	3000pcs

# Absolute Maximum Ratings T<sub>A</sub>=25°C Unless otherwise noted

Symbol	Parameter		Typical	Unit
V <sub>DS</sub>	Drain-Source Voltage		30	V
V <sub>GS</sub>	Gate-Source Voltage		±12	V
ID	Continuous Drain Current	T <sub>A</sub> =25°C	5.3	А
U U	Continuous Drain Current	T <sub>A</sub> =100°C	3.4	
I <sub>DM</sub>	Pulsed Drain Current		21.2	Α
P <sub>D</sub>	Power Dissipation (T <sub>A</sub> =25°ℂ)		1.56	W
FD	Power Dissipation (Derate above 25℃)		0.012	W/°C
TJ	Operating Junction Temperature Range		-55 to +150	$^{\circ}\mathbb{C}$
T <sub>STG</sub>	Storage Temperature Range		-55 to +150	${\mathbb C}$
R <sub>eJA</sub>	Thermal Resistance-Junction to Ambient		80	°C/W



## **Electrical Characteristics**

T<sub>A</sub>=25<sup>°</sup>C Unless otherwise noted

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		Static				
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>D</sub> =250uA	30			V
△BV <sub>DSS</sub> /△T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA		0.06		V/°C
V <sub>GS(th)</sub>	Gate Threshold Voltage		0.4	0.6	0.9	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{DS}=V_{GS},I_{D}=250uA$		-3		mV/ ℃
I <sub>GSS</sub>	Gate Leakage Current	V <sub>DS</sub> =0V,V <sub>GS</sub> =±12V			±100	nA
		V <sub>DS</sub> =30V,V <sub>GS</sub> =0V			1	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =24V,V <sub>GS</sub> =0V, T <sub>J</sub> =125°C			10	uA
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V,			5.3	_
I <sub>SM</sub>	Pulsed Source Current	Force Current			21.2	А
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =4.5V,I <sub>D</sub> =4A	27 3:		32	mΩ
		V <sub>GS</sub> =2.5V,I <sub>D</sub> =3A		32	40	11122
<b>g</b> FS	Forward Transconductance	V <sub>DS</sub> =10V,I <sub>D</sub> =3A		7		S
$V_{\text{SD}}$	Diode Forward Voltage	V <sub>GS</sub> =0V,I <sub>S</sub> =1A			1	V
		Dynamic				
$Q_g$	Total Gate Charge	)/ 40)/// 4.5\/		8.4	12	
$Q_gs$	Gate-Source Charge	$V_{DS}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =4A		1	2	nC
$Q_{gd}$	Gate-Drain Charge	]		2.2	4	
C <sub>iss</sub>	Input Capacitance	)/ =40)/)/ =0)/		695	1000	
Coss	Output Capacitance	$V_{DS}$ =10V, $V_{GS}$ =0V, f=1MHz		45	65	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			36	50	
t <sub>d(on)</sub>	Turn-On Time			4.5	9	
t <sub>r</sub>	Tam on this	V <sub>DD</sub> =10V,I <sub>D</sub> =1A,		13	25	ns
$t_{d(off)}$	Turn-Off Time	$V_{GS}$ =4.5V, $R_{G}$ =25 $\Omega$		27	51	
t <sub>f</sub>	12 2 1			8.3	16	
$R_g$	Gate Resistance	V <sub>DS</sub> =0V,V <sub>GS</sub> =0V, f=1MHz		1.5	3	Ω



## **Typical Performance Characteristics**

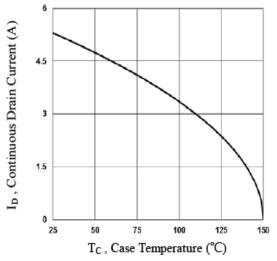


Fig.1 Continuous Drain Current vs. Tc

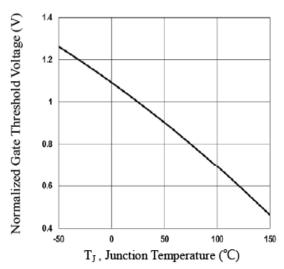


Fig.3 Normalized  $V_{th}$  vs.  $T_J$ 

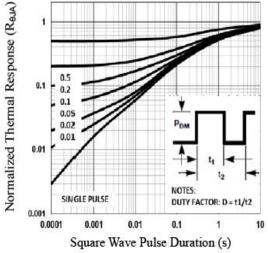


Fig.5 Normalized Transient Impedance

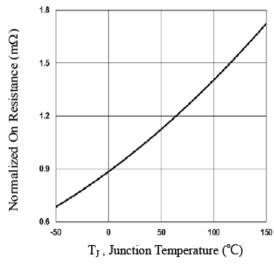


Fig.2 Normalized RDSON vs. T,

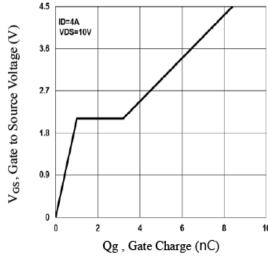


Fig.4 Gate Charge Waveform

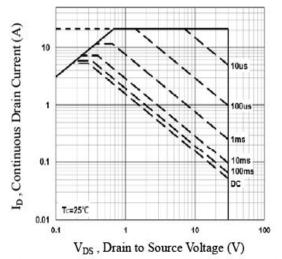
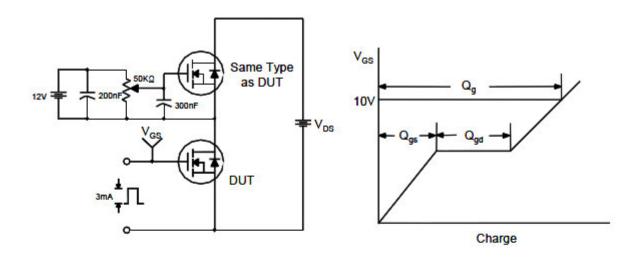


Fig.6 Maximum Safe Operation Area

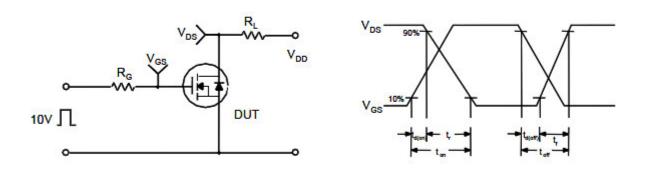


## **Typical Performance Characteristics (Continue)**

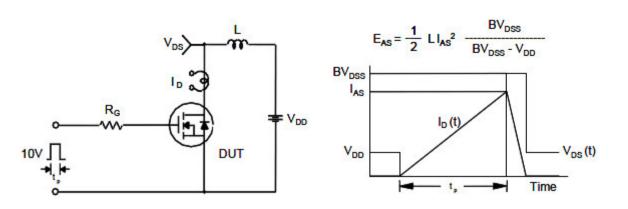
## Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms

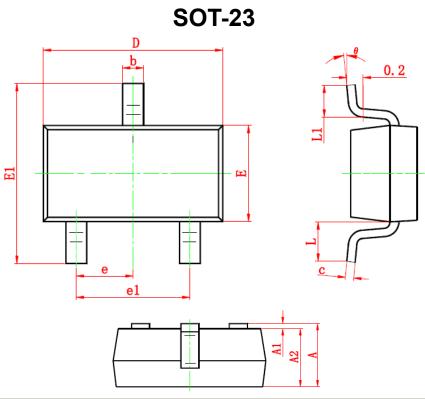


## Unclamped Inductive Switching Test Circuit & Waveforms





# **Package Dimension**



	Dimensions				
Symbol	Millimeters		Inches		
Зуппоот	Min	Max	Min	Max	
Α	0.900	1.200	0.035	0.043	
<b>A</b> 1	0.000	0.100	0.000	0.004	
A2	0.900	1.100	0.035	0.039	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037	7 TYP	
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022	REF	
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	6°	



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