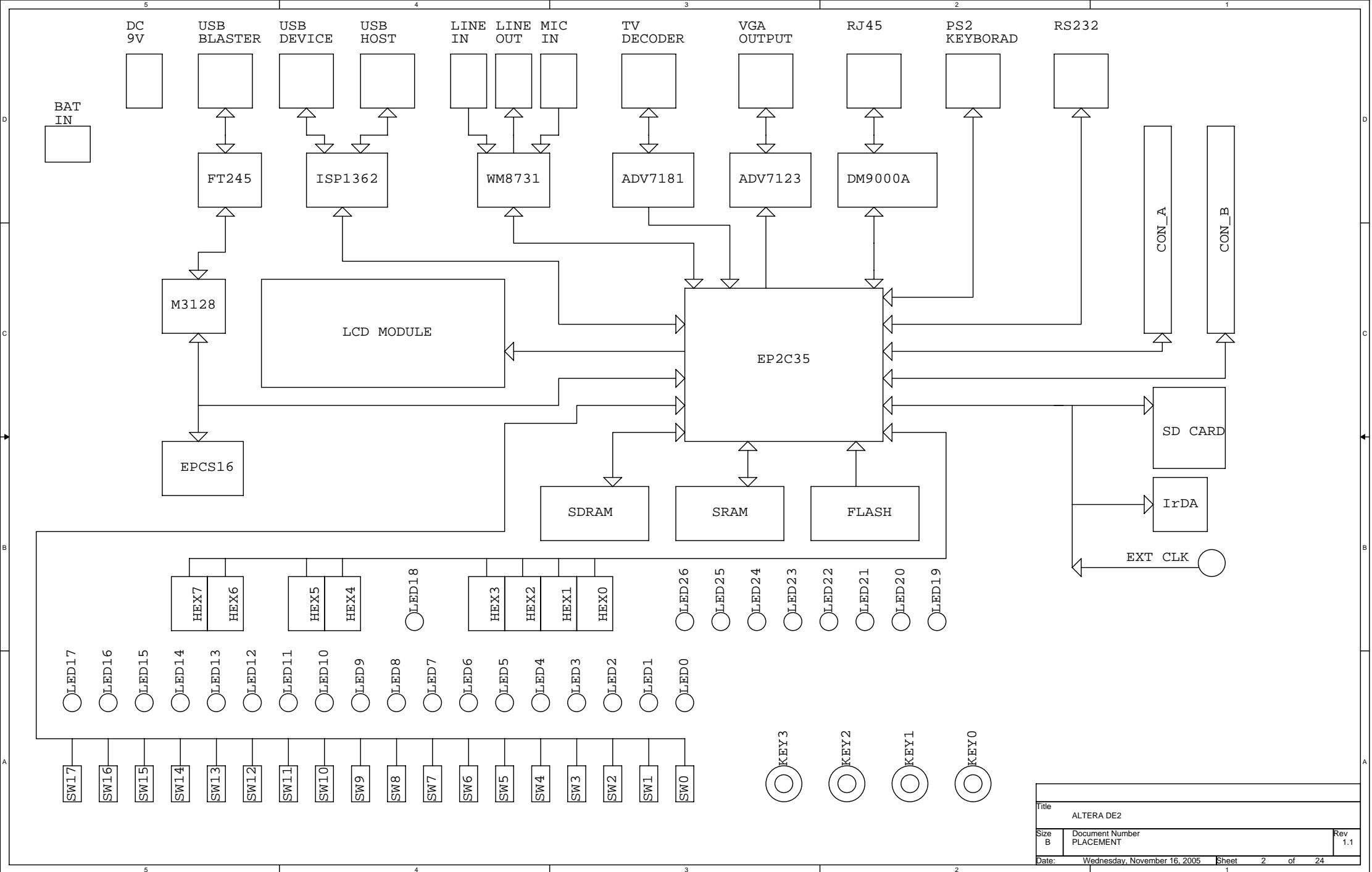
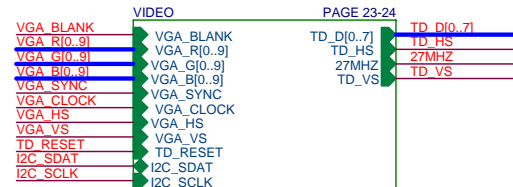
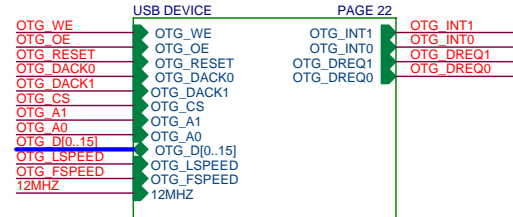
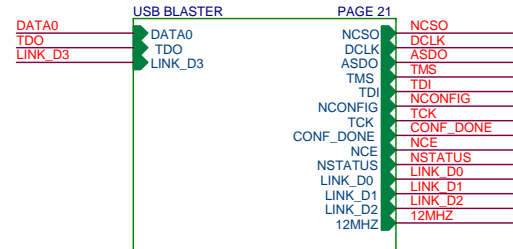
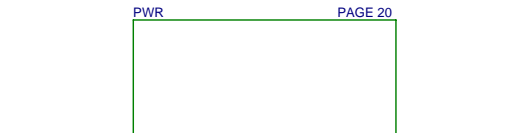
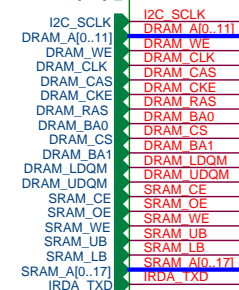
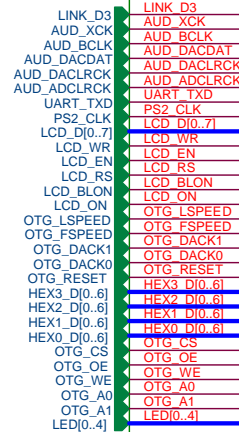
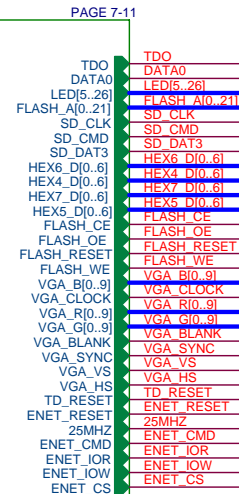
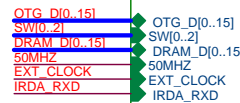
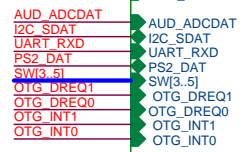
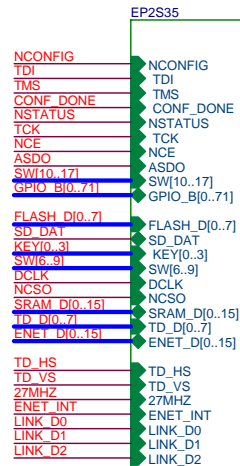
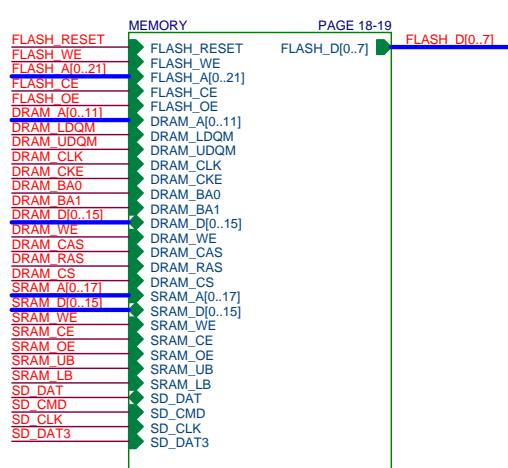
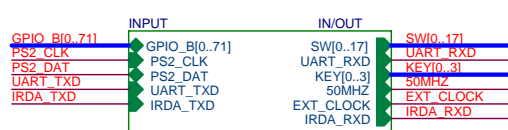
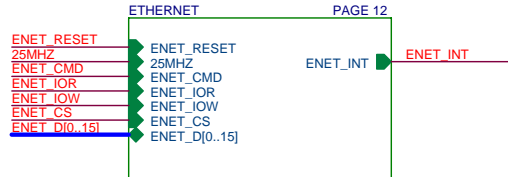
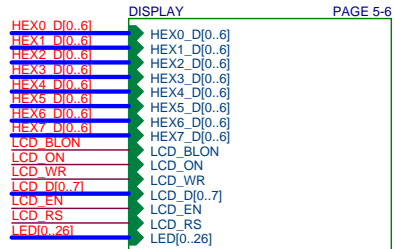
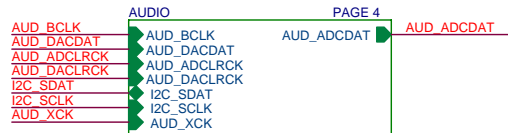
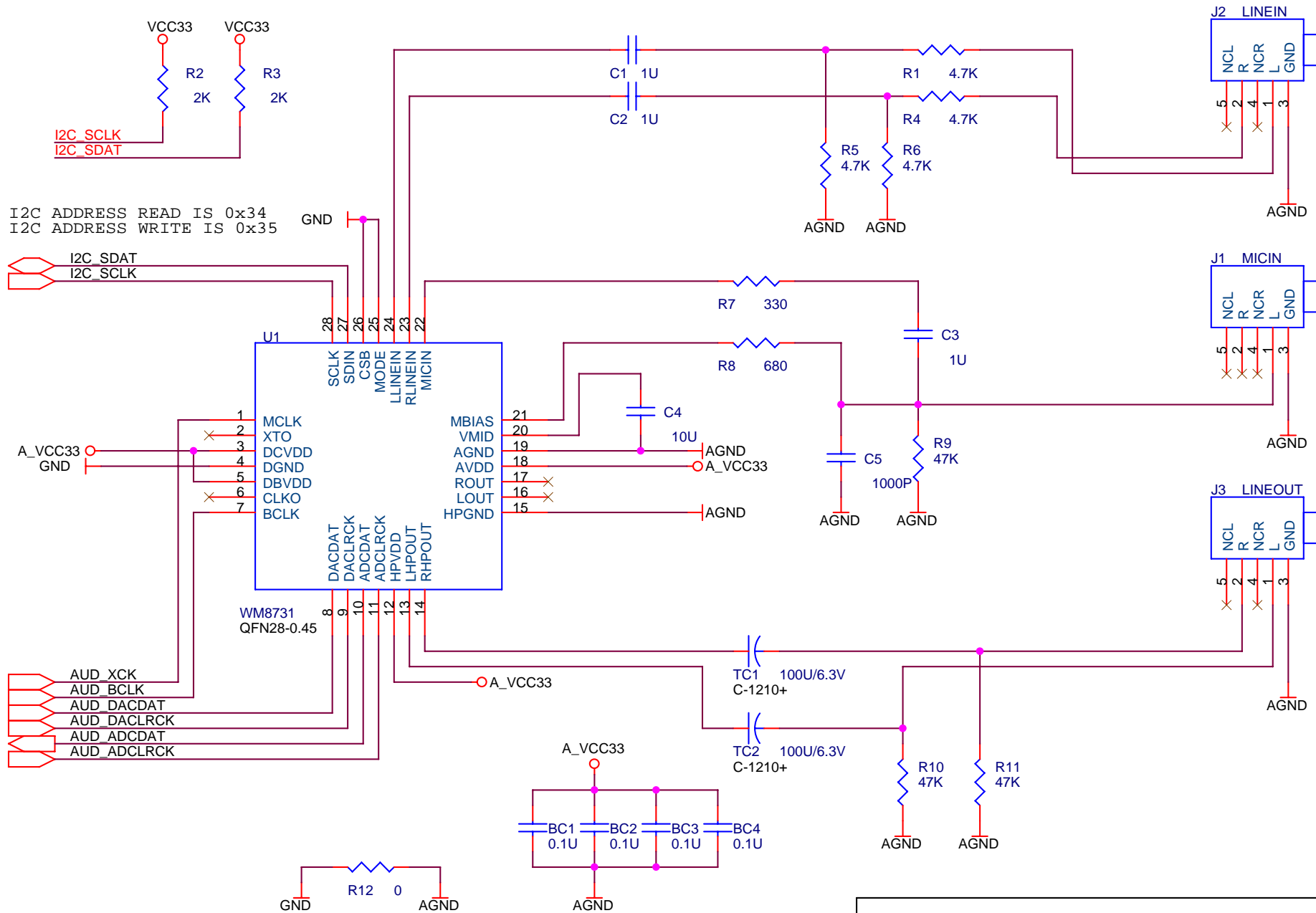


TERASIC CYCLONE II EP2C35 DAUGHTER BOARD

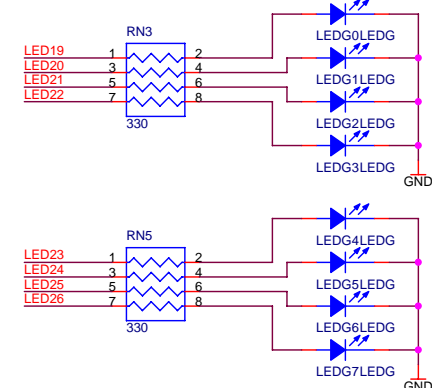
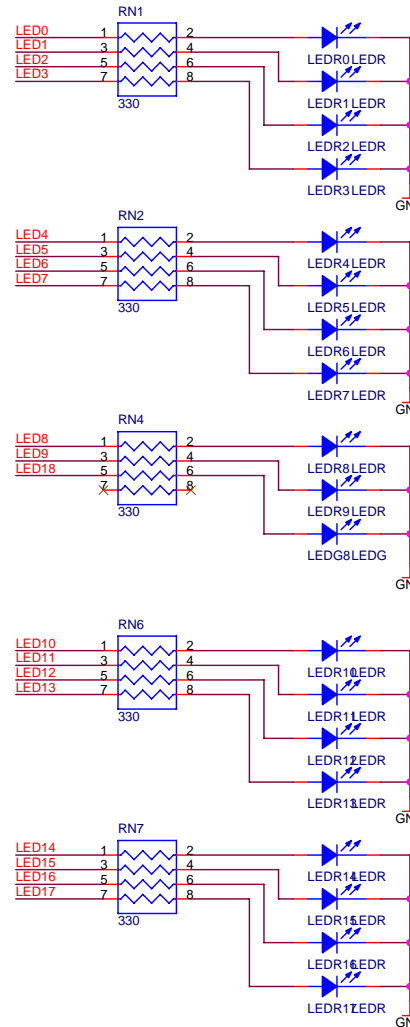
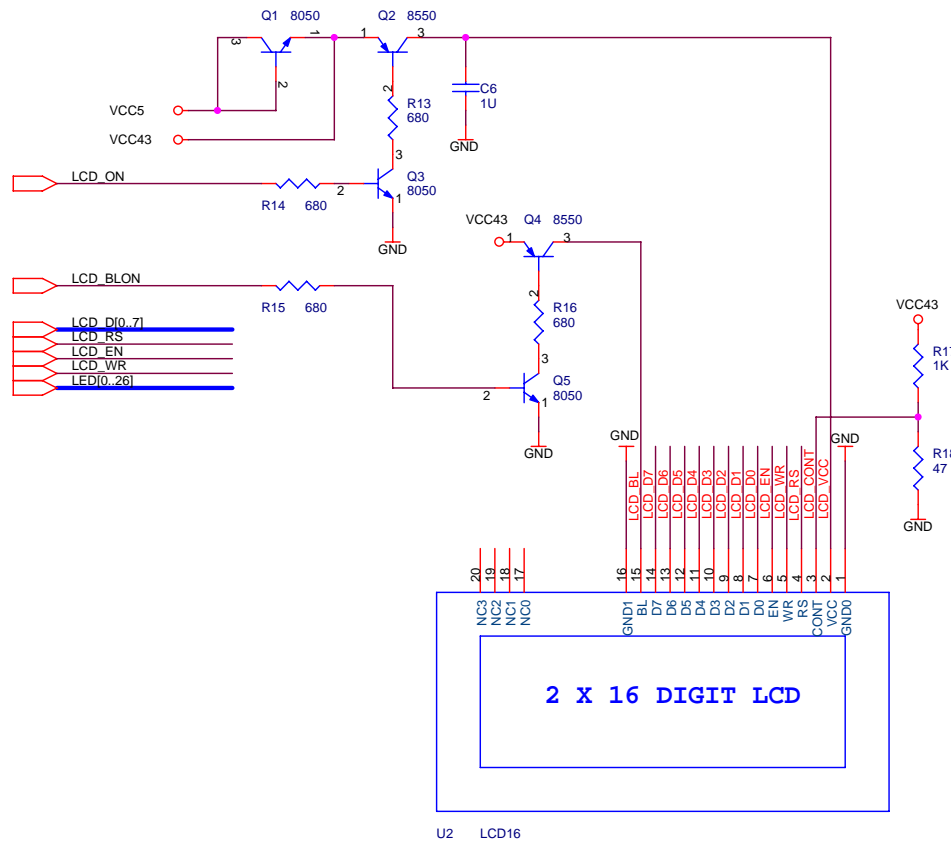
SCHEMATIC	CONTENT	PAGE
TOP	COVER PAGE , TOP	01 ~ 03
AUDIO	WM8731	04 ~ 04
DISPLAY	LCD , LED , 7SEGMENT	05 ~ 06
EP2C35	EP2C35 BANK1..BANK8 , POWER , CONFIG	07 ~ 11
ETHERNET	DM9000A	12 ~ 12
INPUT	CLOCK , PS2 , RS232 , KEY , SWITCH , CONNECT	13 ~ 17
MEMORY	SRAM , DRAM , FLASH , SD CARD	18 ~ 19
POWER	POWER	20 ~ 20
USB BLASTER	USB BLASTER	21 ~ 21
USB DEVICE	USB DEVICE	22 ~ 22
VIDEO	ADV7181 , ADV7123	23 ~ 24

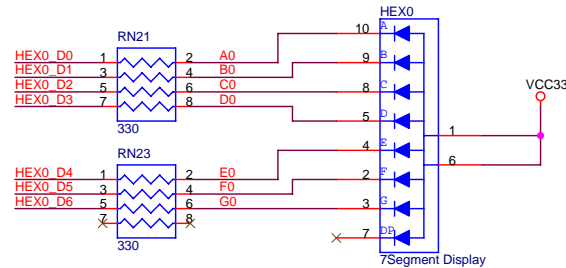
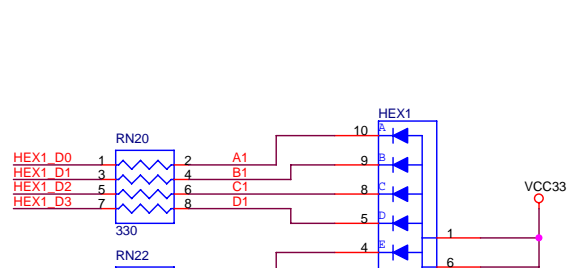
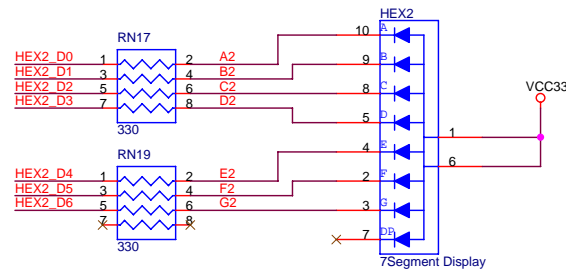
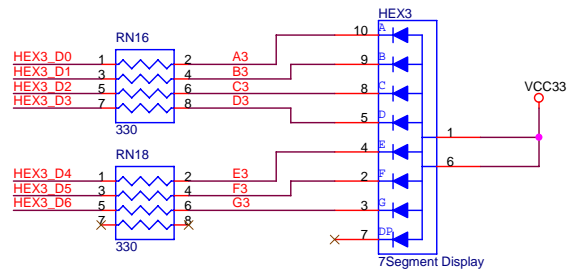
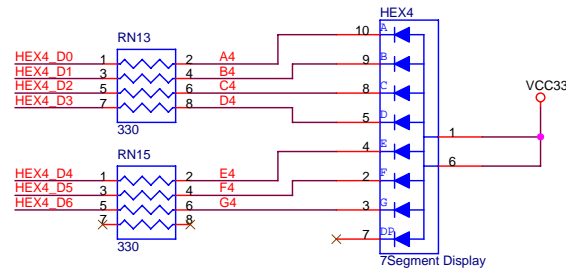
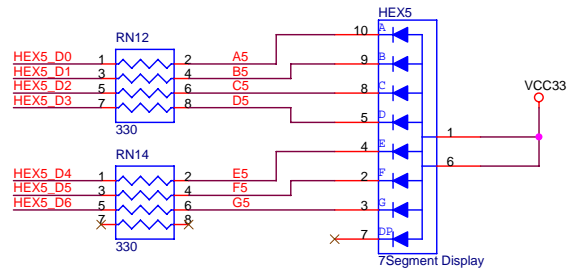
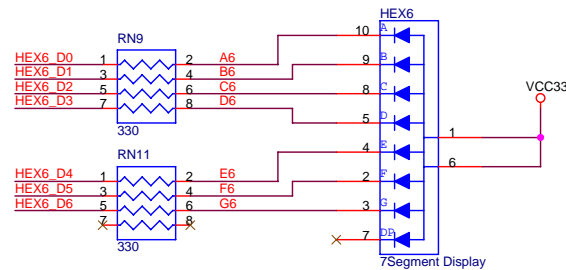
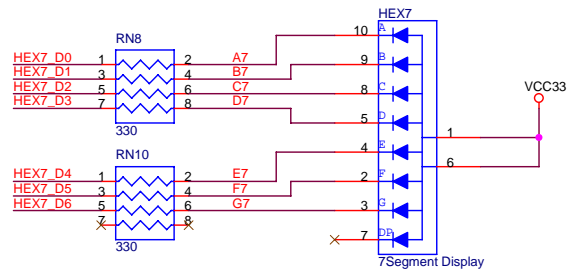
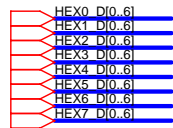




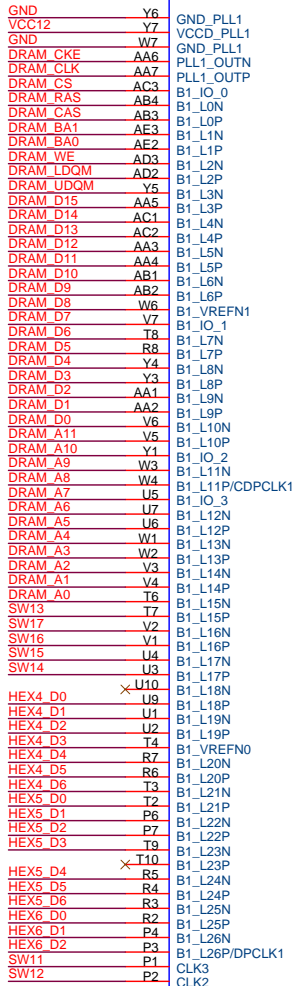
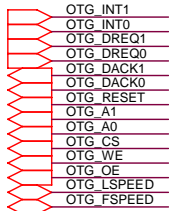
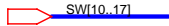
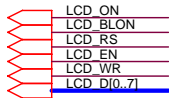
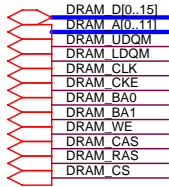
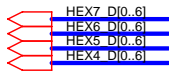


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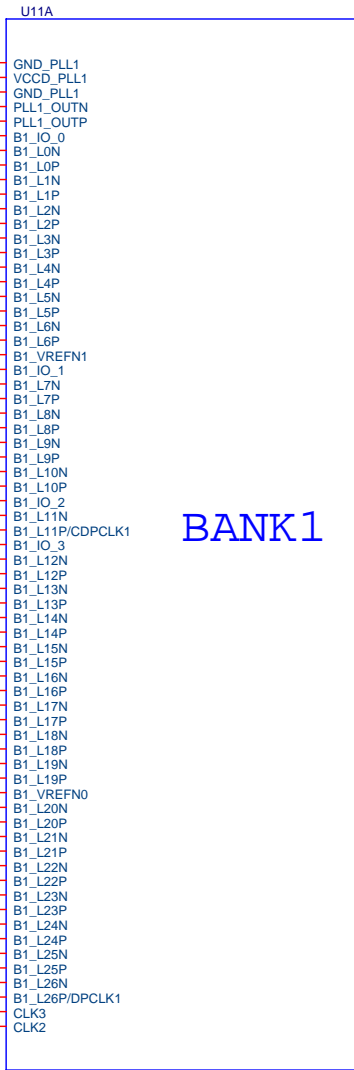




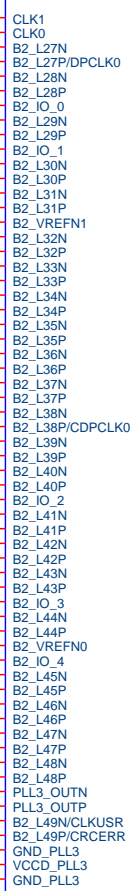
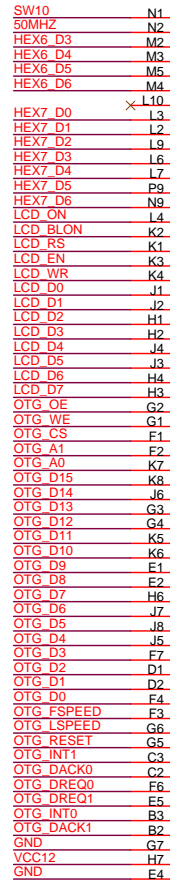
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B	7 SEGMENT	1.1	
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BANK 1

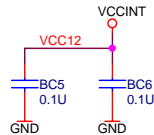


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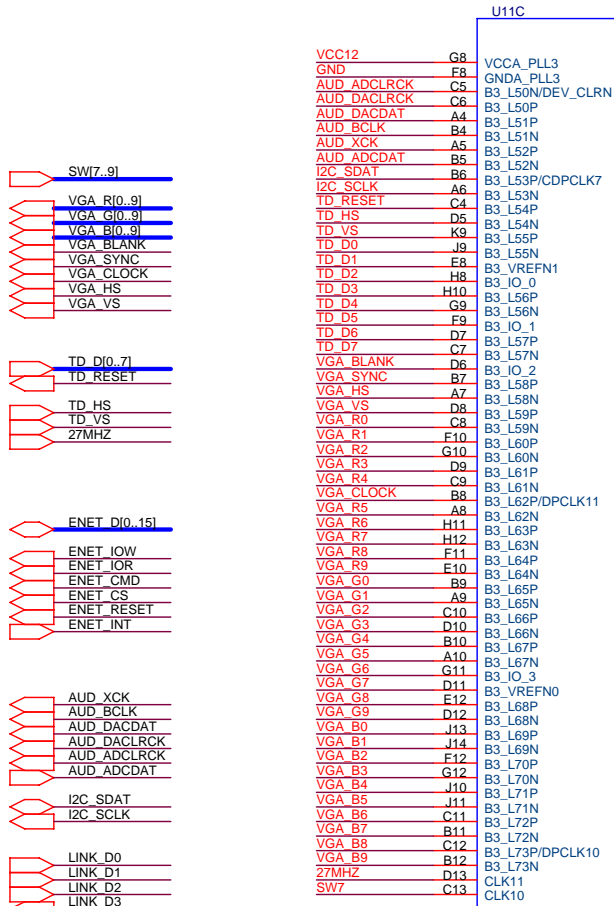


BANK 2

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Title		
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B	EP2C35 BANK1 AND BANK 2	1.1
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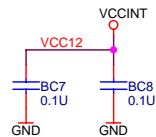


BANK 3

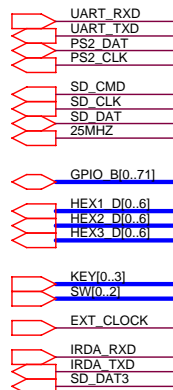
BANK 4

CYCLONE II EP2C35

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Title		
ALTERA DE2		
Size	Document Number	Rev
B	EP2C35 BANK3 AND BANK 4	1.1
Date:	Wednesday, November 16, 2005	Sheet 8 of 24



GND	E21	GND_PLL2
VCC12	H20	VCCD_PLL2
GND	G20	GND_PLL2
	F20	PLL2_OUTN
	F21	PLL2_OUTP
	E22	B5_L100P
	D23	B5_L100N
	G21	B5_L101P
	G22	B5_L101N
	H21	B5_IO_0
	E23	B5_L102P
	E24	B5_L102N
25MHZ	B24	B5_L103P
UART_TXD	B25	B5_L103N
UART_RXD	C25	B5_L104P
PS2_DAT	C24	B5_L104N
PS2_CLK	D26	B5_L105P
GPIO_B0	D25	B5_L105N
GPIO_B1	J22	B5_VREFN0
GPIO_B2	E26	B5_L106P
GPIO_B3	E25	B5_L106N
GPIO_B4	F24	B5_L107P
GPIO_B5	F23	B5_L107N
GPIO_B6	J21	B5_L108P
GPIO_B7	J20	B5_L108N
GPIO_B8	F25	B5_L109P
GPIO_B9	F26	B5_L109N
GPIO_B10	N18	B5_L110P
GPIO_B11	P18	B5_L110N
GPIO_B12	G23	B5_L111P
GPIO_B13	G24	B5_L111N
GPIO_B14	K22	B5_IO_1
GPIO_B15	G25	B5_L112P/CDPCLK5
KEY0	G26	B5_L112N
GPIO_B16	H23	B5_L113P
GPIO_B17	H24	B5_L113N
GPIO_B18	J23	B5_L114P
GPIO_B19	J24	B5_L114N
GPIO_B20	H25	B5_L115P
GPIO_B21	H26	B5_L115N
GPIO_B22	H19	B5_IO2
GPIO_B23	K18	B5_L116P
GPIO_B24	K19	B5_L116N
GPIO_B25	K21	B5_IO_3
GPIO_B26	K23	B5_L117P
GPIO_B27	K24	B5_L117N
GPIO_B28	L21	B5_L118P
GPIO_B29	L20	B5_L118N
GPIO_B30	J25	B5_L119P
GPIO_B31	J26	B5_L119N
GPIO_B32	L23	B5_VREFN1
GPIO_B33	L24	B5_L120P
GPIO_B34	L25	B5_L120N
GPIO_B35	L19	B5_IO_4
GPIO_B36	K25	B5_L121P
GPIO_B37	K26	B5_L121N
GPIO_B38	M22	B5_L122P
GPIO_B39	M23	B5_L122N
GPIO_B40	M19	B5_L123P
GPIO_B41	M20	B5_L123N
GPIO_B42	N20	B5_L124P
GPIO_B43	M21	B5_L124N
GPIO_B44	M24	B5_L125P
GPIO_B45	M25	B5_L125N
KEY1	N23	B5_L126P/DPCLK7
GPIO_B46	N24	B5_L126N
SW0	N25	CLK4
SW1	N26	CLK5

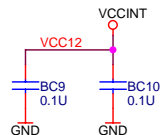
BANK5

CYCLONE II EP2C35

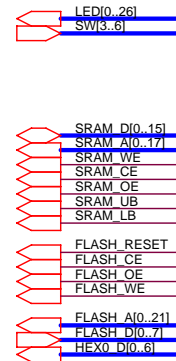
SW2	P25	CLK6
EXT_CLOCK	P26	CLK7
KEY2	P23	B6_L127P/DPCLK6
GPIO_B47	P24	B6_L127N
GPIO_B48	R25	B6_L128P
GPIO_B49	R24	B6_L128N
GPIO_B50	R20	B6_IO_0
GPIO_B51	T22	B6_L129P
GPIO_B52	T23	B6_L129N
	R17	B6_L130P
	P17	B6_L130N
GPIO_B53	T24	B6_L131P
GPIO_B54	T25	B6_L131N
GPIO_B55	T18	B6_L132P
	T17	B6_L132N
GPIO_B56	T21	B6_VREFN0
GPIO_B57	T20	B6_IO_1
GPIO_B58	U26	B6_L133P
GPIO_B59	U25	B6_L133N
GPIO_B60	U23	B6_L134P
GPIO_B61	U24	B6_L134N
GPIO_B62	R19	B6_L135P
GPIO_B63	T19	B6_L135N
GPIO_B64	U20	B6_L136P
GPIO_B65	U21	B6_L136N
GPIO_B66	V26	B6_L137P
GPIO_B67	V25	B6_L137N
GPIO_B68	V24	B6_L138P
GPIO_B69	V23	B6_L138N
KEY3	W26	B6_L139P/CDPCLK4
GPIO_B70	W25	B6_L139N
GPIO_B71	W23	B6_L140P
HEX3_D6	W24	B6_L140N
HEX3_D5	U22	B6_IO_2
HEX3_D4	Y25	B6_L141P
HEX3_D3	Y26	B6_L141N
HEX3_D2	AA26	B6_L142P
HEX3_D1	AA25	B6_L142N
HEX3_D0	Y23	B6_L143P
HEX2_D6	Y24	B6_L143N
HEX2_D5	AB25	B6_L144P
HEX2_D4	AB26	B6_L144N
HEX2_D3	AC26	B6_L145P
HEX2_D2	AC25	B6_L145N
HEX2_D1	V22	B6_VREFN1
HEX2_D0	AB23	B6_L146P
HEX1_D6	AB24	B6_L146N
HEX1_D5	AA23	B6_L147P
HEX1_D4	AA24	B6_L147N
HEX1_D3	Y22	B6_L148P
HEX1_D2	W21	B6_L148N
HEX1_D1	V21	PLL4_OUTN
HEX1_D0	V20	PLL4_OUTP
SD_CMD	Y21	B6_IO_3
SD_DAT	AD24	B6_L149P
SD_CLK	AD25	B6_L149N
IRDA_TXD	AE24	B6_L150P/NCEO
IRDA_RXD	AE25	B6_L150N/INIT_DONE
SD_DAT3	AC23	B6_IO_4
GND	W20	GND_PLL4
VCC12	Y20	VCCD_PLL4
GND	AA21	GND_PLL4

BANK6

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B	EP2C35 BANKS AND BANK 6	1.1
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VCC12	AA19	VCCA_PLL4
GND	Y19	GNDA_PLL4
LED0	AE23	B7_L151N
LED1	AF23	B7_L151P
LED2	AB21	B7_L152N
LED3	AC22	B7_L152P
LED4	AD22	B7_L153N
LED5	AD23	B7_L153P
LED6	AD21	B7_L154N
LED7	AC21	B7_L154P/CDPCLK3
LED19	AE22	B7_L155N
LED20	AE22	B7_L155P
LED21	W19	B7_L156N
LED22	V18	B7_L156P
LED23	U18	B7_L157N
LED24	U17	B7_L157P
LED25	AA20	B7_L157P
LED26	Y18	B7_L157P
FLASH D7	AE21	B7_L158N
FLASH D6	AE21	B7_L158P
FLASH D5	AC20	B7_L159N
FLASH D4	AB20	B7_L159P
FLASH D3	AE20	B7_L160N
FLASH D2	AE20	B7_L160P
FLASH D1	AC19	B7_L161N
FLASH D0	AD19	B7_L161P
FLASH RESET	AA18	B7_L162N
FLASH WE	AA17	B7_L162P
FLASH CE	V17	B7_L163N
FLASH OE	W17	B7_L163P
FLASH A0	AC18	B7_L164N
FLASH A1	AB18	B7_L164P
FLASH A2	AE19	B7_L165N
FLASH A3	AE19	B7_L165P
FLASH A4	AE18	B7_L166N
FLASH A5	AE18	B7_L166P
FLASH A6	Y16	B7_L167N
FLASH A7	AA16	B7_L167P
FLASH A8	AD17	B7_L168N
FLASH A9	AC17	B7_L168P
FLASH A10	AE17	B7_L169N
FLASH A11	AE17	B7_L169P
FLASH A12	W16	B7_L170N
FLASH A13	W15	B7_L170P
FLASH A14	AC16	B7_L171N
FLASH A15	AD16	B7_L171P
FLASH A16	AE16	B7_L172N
FLASH A17	AC15	B7_L172P
FLASH A18	AB15	B7_L173N
FLASH A19	AA15	B7_L173P
FLASH A20	Y15	B7_L174N
FLASH A21	Y14	B7_L174P
LED8	AA14	B7_L175N
LED9	Y13	B7_L175P
LED10	AA13	B7_L176N
LED11	AC14	B7_L176P
LED12	AD15	B7_L177N
LED13	AE15	B7_L177P
SW3	AE14	CLK12
SW4	AF14	CLK13

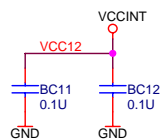
BANK 7

CYCLONE II EP2C35

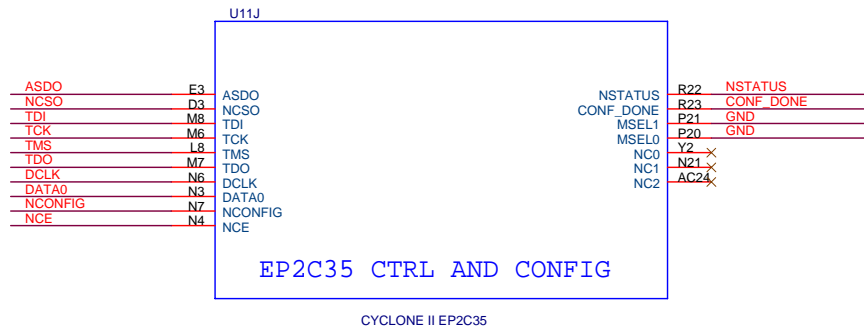
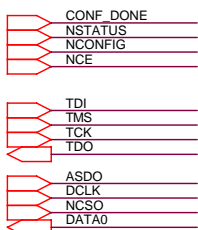
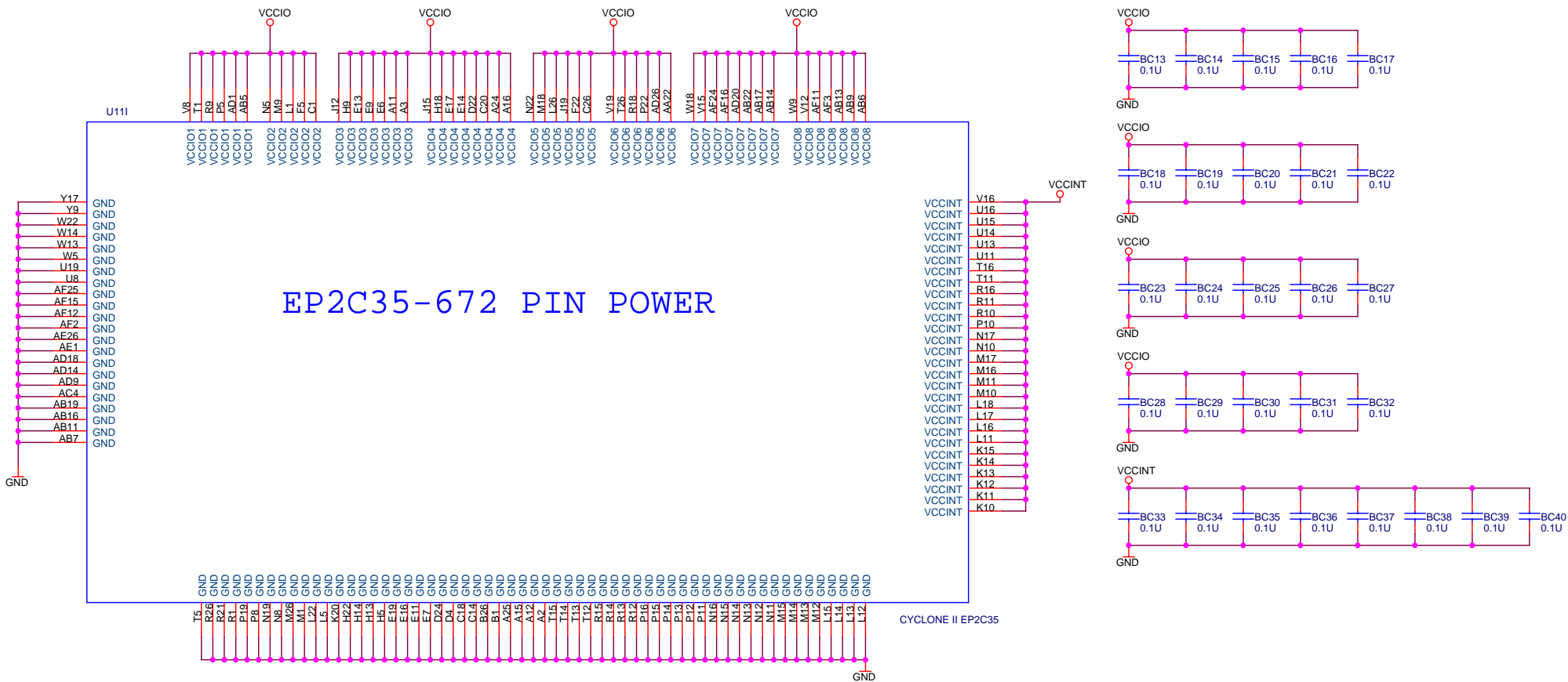
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SW6	AC13	CLK15
LED14	AE13	B8_L177N
LED15	AE13	B8_L177P/DPCLK3
LED16	AE12	B8_L178N
LED17	AD12	B8_L178P
LED18	Y12	B8_L179N
	AA12	B8_L179P
	U12	B8_L180N
	V11	B8_L180P
	V13	B8_L181N
	V14	B8_L181P
HEX0 D6	AE11	B8_L182N
HEX0 D5	AD11	B8_L182P
HEX0 D4	AC12	B8_L183N
HEX0 D2	AB12	B8_L183P
HEX0 D1	AF10	B8_L184N
HEX0 D0	AF10	B8_L184P
SRAM WE	AC11	B8_L185N
SRAM CE	AD10	B8_L185P
SRAM OE	AE9	B8_L186N
SRAM UB	AC10	B8_L186P
SRAM LB	AC9	B8_L187N
SRAM D15	W12	B8_L187P
SRAM D14	W11	B8_L188N
SRAM D13	AF8	B8_L188P/DPCLK2
SRAM D12	AE8	B8_L189N
SRAM D11	AF7	B8_L189P
SRAM D10	AE7	B8_L190N
SRAM D9	Y11	B8_L190P
SRAM D8	AA11	B8_L191N
SRAM D7	AB10	B8_L191P
SRAM D6	AA10	B8_L192N
SRAM D5	AA9	B8_L192P
SRAM D4	AE6	B8_L193N
SRAM D3	AE6	B8_L193P
SRAM D2	AD8	B8_L194N
SRAM D1	AC8	B8_L194P
SRAM D0	AB8	B8_L195N
SRAM A17	Y10	B8_L195P
SRAM A16	W10	B8_L196N
SRAM A15	W8	B8_L196P
SRAM A14	AC7	B8_L197N
SRAM A13	V9	B8_L197P
SRAM A12	V10	B8_L198N
SRAM A11	AD7	B8_L198P
SRAM A10	AD6	B8_L199N
SRAM A9	AE5	B8_L199P
SRAM A8	AE5	B8_L200P
SRAM A7	AD5	B8_L200N/DEV_OE
SRAM A6	AD4	GNDA_PLL1
SRAM A5	AC6	VCCA_PLL1
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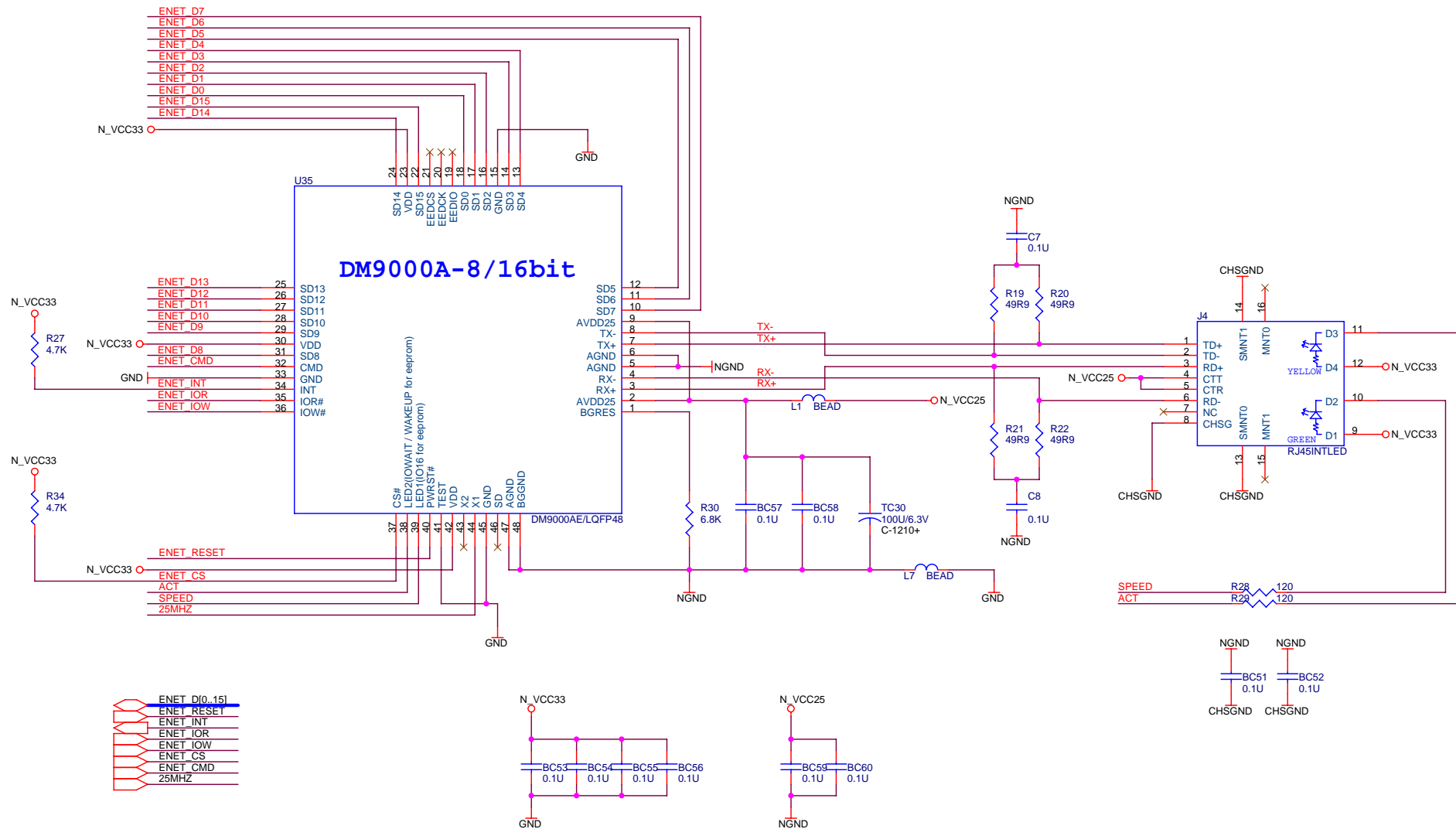
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CYCLONE II EP2C35

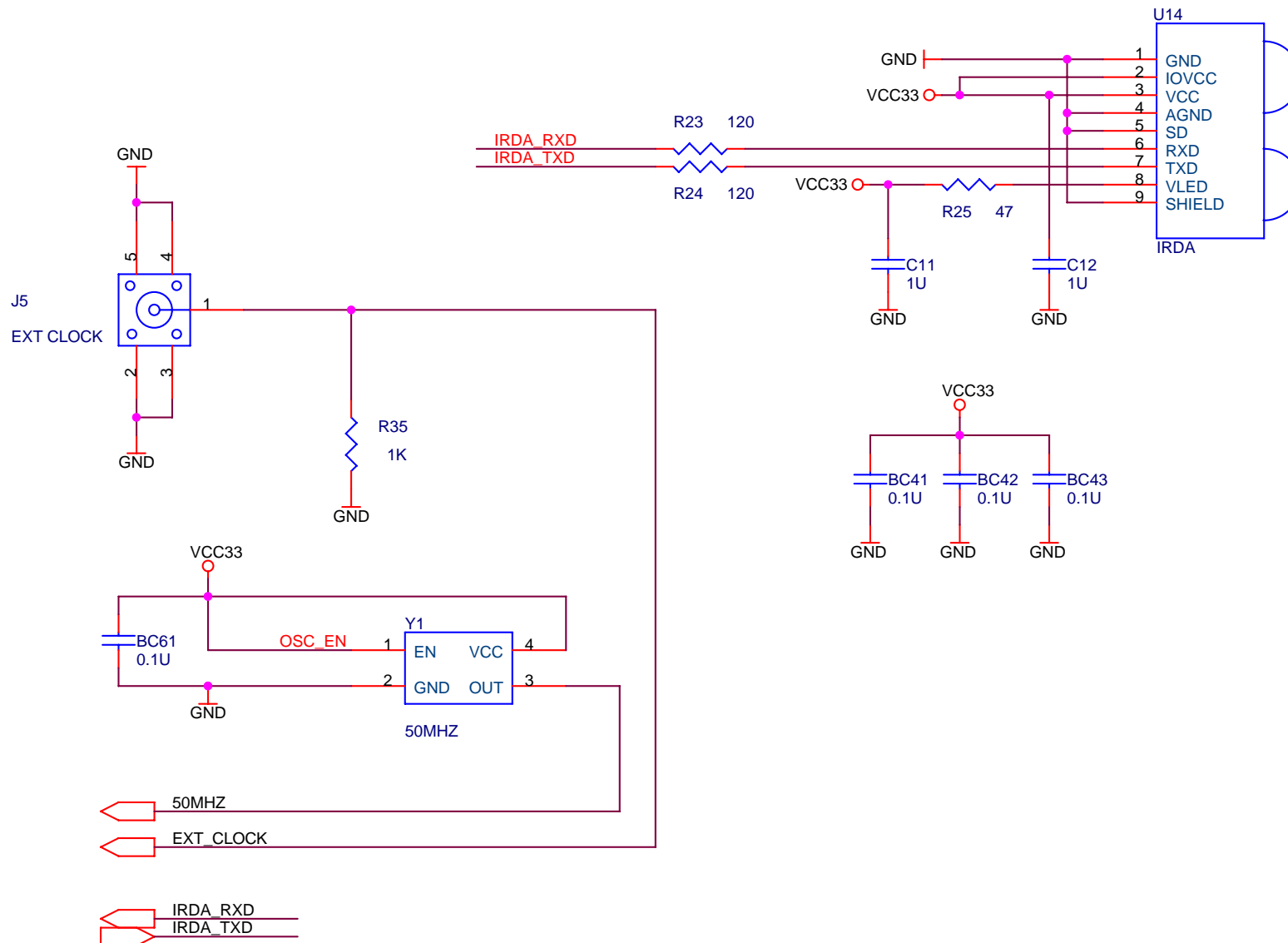


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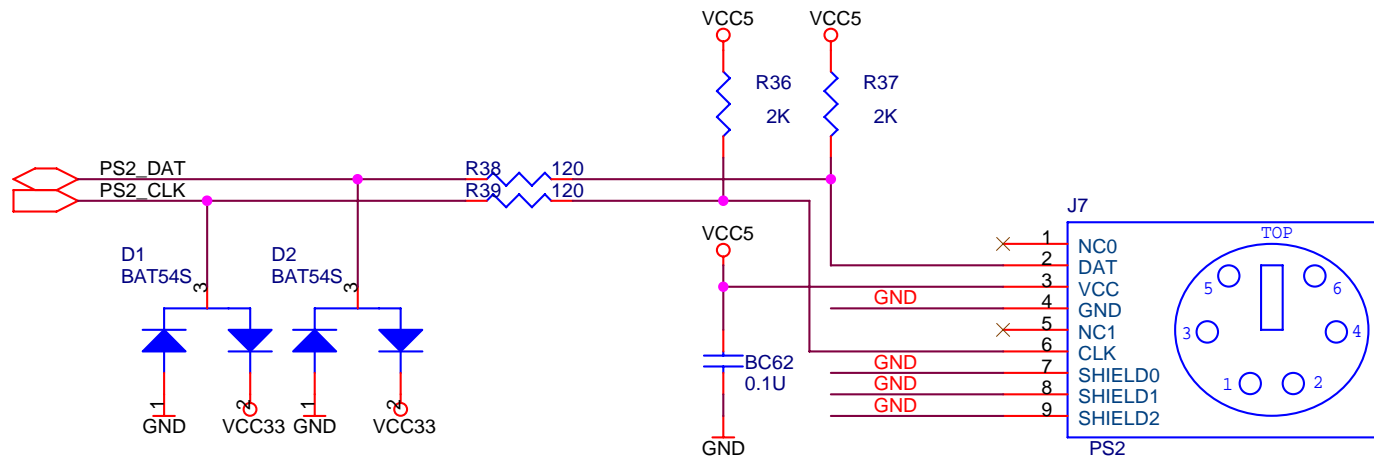
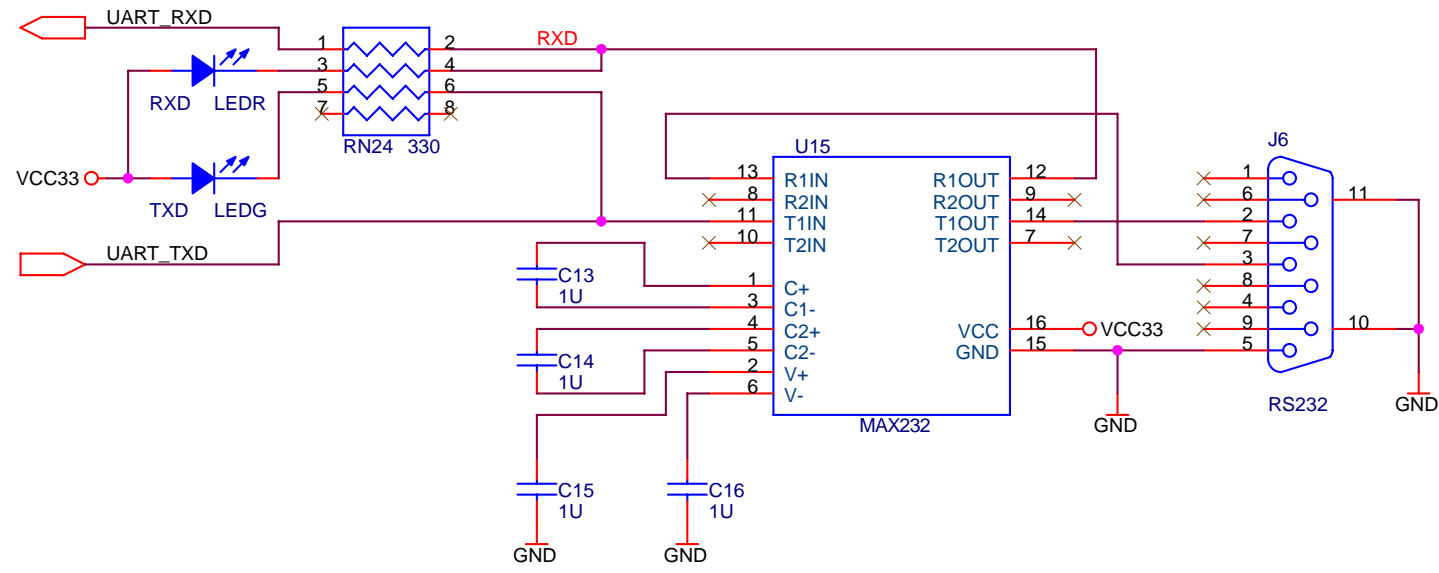




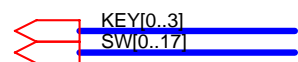
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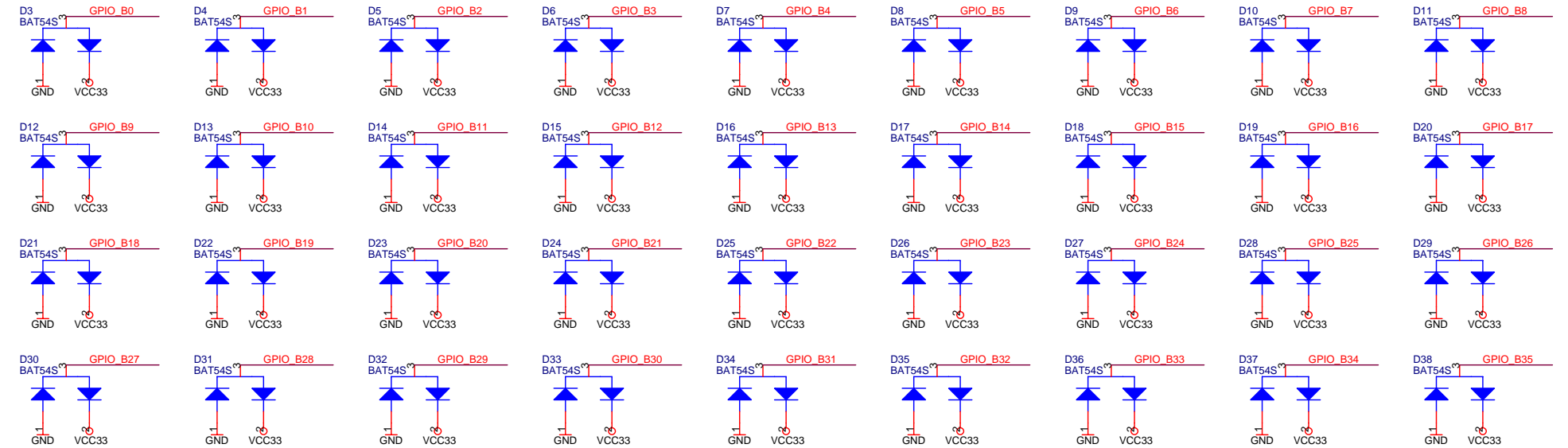
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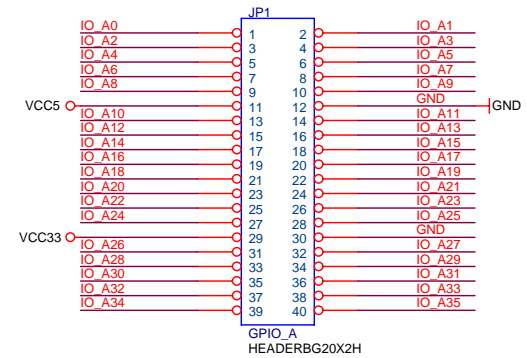
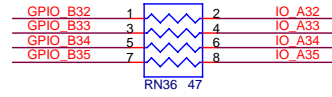
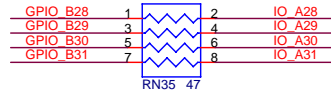
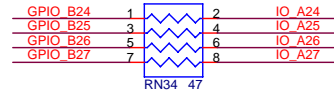
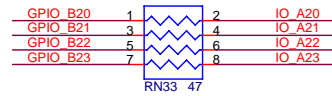
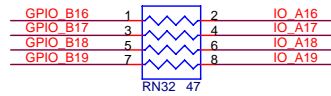
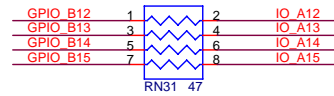
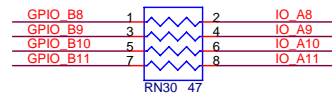
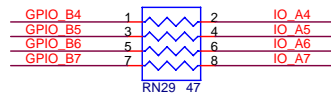
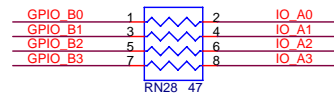
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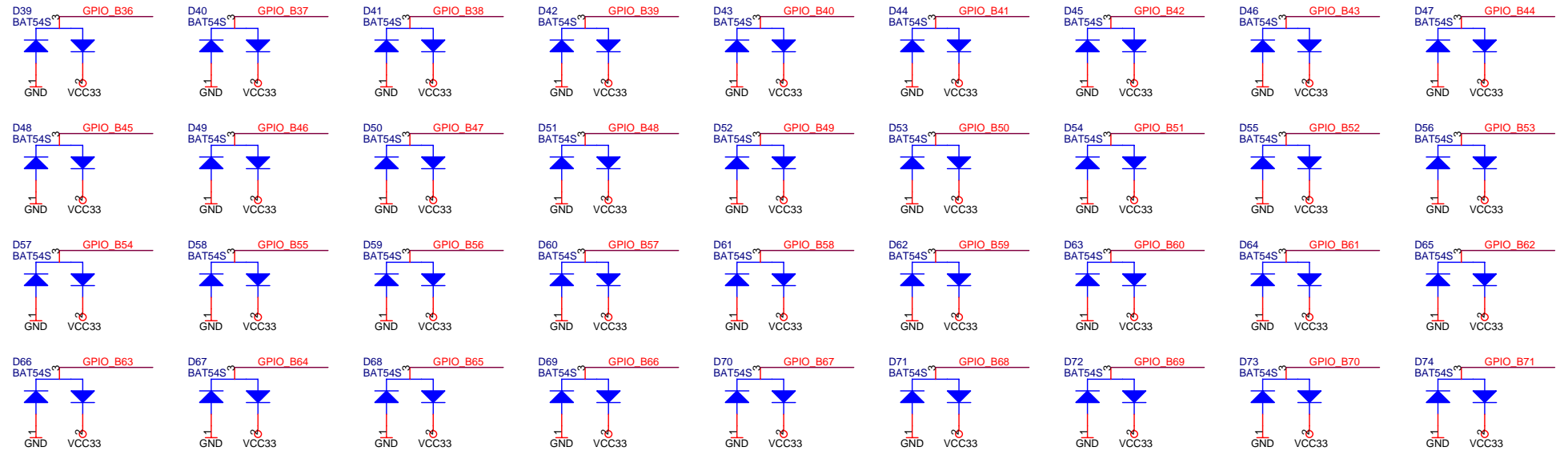


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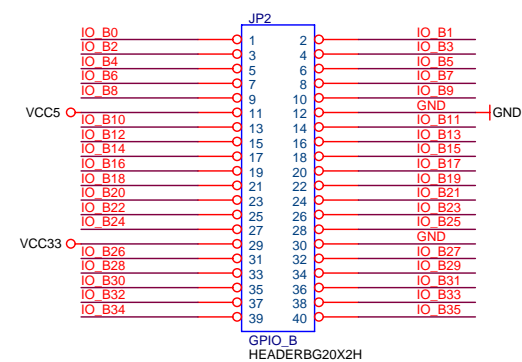
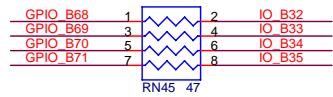
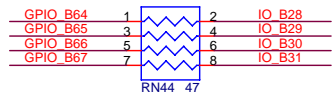
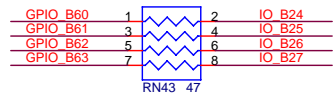
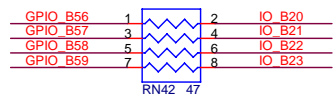
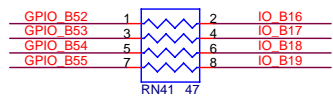
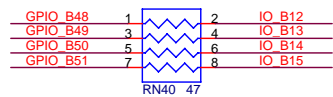
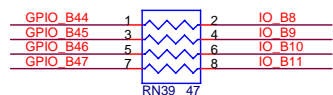
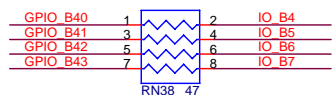
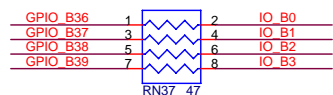


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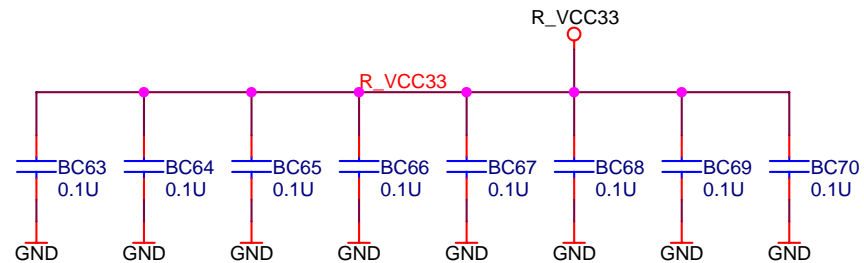
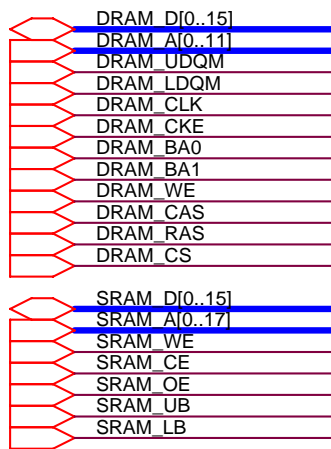
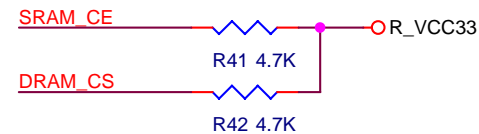
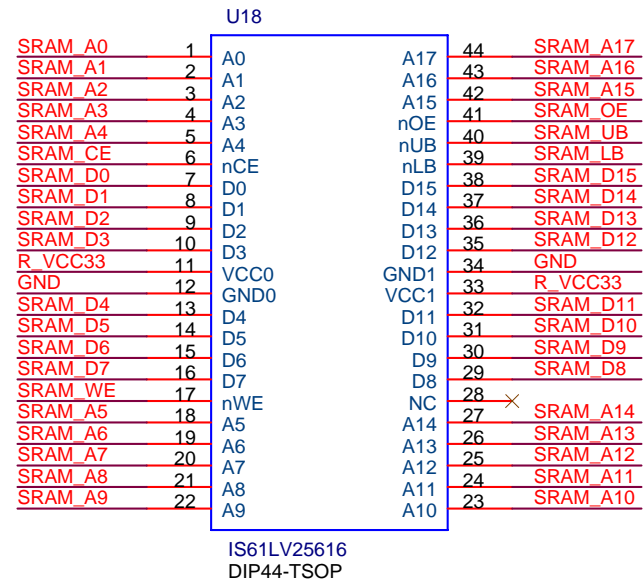
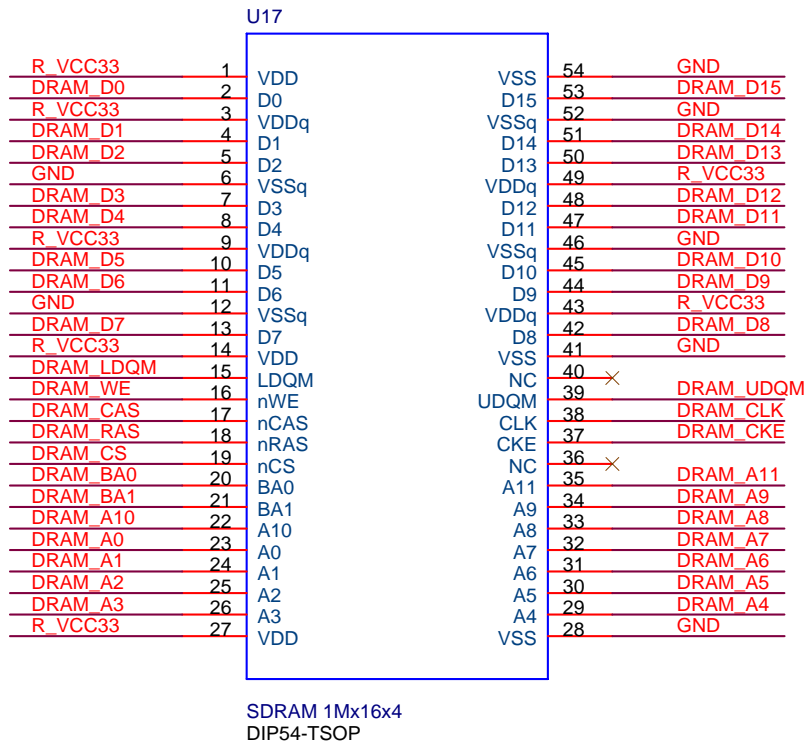




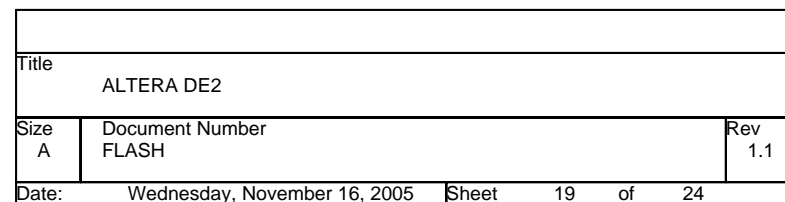
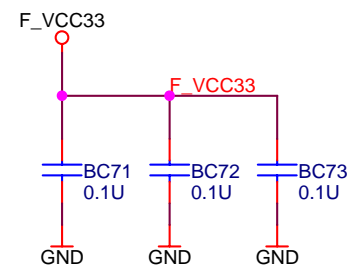
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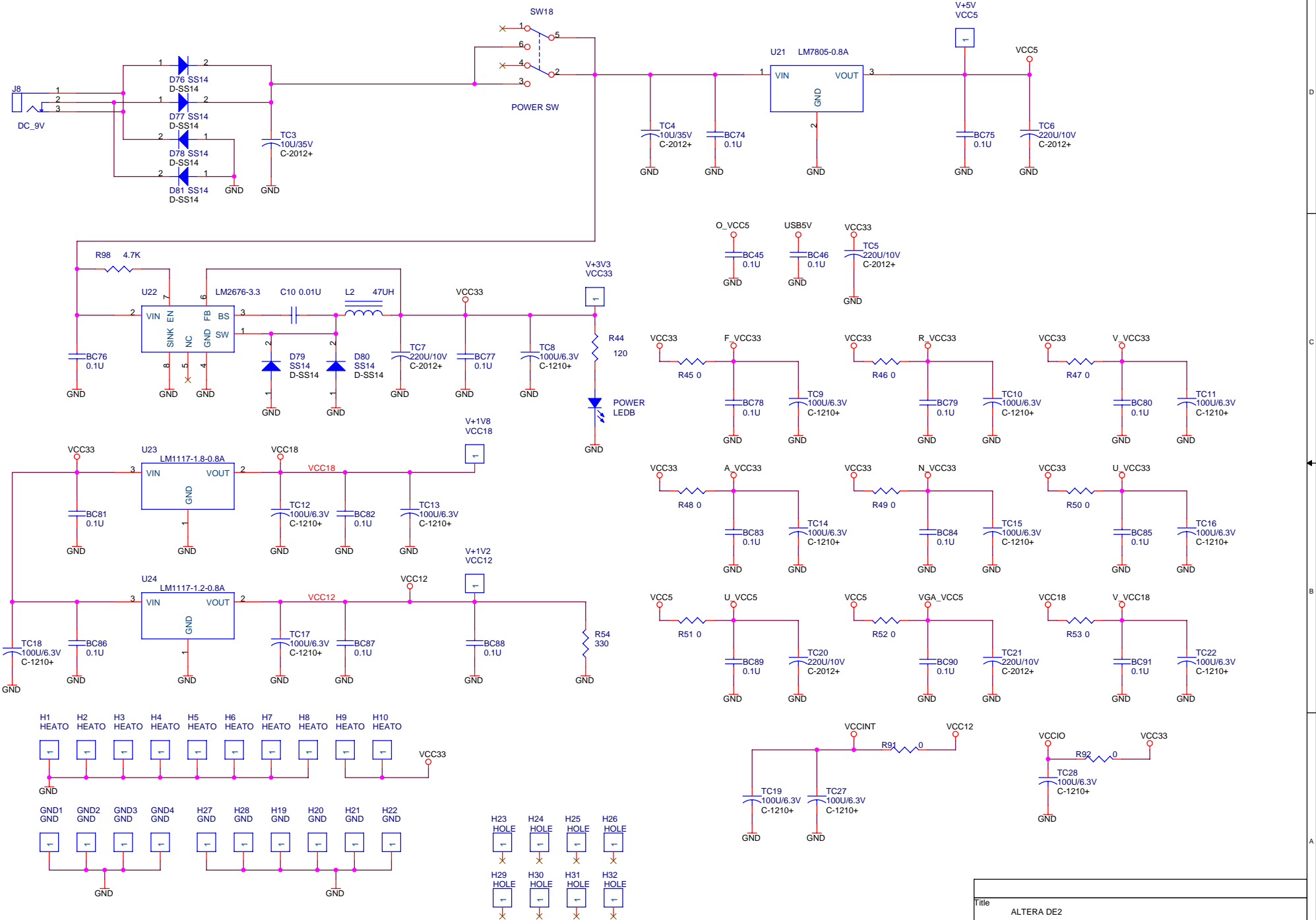


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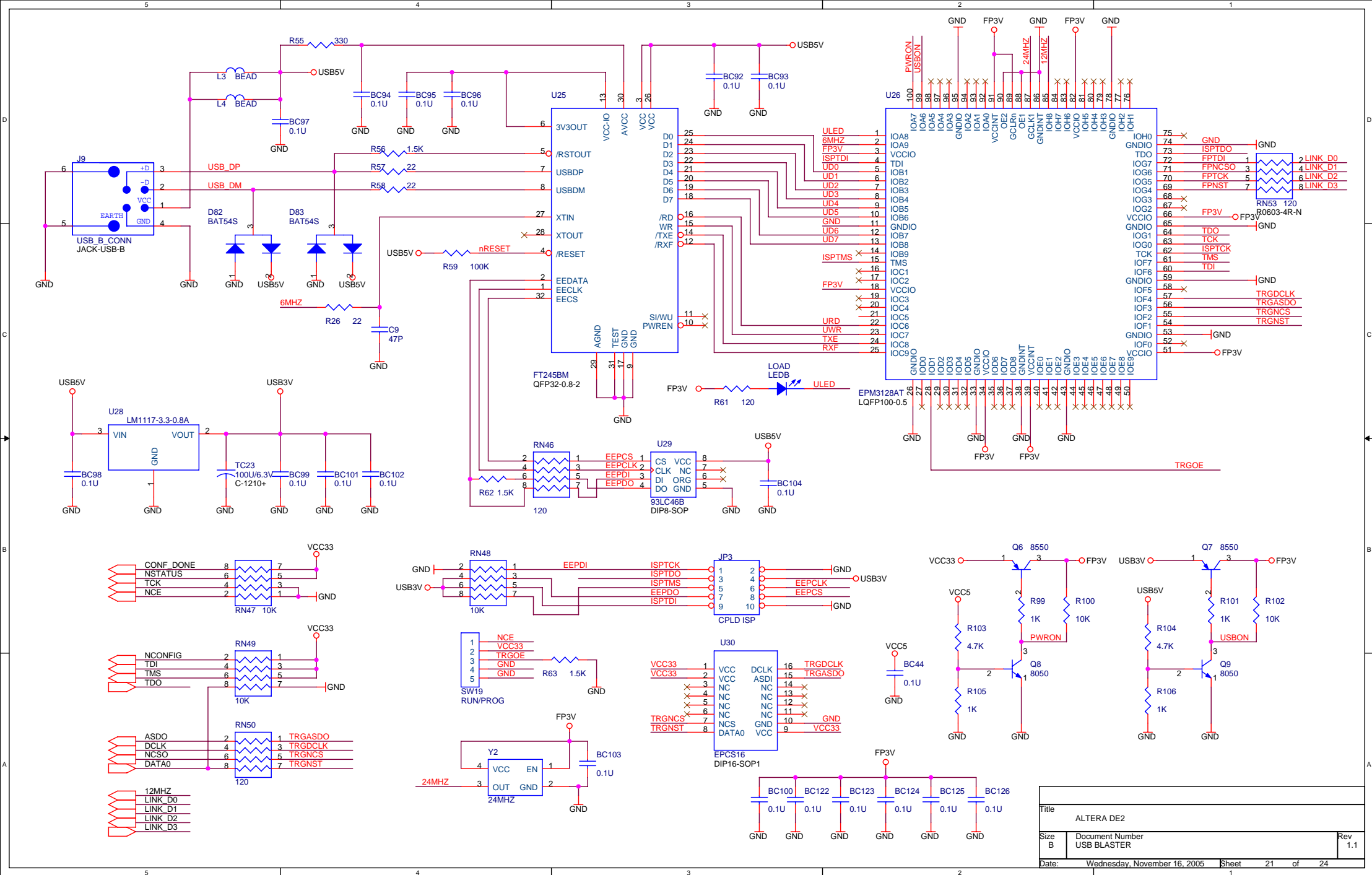


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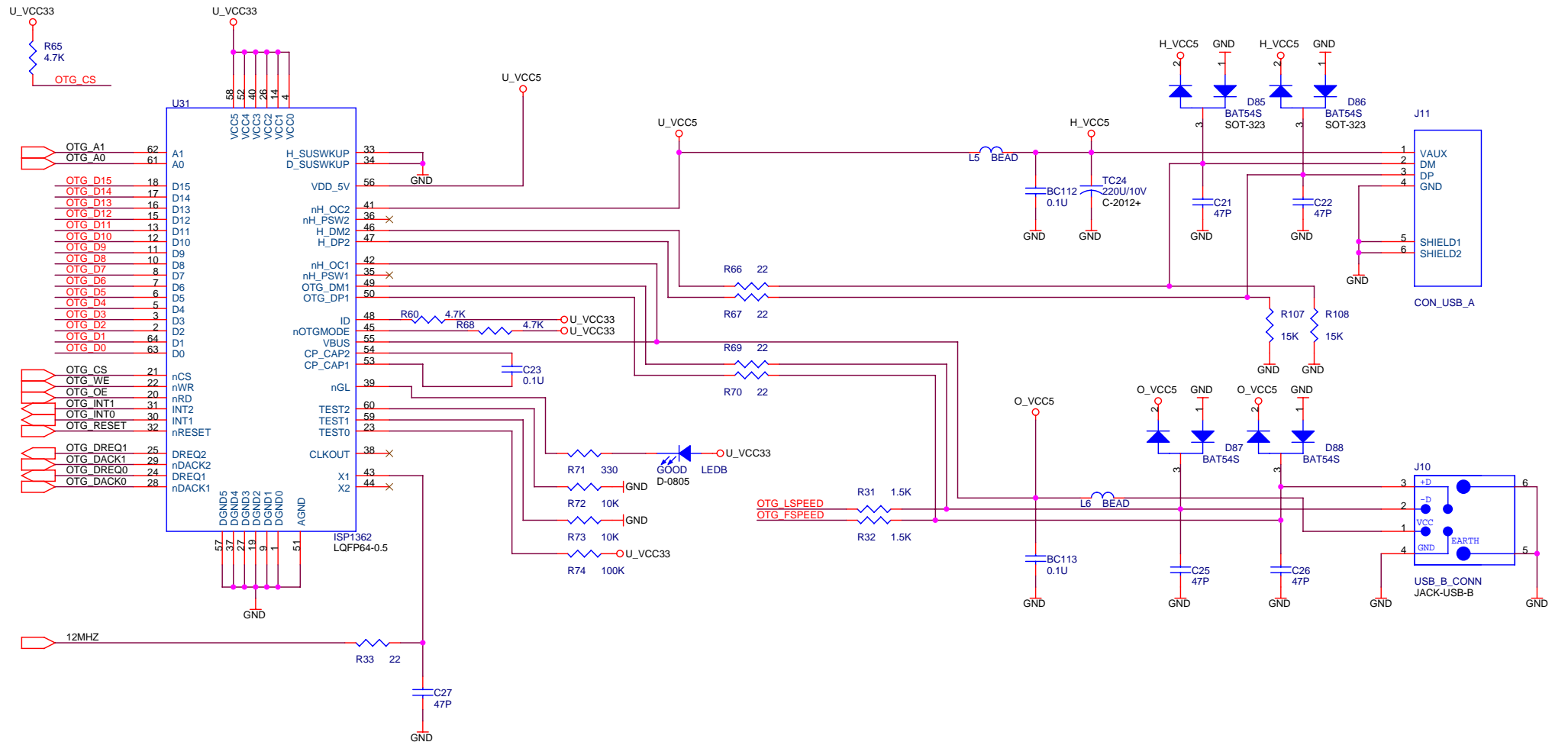




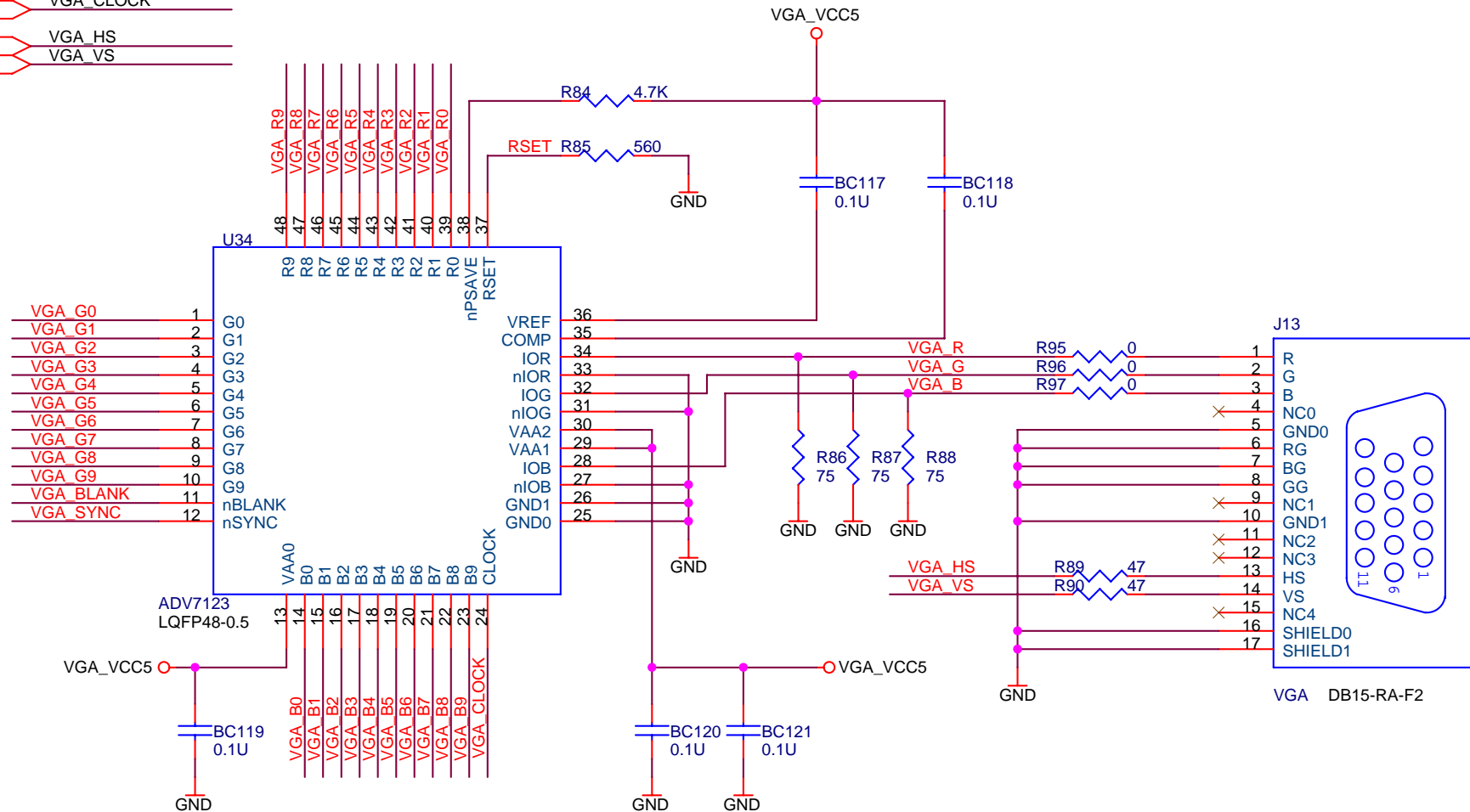
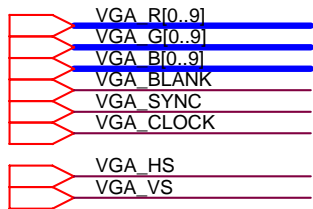
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OTG LSPEED
OTG FSPEED
OTG D[0..15]



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