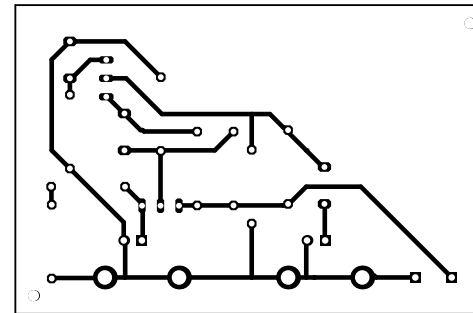
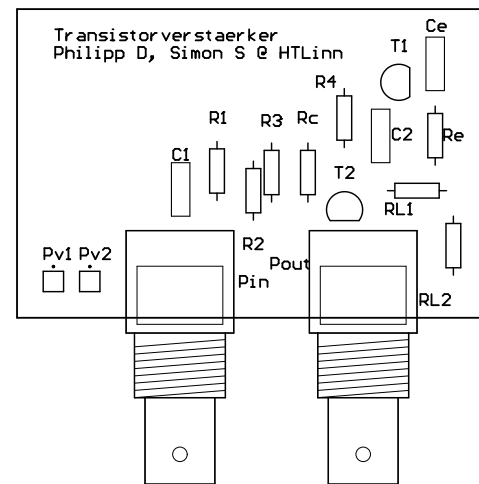


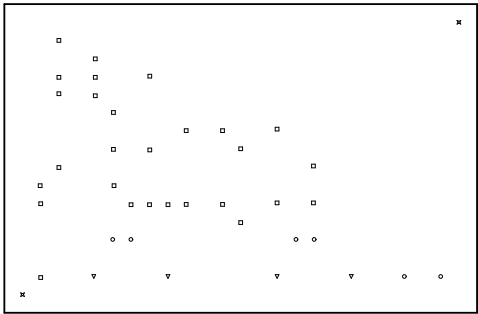
Sachnummer / Dateiname: Transistorverstaerker.SchDoc		Name: Philipp Dona, Simon Salchner		Gruppe: 4	Klasse: 3AHEL
HTBLVA Elektronik & Technische Informatik Anichstraße 26 - 28 A - 6020 Innsbruck		Transistorverstärker Zweistufig		Dokumentenart: Schaltplan	
				Dokumentenstatus: Entwurf	
				Datum: 18.06.2024	
				Ver.: 0.1	Blatt: 1 Blätter: 1





LAYOUT BOTTOM





Symbol	Count	Hole Size	Plated	Hole Type
✕	2	60,00mil (1,524mm)	PTH	Round
▽	4	78,74mil (2,000mm)	PTH	Round
○	6	35,43mil (0,900mm)	PTH	Round
□	28	31,50mil (0,800mm)	PTH	Round
40 Total				