Tidy: Symbolic Verification of Timed Cryptographic Protocols

Gilles Barthe
gjbarthe@gmail.com
MPI-SP & IMDEA Software Institute
Bochum, Germany

Ugo Dal Lago
ugo.dallago@unibo.it
University of Bologna
Bologna, Italy

Giulio Malavolta
giulio.malavolta@mpi-sp.org
MPI-SP
Bochum, Germany

Itsaka Rakotonirina
itsaka.rakotonirina@mpi-sp.org
MPI-SP
Bochum, Germany

ABSTRACT

Timed cryptography refers to cryptographic primitives designed to meet their security goals only for a short (polynomial) amount of time. Popular examples include timed commitments and verifiable delay functions. Such primitives are commonly used to guarantee fairness in multiparty protocols ("either none or all parties obtain the output of the protocol") without relying on any trusted party. Despite their recent surge in popularity, timed cryptographic protocols remain out of scope of current symbolic verification tools, which idealise cryptographic primitives as algebraic operations, and thus do not consider fine-grained notions of time.

In this paper, we develop, implement, and evaluate a symbolic approach for reasoning about protocols built from timed cryptographic primitives. First, we introduce a timed extension of the applied π-calculus, a common formalism to specify cryptographic protocols. Then, we develop a logic for timed hyperproperties capturing many properties of interest, such as timeliness or time-limited indistinguishability. We exemplify the usefulness of our approach by modelling a variety of cryptographic protocols, such as distributed randomness generation, sealed-bid auctions, and contract signing.

We also study the decidability of timed security properties. On the theoretical side, we reduce the decision of hyperproperties expressed in our logic to a form of constraint solving generalising standard notions in protocol analysis, and showcase the higher complexity of the problem compared to similar well-established logics through complexity lower bounds. On the automation side, we mechanise proofs of timed safety properties by relying on the Tamarin tool as a backend, a popular symbolic protocol analyser, and validate several examples with our approach.

1 INTRODUCTION

Multi-party protocols are distributed programs executed by heterogeneous entities, with the underlying goal of computing an output, e.g., a signed contract, the result of an auction, or simply a random value. Although the output is intended to be shared by all participants, some parties may have an incentive to prevent other parties from obtaining it. Or, they may decide to abort the computation if the potential output is not of their convenience. To exemplify the issue, consider the prototypical scenario where two mutually distrustful parties (A, B) want to agree on a common random string. A natural protocol to achieve this goal would be to let A sample a random string x locally and then send a *commitment* on x to B. After B responds with a uniformly sampled bit y, A can then reveal the committed message and the output of the protocol is set to $z = x \oplus y$. The rationale of this protocol is that y cannot depend on x (the commitment hides x to B), while at the same time x cannot be changed "after the fact" (the commitment binds A to x).

Unfortunately, this simplistic analysis neglects to account for the fact that A learns the output of the protocol before B. Therefore, A may decide to stall the protocol, by not opening the commitment, if the output is not favourable. Using this strategy, A can bias the distribution of z arbitrarily. The root of the problem is that this protocol does not satisfy *fairness*, a cryptographic notion that guarantees that either all parties obtain the output of the protocol, or none of them does. This property is notoriously hard to enforce, and it is in fact known to be unachievable in classic models of asynchronous communications [28, 33]. One way to circumvent this impossibility result is to assume a *trusted third party* (TTP), responsible to deliver the output to all participants. However, the assumption of a TTP is unrealistic in many scenarios, raising the question of whether multi-party protocols can realise fairness without trusted parties.

Fairness via Timed Cryptography. Modern cryptography bypasses this barrier by relying on *timed cryptographic primitives*, i.e., primitives that are assumed not to be computable in less than a chosen amount of time [56]. As an example, *timed* commitments [17] allow to hide a message for a pre-determined amount of time d. Going back to our above problem of string sampling, timed commitments are the right tool to construct a fair string sampling protocol: A can no longer withhold the opening of its commitment, since it will anyway be public in time d. On the other hand, the timed commitment still ensures that B does not learn the committed value during the protocol, as long as the protocol terminates in time less than d. Importantly, both parties need to place a strict timeout on the duration of the protocol to ensure that this requirement is met.

Other examples of timed cryptographic primitives include verifiable delay functions [16], verifiable timed signatures [59], and (homomorphic) time-lock puzzles [52, 56]. These primitives have been developed to enforce fairness at a cryptographic level in a variety of settings, and count numerous applications: sealed-bid auctions [17], fair contract signing [17], randomness beacons [16], computational time-stamping [16], or even electronic voting [52].

Symbolic Analysis of Cryptographic Protocols. Cryptographic protocols are complex and error-prone, but there exist symbolic models in which these protocols can be specified and verified. These models, such as the Dolev-Yao model [31] and the subsequent applied π-calculus [1], view cryptographic primitives as (idealised)

algebraic constructions, and focus on the interactions of the protocol's participants. Thanks to this abstraction, security properties can be expressed as logical formulae and can be verified, expectedly automatically, by means of decision procedures or proof systems. The symbolic analysis of cryptographic protocols is a flourishing area of research, supported by performing tools that have been used to analyse or guide the design of widely deployed protocols [9]. However, current symbolic models disregard low-level considerations such as the execution time and, as such, are not suitable to reason about protocols built on top of timed cryptography.

Contributions

In this paper, we propose the first symbolic model of cryptographic protocols built over timed cryptography.

(1) We propose a model *à la Dolev-Yao* of timed cryptography, and of timed distributed processes executed in an unbounded adversarial environment. For that we extend the applied π-calculus [1], a classical framework for specifying cryptographic protocols, with time constraints.
(2) We introduce a logic for expressing security properties of such processes, inspired by popular logics for hyperproperties such as HyperCTL* [26, 40]. We call ours Hyper*tidy* CTL* as it is designed to express hyperproperties in *timed Dolev-Yao* models. A major difference with existing hyperlogics—designed for finite-state systems—is our ability to quantify over unbounded (adversarial) computations. This makes our framework substantially more expressive in the context of protocols, but also more complex from a decidability standpoint.
(3) We illustrate this gap through complexity lower bounds for the verification problem. Typically, even the non-relational fragment of our logic, *tidy* CTL*, is undecidable while the analogue problem in CTL* (i.e., model checking) is **PSPACE** complete. On the positive side, we show that verifying that a bounded number of sessions of a protocol satisfy a Hyper*tidy* CTL* formula can be reduced to a form of *constraint solving*. It is inspired by popular analogues in protocol analysis used to verify reachability and indistinguishability properties in the untimed case [22, 23].
(4) Finally, we verify timed safety properties (*tidy* LTL) by relying on Tamarin, a popular automated prover for (untimed) security protocols. We develop an approach to handle time constraints while relying on the tool as a backend soundly, and successfully analyse several properties of interest of various protocols.

2 MOTIVATING EXAMPLE

Timed commitments [17] are a classic tool to build fair protocols. In this section, we give a first insight of our model by outlining the construction and verification of the fair sampling protocol briefly described in the introduction [17].

Recall that commitments are cryptographic primitives allowing a party to commit to a value while keeping it hidden from other parties, until a moment of their choice. Timed commitments enhance this scheme with an additional algorithm for revealing the committed value within some time d, even if the committer refuses to open its commitment. We model them by *function symbols*, written:

$$\mathsf{commit}[x,y,d] \qquad \mathsf{open}[x,y,z] \qquad \mathsf{force}[x] \qquad \mathsf{valid}[x,d]$$

Variables named as $x, y, z, \ldots$ stand for arguments serving as cryptographic material, while d, t stand for durations and time parameters. The expression $\mathsf{commit}(m,r,d)$ for example represents a commitment of the message m that can be opened with the opening data r, or forced in time d. The opening of a commitment c with m, r is represented by $\mathsf{open}(c,m,r)$, whereas $\mathsf{force}(c)$ is a forced recovery of m from c. Finally, $\mathsf{valid}(m,d)$ tests that the bitstring m is a well-formed commitment of time parameter d. We express the operational behaviour of these expressions by *timed rewriting rules*:

$$\begin{aligned} \mathsf{open}(\mathsf{commit}(x,y,d),x,y) &\to \mathsf{ok} \\ \mathsf{valid}(\mathsf{commit}(x,y,d),d) &\to \mathsf{ok} \\ \mathsf{force}(\mathsf{commit}(x,y,d)) &\xrightarrow{+d} x \end{aligned}$$

The first rule models the correctness of commitments: opening $\mathsf{commit}(x,y,d)$ with the correct data instantaneously yields validation. The second rule simply states that one can test that a message is valid commitment parametrised with d (without opening it). Finally, the last rule models a forced opening: the message x can always be recovered in time d. This is indicated by the $+d$ label, whereas other rules are implicitly labelled $+0$. The absence of other rules models that no information can be extracted about the committed message without knowing the opening data, or spending d units of time forcing it. This scheme can be used in a string-sampling protocol, as the exclusive or (xor, $\oplus$) of two bitstrings x and y held by parties A and B, respectively. Its goal is to ensure that no party can bias the outcome, i.e., $x \oplus y$ is uniformly distributed if at least A or B follows the protocol and samples its bitstring uniformly:

$$\begin{aligned} A &\to B : \ \mathsf{commit}(x,r,d) \\ B &\to A : \ y \\ A &\to B : \ x,r \end{aligned}$$

After this exchange, A and B can compute $x \oplus y$. However, to ensure that the result is unbiased, several additional precautions have to be taken. We formalise in our model the views of each party below.

View of A. First, A generates a fresh nonce r and outputs the commitment $\mathsf{commit}(x,r,d)$ at time t. Then, it waits for a response y, but only accepts it if received at some time $t' < t+d$. This way, B is too short on time to force the commitment, and can therefore not compute y depending on the retrieved value of x. If A received y in time, it accepts $x \oplus y$ as the protocol's result and reveals x and r. We express this process in a timed extension of applied π-calculus:

$$\begin{aligned} A(x,d) = \ & \mathsf{new}\ r;\ \mathsf{out}(\mathsf{commit}(x,r,d))@t; \\ & \mathsf{event}\ \mathsf{Standby}@t_s\ \mathsf{when}\ t_s < t+d; \\ & \mathsf{in}(y)@t'\ \mathsf{when}\ t' < t+d; \\ & \mathsf{event}\ \mathsf{Accept}(x \oplus y); \mathsf{out}(x); \mathsf{out}(r); 0. \end{aligned}$$

Inputs and outputs are materialised by $\mathsf{in}(\cdot)$ and $\mathsf{out}(\cdot)$ instructions, while 0 simply indicates a terminated process. Timestamps and time conditions are expressed using the @ and when operators. Finally, the events (Standby and Accept) are not actual instructions of the protocol: they should rather be seen as meta-events identifying specific phases of the protocol, for formalising security properties. Here for example, Standby indicates that A is still waiting for B's reply at time t_s. The expected security property is then, informally:

> *Fairness towards A: the bitstring x emitted by A remains secret until B responds (if it does).*

Note that *timed* commitment are only beneficial to B, i.e., the property would hold for a protocol relying on a regular commitment. We formalise the property by requiring that no computation X of the adversary (here B) may result in x while A still awaits an answer. We express this in our model by the following *tidy CTL* formula*:

$$\forall\pi.\forall z.\mathsf{F}\ \mathsf{Accept}(z)_\pi \Rightarrow \neg \mathsf{K}(x)_\pi\ \mathsf{U}\ \mathsf{Standby}_\pi$$

The syntactic sugar $\mathsf{K}(x)_\pi \triangleq \exists X, X \vdash_\pi x$ is a formula intuitively expressing that x is computable with access to the outputs sent by A during an execution π. The operators F and U are respectively read as "finally" and "until" and the property itself thus intuitively means "*for all executions π of $A(x,d)$ that eventually accepts a result z, the attacker B cannot compute x before the event* Standby". Note in particular how the quantification $\forall\pi$ requires to consider executions where the Standby event occurs arbitrarily close to the reception of y. We also later formalise stronger versions of this property exploiting the full expressivity of our logic for hyperproperties.

View of B. Dually, B awaits for a value x_c, and upon reception checks that it is a valid commitment. It then awaits for x and r, and attempts to open x_c with these values. If everything proceeds as expected, B accepts the value $x \oplus y$; otherwise, it forces the commitment through process $F = \mathsf{event}\ \mathsf{Accept}(\mathsf{force}(x_c) \oplus y); 0$.

$$B(y,d) = \mathsf{in}(x_c);\ \mathsf{if}\ \mathsf{valid}(x_c,d) = \mathsf{ok}\ \mathsf{then}\ \mathsf{out}(y);\ \mathsf{event}\ \mathsf{Standby};\ (F + \mathsf{in}(x); (F + \mathsf{in}(r); C))$$

$$C = \mathsf{if}\ \mathsf{open}(x_c,x,r) = \mathsf{ok}\ \mathsf{then}\ \mathsf{event}\ \mathsf{Accept}(x \oplus y); 0\ \mathsf{else}\ F$$

A sum $P + Q$ indicates an (external) non deterministic choice, i.e., B may execute either of two process options P or Q. In particular, B always has the choice to force the commitment instead of waiting for further opening data. The desired security property is then:

> *Fairness towards B: after B sent its bitstring y, the protocol can always proceed until the end, even if A aborts.*

A classical approach to formalise such *liveness* properties is to assume that B always executes instructions until being stuck to wait for an input from A. In practice, the *tidy* CTL* formula

$$\mathsf{stuck} = \forall\pi.\ \tau_\pi\ \mathsf{U}^?\ \exists X.\ \mathsf{in}(X)_\pi$$

captures this idea that B cannot progress. Indeed, τ_π can be understood here as "*the execution π is pending*", while $\varphi\ \mathsf{U}^?\ \psi$ ("weak until") is a relaxed variant of $\varphi\ \mathsf{U}\ \psi$ allowing φ to hold forever, should ψ never be true. This lets the stuck formula be read as "*the first action (if any) of any potential executions π starting from the current state is an input from A*". Therefore, the following formula:

$$\mathsf{Progress} = \mathsf{G}\ (\neg\mathsf{stuck} \Rightarrow \mathsf{F}\ \mathsf{stuck})$$

expresses that B always keeps going until being stuck, as $\mathsf{G}\ \varphi$ ("globally") means that φ is true all along the protocol execution. Altogether, fairness towards B is captured by the following formula:

$$\forall\pi.\ \mathsf{Progress} \Rightarrow \mathsf{G}\ (\mathsf{Standby}_\pi \Rightarrow \mathsf{F}\ \exists z.\ \mathsf{Accept}(z)_\pi)$$

The second part of the formula is read as "*at any point, if the event* Standby *occurs in the execution π, then* Accept(z) *will occur later*".

Rest of the paper. In the following sections, we formalise the timed extension of applied π-calculus used above. We also introduce a rich language for expressing security properties, Hyper*tidy* CTL*, generalising the simple (non-relational) formulas seen so far, and illustrate their use by modelling several protocols of interest in our framework. We then study the decidability of verifying that a protocol with a fixed number of sessions satisfies a Hyper*tidy* CTL* formula. We show that the problem is undecidable in general despite boundedness, but can be reduced to forms of constraint solving. Finally, we formalise a Tamarin-based approach to prove fairness towards A, as well as properties of other relevant protocols.

3 APPLIED π-CALCULUS WITH TIME

We first formalise our extension of the applied π-calculus including a support for timed cryptography.

3.1 Cryptographic Primitives and Messages

Term Algebra. We first review the algebraic setting we use to model cryptographic primitives and protocol messages. All the material is standard, except for the notions of time and cost, which are new. We start with *atomic values*:

$$\mathsf{A} = \mathcal{F}_0 \uplus \mathcal{N} \uplus \mathcal{X} \uplus \mathbb{R}^+ \qquad \text{and with } \mathcal{TX} \subseteq \mathcal{X}$$

The infinite set $\mathcal{F}_0$ of *constants* is used to model public values such as identities, or any value known to the adversary. On the contrary, the infinite set $\mathcal{N}$ of *names* models private values such as nonces or long-term keys. The infinite set $\mathcal{X}$ contains the *variables*, including a distinguished infinite subset $\mathcal{TX}$ of *temporal variables* used to specifically bind time values, represented by elements of $\mathbb{R}^+$.

In particular, the set of atomic values is implicitly typed: some values represent time, others cryptographic material. For the sake of readability, we gloss over this distinction and implicitly assume that all incoming notions (terms, substitutions, etc.) are well-typed. Next, we introduce a notion of *signature*, that is a finite set

$$\mathcal{F} = \{\mathsf{f}[x_1,\dots,x_n],\ \mathsf{g}[x_1,\dots,x_m],\ \mathsf{h}[x_1,\dots,x_p],\dots\}$$

used to represent operations such as cryptographic primitives. A symbol $\mathsf{f}[x_1,\dots,x_n]$ models a primitive taking n arguments materialised by the variables x_i, the temporal variables among them indicating time parameters. A *cost* for f is then an arithmetic expression $cost(\mathsf{f})$ over temporal variables among $x_1,\dots,x_n$. The cost of a function can be seen as a static amount of time necessary to compute it, regardless of its result. From all this, we can then define a standard notion of *term* to model computations.

Definition 3.1 (Term). A *term* is an atomic value $a \in \mathsf{A}$, or a function symbol f or an arithmetic operator $(+, \times, \dots)$ applied to other terms while respecting arities and types. We write $\mathcal{T}(S)$ the set of terms built from the atomic values and functions of $S \subseteq \mathsf{A} \cup \mathcal{F}$.

Finally, the notion of *substitution* σ is defined as usual, i.e.,

$$\sigma = \{x_1 \mapsto u_1, \dots, x_n \mapsto u_n\}$$

is a mapping where $x_1,\dots,x_n$ are pairwise distinct variables forming the *domain* of σ written $dom(\sigma)$, and $u_1,\dots,u_n$ are terms. The application of a substitution σ to a term t is denoted by $t\sigma$ which is, informally, the term obtained by simultaneously replacing all occurrences of x_i in t by $u_i = x_i\sigma$. Regarding notations, we write $\sigma \cup \sigma'$ the substitution of domain $dom(\sigma) \cup dom(\sigma')$ that extends both σ and σ' (provided they coincide on $dom(\sigma) \cap dom(\sigma')$); and $\sigma\sigma'$ refers to the composition $\sigma' \circ \sigma$, i.e., for all terms t, $t(\sigma\sigma') = (t\sigma)\sigma'$.

Rewriting. Terms have an operational behaviour (including the execution time) defined by a rewriting system that we augment with a cost function.

Definition 3.2 (Timed rewriting). A *(timed) rewriting system* $\mathcal{R}$ is a finite set of triples (ℓ, d, r), with ℓ, r terms that do not contain names, and d an arithmetic expression over temporal variables of ℓ. We call such triples (timed) *rewrite rules*, written

$$\ell \xrightarrow{+d} r\,.$$

We call d the *cost* of the rule (omitted when null). It intuitively indicates the execution time of the corresponding computation. In particular, a rewriting system $\mathcal{R}$ induces a rewriting relation $\xrightarrow{+d}_{\mathcal{R}}$ that is the closure of $\mathcal{R}$ under substitution and context. That is:

- $\ell\sigma \xrightarrow{+d\sigma} r\sigma$ for any $(\ell \xrightarrow{+d} r) \in \mathcal{R}$ and substitution σ;
- $C[u] \xrightarrow{+d} C[v]$ if $u \xrightarrow{+d} v$ and $C[\cdot]$ is a linear context, i.e., an application of function symbols on top of a term.

In practice, we only consider rewriting systems defining convergent computations for simplicity and coherence of our definitions:

Definition 3.3 (Normal form and Convergence). A term t is said to be in *normal form* if it cannot be reduced by $\rightarrow_{\mathcal{R}}$. A rewriting system is *convergent* if for all terms t, there exists a unique term in normal form, written $t\!\downarrow$, such that $t \xrightarrow{+d_1}_{\mathcal{R}} \cdots \xrightarrow{+d_n}_{\mathcal{R}} t\!\downarrow$.

Symbolic models also sometimes consider (unoriented) *equations* to specify algebraic properties such as associativity and commutativity, typically useful to model $\oplus$. As they would be untimed in our context and are irrelevant to all security properties we consider, we omit them from the model for the sake of succinctness.

3.2 Protocols

Syntax. We model distributed protocols through a notion of *(timed) process*, mostly borrowed from the applied π-calculus, except that instructions may have timestamps and time conditions. These conditions are typically built from classical arithmetic or logical operators such as $+$, $\times$, $\wedge$, or $<$, with their usual interpretation. Recalling the motivating example, an example of a time condition is $t' < t + d$ for some temporal variables $t, t', d \in T\mathcal{X}$. We also let a dedicated set of function symbols and constants $\mathcal{F}_e$, called *events*.

Definition 3.4 (Process). The grammar of *processes* is:

$P ::= 0$	*null process*
$\quad \mathsf{new}\ k; P$	*new name*
$\quad \mathsf{in}(u, x)@t\ \mathsf{when}\ b; P$	*timed input*
$\quad \mathsf{out}(u, v)@t\ \mathsf{when}\ b; P$	*timed output*
$\quad \mathsf{event}\ \mathsf{Ev}(\vec{u})@t\ \mathsf{when}\ b; P$	*timed event*
$\quad P \mid P$	*parallel composition*
$\quad !\,P$	*replication*
$\quad P + P$	*external choice*

where $k \in \mathcal{N}$, u, v are terms, $\vec{u}$ is a sequence terms (respecting the number of arguments of the event symbol $\mathsf{Ev}[\vec{x}] \in \mathcal{F}_e$), $x \in \mathcal{X}$, $t \in T\mathcal{X}$, and b is a time condition (that may make reference to t). Naturally, for succinctness, the timestamp t and the time conditions b may be omitted if unused, thus subsuming the syntax of the usual applied π calculus for untimed processes.

Intuitively, new k binds a name k, typically modelling a fresh session nonce. An instruction $\alpha@t$ when $b; P$ executes α, provided b is satisfied, and records the current time inside t. More specifically, the instructions $\mathsf{in}(u, x); P$ and $\mathsf{out}(u, v); P$ model, respectively, inputs and outputs on a communication channel u. As the adversary is assumed to control the communication network, in case u is public (e.g., $u \in \mathcal{F}_0$), an output on u adds v to the adversary's knowledge while an input x is crafted by the adversary, possibly computing a new message from previous outputs. In case u is private (e.g., $u \in \mathcal{N}$), the communication is done synchronously (*internal communication*), without adversarial interferences. Most often, we will however use the simpler notations:

$$\mathsf{in}(x); P \qquad\qquad \mathsf{out}(v); P$$

referring to an implicit arbitrary public channel. Finally, event $\mathsf{Ev}(\vec{u})$ is a meta instruction used to formalise security properties. Regarding structural operators, $P \mid Q$ models two processes executed concurrently. Its unbounded analogue $!\,P$ represents infinitely many parallel copies of P. The *external* non-deterministic choice $P + Q$ is a classical extension of the calculus that executes either P or Q. The branching is only effective to actually execute an instruction from P or Q, thus preventing to branch to a stuck process; this makes it more suited for modelling liveness than some of its variants [7].

Syntax Extensions. One may note that our grammar does not provide a syntax for "if $u = v$ then P else Q" to perform tests as in the motivating example. It can however be encoded as the process

$$(\mathsf{event}\ \mathsf{Pos}(u, v); P) + (\mathsf{event}\ \mathsf{Neg}(u, v); Q)$$

and by enforcing within security properties (see Section 4) that $\mathsf{Pos}(u, v)$ always implies that u and v have the same normal form, and $\mathsf{Neg}(u, v)$ implies that they do not. Such encodings of tests are standard in practical analysers, see, e.g., [44], and we will therefore use the "if ... then ... else" syntactic sugar for convenience. Another convenient syntax extension is the let binding $\mathsf{let}\ x = u\ \mathsf{in}\ P$ that evaluates the term u, and binds its normal form to x in P. This may be crucial, for example, to precompute the result of a timed primitive that will be used several times in P. It is encodable using internal communications, i.e., given a fresh name $e \in \mathcal{N}$:

$$\mathsf{out}(e, u); 0 \mid \mathsf{in}(e, x); P$$

3.3 Adversaries

We consider an adversarial semantics where processes are executed in parallel with an adversary that impersonates corrupted parties and builds knowledge from observing the values output by processes. We define in this section the related notions (adapted to the timed setting). First of all, we introduce a form of store recording the computations performed during the execution—including the adversarial knowledge. Formally, a *dated frame* is a substitution:

$$\Phi = \{x_1@t_1 \mapsto u_1, \ldots, x_n@t_n \mapsto u_n\}$$

where an entry $x_i@t_i \mapsto u_i$ models a term u_i computed at time $t_i \in \mathbb{R}^+$ and stored under the handle $x_i \in \mathcal{X}$. This induces a notion of *dated computability*, where ξ, u are terms and Φ is a dated frame, defined by the inference rules of Figure 1. Intuitively, $\xi \vdash_{\Phi@t} u$ indicates that, by time t, the evaluation of $\xi\Phi$ has resulted in u.

$$\frac{\xi \vdash_{\Phi@t'} u \qquad t' \leqslant t}{\xi \vdash_{\Phi@t} u} \qquad \frac{a \in \mathsf{A}}{a \vdash_{\Phi@0} a} \qquad \frac{x@t \in dom(\Phi)}{x \vdash_{\Phi@t} x\Phi} \qquad \frac{\xi \vdash_{\Phi@t} u \qquad u \xrightarrow{+d}_{\mathcal{R}} v}{\xi \vdash_{\Phi@t+d} v}$$

$$\frac{\mathsf{f}[x_1, \ldots, x_n] \in \mathcal{F} \qquad \forall i, \xi_i \vdash_{\Phi@t_i} u_i \qquad \sigma = \{x_i \mapsto u_i\}_{i=1}^{n}}{\mathsf{f}(\xi_1, \ldots, \xi_n) \vdash_{\Phi@cost(\mathsf{f})\sigma+(\max_i t_i)} \mathsf{f}(u_1, \ldots, u_n)}$$

Figure 1: Inference rules for dated deducibility

Example 3.1. Consider the following dated frame giving access to a commitment and its opening data $m, r \in \mathcal{N}$:

$$\Phi = \{x_1@t_1 \mapsto \mathsf{commit}(m, r, d),\ x_2@t_2 \mapsto m,\ x_3@t_3 \mapsto r\}$$

The commitment may be opened using the data revealed at time t_2, t_3, or forced starting at time t_1 and for a duration of d. Formally:

$$\mathsf{open}(x_1, x_2, x_3) \vdash_{\Phi@\max(t_1,t_2,t_3)} \mathsf{ok} \qquad \mathsf{force}(x_1) \vdash_{\Phi@t_1+d} m \quad \blacktriangleleft$$

The adversarial computations themselves (with access to a dated frame) are then described by means of *recipes*.

Definition 3.5 (Recipe). We let an infinite set $\mathcal{AX} \subseteq \mathcal{X}$ of dedicated variables called *axioms*, not used in processes but possibly in a frame's domain. A *recipe* is then a term $\xi \in \mathcal{T}(\mathcal{F} \cup \mathcal{F}_0 \cup \mathbb{R}^+ \cup \mathcal{AX})$.

The axioms serve as handles to the protocols outputs, that the adversary reads by spying on the network. Thus, recipes describe how the adversary computes a message from the observations stored in a dated frame Φ. Yet recipes may only use axioms, and not arbitrary variables, as the latter identify internal computations of the process that the adversary has not access to. Names are prohibited in recipes since they model secret values.

3.4 Operational Semantics

The actual operational semantics of the calculus operates on an extended form of process recording time and attacker's knowledge.

Definition 3.6 (Extended process). An *extended process* is a tuple $(\mathcal{P}, \Phi, t)$, with $\mathcal{P}$ multiset of processes, Φ dated frame, and $t \in \mathbb{R}^+$. We often interpret a process P as the extended process $(\{\!|P|\!\}, \emptyset, 0)$.

Intuitively, $\mathcal{P}$ is the multiset of subprocesses currently being executed in parallel. The concrete time value t indicates the time elapsed since the beginning of the execution, while Φ records the adversary's knowledge and the values computed during the protocol. The semantics is then a labelled transition relation $\xrightarrow{\alpha}$, where α is called an *action* that may be either of:

(1) an *input action* $\mathsf{in}(\xi, \zeta)$ modelling the reception of a message forged by the adversary, where ξ, ζ are recipes;
(2) an *output action* $\mathsf{out}(\xi, \mathsf{ax})$ modelling a message sent on the network, and recorded in the frame under axiom ax;
(3) a non observable action, namely either *silent action* τ, or an *event action* $\mathsf{Ev}(\vec{u})$, $\vec{u}$ sequence of terms in normal form.

We comment the relation, formalised in Figure 2. Rules (In) and (Out) tackle public communications: outputs increase the attacker's knowledge (i.e., add an axiom to $dom(\Phi)$), and inputs are computed using previous outputs (i.e., through a recipe ζ). Note that Rule (In) renames x as x' simply to avoid conflicts when storing the result in Φ. The communication in Rules (In) and (Out) is public in the sense that the channel u is computable by the adversary, using recipe ξ. On the contrary, no interactions with the adversary arise in (Comm). Rule (Event) triggers a meta action, materialising some control points to formalise security properties. Observe in particular that in the semantics of all instructions $\alpha@t$ when b, the time condition $b\{t \mapsto t_0\}$ is required to hold, which is how we handle that the time variable t may appear in b. Finally, (Par) and (Repl) spawn parallel threads, while (Choice) uses either of two processes for the next transition. Rule (Tic) then lets time elapse, structuring the temporal behaviour of the protocol. Process executions thus alternate between (Tic) rules (that make time progress) and other transitions (that require enough time to have elapsed).

Definition 3.7 (timed trace). A *timed trace* T of an extended process A_0 is an infinite sequence of transitions of Figure 2:

$$T : A_0 \xrightarrow{\alpha_1} A_1 \xrightarrow{\alpha_2} A_2 \xrightarrow{\alpha_3} \cdots$$

Finite portions of T may be more succinctly written $A_p \overset{w}{\Longrightarrow} A_q$ when the intermediary processes are unimportant, where w is the word $\alpha_{p+1} \cdots \alpha_q$ with τ removed. In addition, we require that T is *temporally structured*, i.e., writing $A_i = (\mathcal{P}_i, \Phi_i, t_i)$:

(1) *time elapses between different actions*: $\forall i \in \mathbb{N},\ t_i < t_{i+2}$;
(2) *time progresses*: the sequence $(t_i)_{i \in \mathbb{N}}$ is not bounded.

The restriction to temporally structured traces is motivated by practice. Typically, the time progressing assumption is key when expressing fairness towards B in the motivating example ("B can get always the result by forcing the commitment"), as the property makes reference to moments arbitrarily far in the future. This also permits to consistently define the following notion of suffix:

Definition 3.8 (trace suffix). Let $T = A_0 \xrightarrow{\alpha_1} A_1 \xrightarrow{\alpha_2} \cdots$ be a (temporally structured) trace, with $A_i = (\mathcal{P}_i, \Phi_i, t_i)$. The *suffix of* T *at time* $t \in \mathbb{R}^+$, $t \geqslant t_0$, is the trace written $T@t$ and defined as

$$T_{padding} \cdot (A_i \xrightarrow{\alpha_{i+1}} A_{i+1} \xrightarrow{\alpha_{i+2}} \cdots)$$

where $i = \min\{j \in \mathbb{N} \mid t \leqslant t_j\}$, and $T_{padding}$ is an empty transition if $t = t_i$, and the (Tic) transition $(\mathcal{P}_i, \Phi_i, t) \to A_i$ otherwise.

Example 3.2. We refer to the motivating example of Section 2 parametrised with $d = 1$. Consider the timed trace $T \cdot T_\infty$, where

$$T : A(x, d) \xrightarrow{\mathsf{out(ax)}\ \mathsf{Standby}\ \mathsf{in}(\xi)} (\{\!|A'|\!\}, \Phi \cup \sigma, 0.59)$$

and T_∞ is an infinite, temporally structured sequence of (Tic) transitions, $\xi \vdash_{\Phi@0.59} u$, and given fresh $y', x_r \in \mathcal{X}, r' \in \mathcal{N}$:

$$A' = \mathsf{event}\ \mathsf{Accept}(x \oplus y'); \mathsf{out}(x); \mathsf{out}(x_r); 0$$
$$\sigma = \{x_r@0.09 \mapsto r', y'@0.59 \mapsto u\}$$
$$\Phi = \{\mathsf{ax}@0.1 \mapsto \mathsf{commit}(x, r', d)\}$$

$$(\{\!\{\mathsf{in}(u,x)@t \text{ when } b; P\}\!\} \cup \mathcal{P}, \Phi, t_0) \xrightarrow{\mathsf{in}(\xi,\zeta)} (\{\!\{P\rho\}\!\} \cup \mathcal{P}, \Phi \cup \{x'@t_0 \mapsto v\downarrow\}, t_0) \quad \text{(In)}$$
with $\rho = \{t \mapsto t_0, x \mapsto x'\}$, x' fresh renaming of x, $b\rho$ holds, $\xi \vdash_{\Phi@t_0} u\downarrow$, and $\zeta \vdash_{\Phi@t_0} v\downarrow$

$$(\{\!\{\mathsf{out}(u,v)@t \text{ when } b; P\}\!\} \cup \mathcal{P}, \Phi, t_0) \xrightarrow{\mathsf{out}(\xi,\mathsf{ax})} (\{\!\{P\rho\}\!\} \cup \mathcal{P}, \Phi \cup \{\mathsf{ax}@t_0 \mapsto v\downarrow\}, t_0) \quad \text{(Out)}$$
with $\rho = \{t \mapsto t_0\}$, $b\rho$ holds, $\mathsf{ax} \in \mathcal{AX} \setminus dom(\Phi)$, $\xi \vdash_{\Phi@t_0} u\downarrow$, and $v \vdash_{\Phi@t_0} v\downarrow$

$$(\{\!\{\mathsf{in}(u,x)@t \text{ when } b; P, \mathsf{out}(w,v)@t' \text{ when } b'; Q\}\!\} \cup \mathcal{P}, \Phi, t_0) \xrightarrow{\tau} (\{\!\{P\rho, Q\rho'\}\!\} \cup \mathcal{P}, \Phi \cup \{x'@t_0 \mapsto v\downarrow\}, t_0) \quad \text{(Comm)}$$
with $\rho = \{t \mapsto t_0, x \mapsto x'\}$, x' fresh renaming of x, $\rho' = \{t' \mapsto t_0\}$, $b\rho$ and $b'\rho'$ hold, $v \vdash_{\Phi@t_0} v\downarrow$, $u \vdash_{\Phi@t_0} u\downarrow$ and $w \vdash_{\Phi@t_0} u\downarrow$

$$(\{\!\{\mathsf{event}\ \mathsf{Ev}(\vec{u})@t \text{ when } b; P\}\!\} \cup \mathcal{P}, \Phi, t_0) \xrightarrow{\mathsf{Ev}(\vec{u}\downarrow)} (\{\!\{P\{t \mapsto t_0\}\}\!\} \cup \mathcal{P}, \Phi, t_0) \quad \text{if } b\{t \mapsto t_0\} \text{ holds, and } \vec{u} \vdash_{\Phi@t_0} \vec{u}\downarrow \quad \text{(Event)}$$

$$(\{\!\{\mathsf{new}\ k; P\}\!\} \cup \mathcal{P}, \Phi, t_0) \xrightarrow{\tau} (\{\!\{P\{k \mapsto x\}\}\!\} \cup \mathcal{P}, \Phi \cup \{x@t_0 \mapsto k'\}, t_0) \quad \text{with } x \in \mathcal{X} \text{ and } k' \in \mathcal{N} \text{ fresh} \quad \text{(New)}$$

$$(\mathcal{P}, \Phi, t_0) \xrightarrow{\tau} (\mathcal{P}, \Phi, t_0 + \delta) \quad \text{if } \delta > 0 \quad \text{(Tic)}$$

$$(\{\!\{P_0 + P_1\}\!\} \cup \mathcal{P}, \Phi, t_0) \xrightarrow{\alpha} (\mathcal{P}', \Phi', t_0) \quad \text{if } \exists i \in \{0,1\}, (\{\!\{P_i\}\!\} \cup \mathcal{P}, \Phi, t_0) \xrightarrow{\alpha} (\mathcal{P}', \Phi', t_0) \text{ and } P_i \notin \mathcal{P}' \quad \text{(Choice)}$$

$$(\{\!\{P \mid Q\}\!\} \cup \mathcal{P}, \Phi, t_0) \xrightarrow{\tau} (\{\!\{P, Q\}\!\} \cup \mathcal{P}, \Phi, t_0) \quad \text{(Par)}$$

$$(\{\!\{!\,P\}\!\} \cup \mathcal{P}, \Phi, t_0) \xrightarrow{\tau} (\{\!\{!\,P, P\}\!\} \cup \mathcal{P}, \Phi, t_0) \quad \text{(Repl)}$$

Figure 2: Operational semantics of the applied pi-calculus with time

In this trace, A initiates a session with the attacker B by sampling r' at time 0.09, commits on x with r' at time 0.1, and the attacker responds with a recipe ξ at time 0.59—not long before the timeout expiring at time 0.6. However, if the timeout were d or a greater value (say, 1.1 here), the attacker could make the outcome be a chosen boolean b_0 through another trace with the actions:

- $\mathsf{out}(\mathsf{ax}_1)$ at time $t = 0.05$,
- Standby at time $t = 0.06$,
- $\mathsf{in}(\mathsf{force}(\mathsf{ax}_1) \oplus b_0)$ at time $t = 1.05$,
- $\mathsf{Accept}(b_0)$ at time $t = 2$. ◂

4 SECURITY (HYPER)PROPERTIES

4.1 A Hyperlogic

We finally present a logic for specifying hyperproperties of protocols. Its key novelty compared to similar logics is its ability to express properties about the adversary, through more developed atomic formulae and quantifiers.

Definition 4.1 (Formula). The set of *Hypertidy CTL* formulae* is given by the grammar below:

φ	::=	$\forall\pi.\varphi$	$\forall x.\varphi$	$\forall X.\varphi$	*quantifiers*
		$\varphi \mathsf{U} \varphi$			*until*
		$\varphi \wedge \varphi$	$\neg\varphi$		*logical operators*
		$X \vdash_\pi v$	$u \vdash_\pi v$	α_π	*atomic formulae*

with π called a *path variable*, $X \in \mathcal{X}^2$ is called a *second-order variable*, $x \in \mathcal{X}$, α is an action (as in the semantics), and u, v are terms.

The "until" operator $\varphi \mathsf{U} \psi$ means that φ should hold continuously from the current time t, and for a duration δ, while ψ holds at time $t + \delta$. It is not required that φ holds at time t or $t + \delta$. *Path quantifiers* $\forall\pi$ then quantify over all traces of the studied process. The quantification is performed from the current state: for example, $\forall\pi.(\varphi \mathsf{U} \exists\pi'.\psi)$ expresses that φ should hold all along any trace π, until a point where π could potentially branch to a trace π' verifying ψ. The logical operators $\wedge$ and $\neg$ have the expected semantics. The quantifiers over computations are also key features ($\forall x$ for terms, $\forall X$ for recipes). The atomic formulae of the logic are finally $\xi \vdash_\pi u$, ("the computation ξ results in u with access to the frame of π"), and α_π ("the action α occurs at the current time"). The case $\alpha = \tau$ is also used to express that no particular action occurs.

Figure 3 then formalises when a process P satisfies a Hyper*tidy* CTL* formula φ. This is done through judgements of the form $\Pi \models \varphi$ where φ is a formula and Π is a substitution from path variables to traces. We assume in particular a dedicated path variable $\varepsilon \in dom(\Pi)$ used to track the last path quantifier in scope. We also write, to restrict all traces of Π to their suffix starting at time t:

$$\Pi@t = \{\pi \mapsto \pi(\Pi)@t \mid \pi \in dom(\Pi)\}\,.$$

Definition 4.2 (satisfiability). A process P *satisfies* a formula φ, written $P \models \varphi$, when $\{\varepsilon \mapsto \varepsilon_\infty\} \models \varphi$, where ε_∞ is an infinite, temporally structured sequence of (Tic) transitions starting from P.

4.2 Useful Syntax Extensions

We now present syntax extensions encodable into our logic, that will be convenient for our case studies. We recall that, beyond classical logical operators ($\exists$, $\vee$, etc.), we already introduced in the motivating example the Progress formula, and $\mathsf{K}(x)_\pi \triangleq \exists X, X \vdash_\pi x$ expressing the non-secrecy of x at the current time in π.

Temporal Operators. The most common syntax extensions of temporal logics are the following additional temporal operators, some of them appearing in the motivating example:

$$\mathsf{F}\,\varphi = \top \mathsf{U} \varphi \qquad \mathsf{G}\,\varphi = \neg(\mathsf{F}\,\neg\varphi) \qquad \varphi \mathsf{U}^?\,\psi = (\varphi \mathsf{U} \psi) \vee \mathsf{G}\,\varphi$$

$$\varphi \mathsf{R}\,\psi = \neg(\neg\varphi \mathsf{U} \neg\psi) = \psi \mathsf{U}^?\,(\varphi \wedge \psi)$$

Intuitively, $\mathsf{F}\,\varphi$ ("finally φ") means that φ will eventually be true. Its dual operator $\mathsf{G}\,\varphi$ ("globally φ") expresses that φ is continuously true. Typically, the formula $\mathsf{F}\,\mathsf{G}\,\tau_\pi$ is satisfied by finite traces, i.e., traces as usually considered in protocol analysis that only perform a finite number of non-silent actions. The relaxed variant of "until", $\varphi \mathsf{U}^?\,\psi$ ("φ weak until ψ"), allows φ to hold forever in case ψ never holds. The $\varphi \mathsf{R}\,\psi$ ("φ release ψ") has a dual semantics: at time t, it states that ψ should hold at any time $t+\delta$ such that φ is continuously false in the window $(t, t+\delta)$. I.e., the first occurrence of φ "releases" the obligation of ψ to hold, hence its equivalence with $\psi \mathsf{U}^?\,(\varphi \wedge \psi)$.

$\Pi \models \forall \pi, \varphi$ *iff* writing A the initial process of $\Pi(\varepsilon)$, for all traces T of A, $\Pi[\pi \mapsto T, \varepsilon \mapsto T] \models \varphi$
$\Pi \models \forall x, \varphi$ *iff* for all terms u in normal form, $\Pi \models \varphi\{x \mapsto u\}$
$\Pi \models \forall X, \varphi$ *iff* for all recipes ξ, $\Pi \models \varphi\{X \mapsto \xi\}$
$\Pi \models \varphi \wedge \psi$ *iff* $\Pi \models \varphi$ and $\Pi \models \psi$
$\Pi \models \neg\varphi$ *iff* $\Pi \not\models \varphi$
$\Pi \models \varphi \mathsf{U} \psi$ *iff* writing $A = (\mathcal{P}, \Phi, t)$ the initial process of $\Pi(\varepsilon)$, $\exists \delta > 0$, $\Pi@t+\delta \models \psi$ and $\forall t' \in (t, t+\delta)$, $\Pi@t' \models \varphi$
$\Pi \models \alpha_\pi$ *iff* the first transition of $\Pi(\pi)$ is of the form $A \xrightarrow{\alpha} B$ for some processes A, B
$\Pi \models \xi \vdash_\pi u$ *iff* writing $A = (\mathcal{P}, \Phi, t)$ the initial process of $\Pi(\pi)$, we have $\xi \vdash_{\Phi@t} u$

Figure 3: Satisfiability relation for formulae

It is also possible to consider, as in real-time extensions of LTL such as (Hyper)MITL [4, 40], constrained versions of the temporal operators. The formula $\varphi \mathsf{U}_I \psi$, where I is a real interval, means that φ should hold during a time lapse $\delta \in I$, and ψ holds after that. Therefore $\varphi \mathsf{U} \psi$ is equivalent to $\varphi \mathsf{U}_{(0,+\infty)} \psi$. The analogues $\mathsf{F}_I \varphi = \top \mathsf{U}_I \varphi$, $\mathsf{G}_I \varphi = \neg\mathsf{F}_I \neg\varphi$ and $\varphi \mathsf{R}_I \psi = \neg(\neg\varphi \mathsf{U}_I \neg\psi)$ can naturally be defined. For example, at time t, the formula $\varphi \mathsf{R}_I \psi$ is notably read as "*ψ should hold at any moment $t+\delta$, $\delta \in I$, except maybe when φ held at some time $t' \in (t, t+\delta)$*". Such interval constraints can most often be encoded as time conditions in processes and are therefore not necessary in our framework from an expressivity standpoint. We however describe in Appendix A a blockchain atomic swap protocol where this operator is convenient to express properties of the form "*the protocol is fair for all participants reacting fast enough*".

Indistinguishability. To express stronger notions of secrecy than what is possible using the K formula, we can formalise *process indistinguishability*. Typically, to model that no information can be extracted about the parameter x of a process $P(x)$, we require that the executions of $P(x_0)$ and $P(x_1)$ are indistinguishable from the adversary's view for any term x_0 and x_1. Formally, two traces are indistinguishable if, first, they perform the same interactions with the attacker (inputs and outputs):

$$\varphi_A(\pi, \pi') \triangleq \forall X.\forall Y.(\mathsf{in}(X,Y)_\pi \Leftrightarrow \mathsf{in}(X,Y)_{\pi'}) \wedge (\mathsf{out}(X,Y)_\pi \Leftrightarrow \mathsf{out}(X,Y)_{\pi'})$$

and if, second, the adversary cannot compute an equality test (started as early as possible and finished by the current time) that holds in one process and not on the other. This would indeed permit to distinguish a black-box access to one process against the other by effectively computing this test. This is expressed by the formula:

$$\varphi_E(\pi, \pi') \triangleq \forall X.\forall Y.\, X \sim_\pi Y \Leftrightarrow X \sim_{\pi'} Y$$

where $X \sim_\pi Y \triangleq \exists x, X \vdash_\pi x \wedge Y \vdash_\pi x$. We would then call *static equivalence* the conjunction of these two formulae:

$$\pi \sim \pi' \triangleq \varphi_A(\pi, \pi') \wedge \varphi_E(\pi, \pi')$$

Process indistinguishability itself may then be modelled by *trace equivalence* [23]. It states that for each trace of any of two processes P_1, P_2, there is a statically-equivalent trace in the other. We use:

$$P = (\mathsf{event\ Left}; P_1) + (\mathsf{event\ Right}; P_2)$$

to encode path quantifiers over multiple processes P_1, P_2, with $\mathsf{Left}, \mathsf{Right} \in \mathcal{F}_0$. Trace equivalence of P_1 and P_2 then rephrases as:

$$P \models \forall \pi_1.\exists \pi_2.(\mathsf{F}\ \mathsf{Left}_{\pi_1} \Leftrightarrow \mathsf{F}\ \mathsf{Right}_{\pi_2}) \wedge \mathsf{G}\ (\pi_1 \sim \pi_2)$$

This can be generalised to quantify over an arbitrary number of processes, at least regarding path quantifiers not under the scope of an "until". Under this restriction, we will thus use a syntactic sugar $\forall \pi : Q.\, \varphi$ to quantify specifically over the traces of Q. Going back to the motivating example, we then model the (strong) fairness towards A using indistinguishability, by considering:

$$P_i = \mathsf{in}(x_0); \mathsf{in}(x_1); \mathsf{in}(d); A(x_i, d) \qquad i \in \{0,1\}, d \in \mathcal{TX}$$

We then require that for all accepting executions π_0 of P_0, there exists an execution π_1 of P_1 that is indistinguishable from π_0 until the event Standby occurs. This is expressed by:

$$\forall \pi_0 : P_0.\, \exists \pi_1 : P_1.\, \forall z.\mathsf{F}\ \mathsf{Accept}(z)_{\pi_0} \Rightarrow \pi_0 \sim \pi_1\ \mathsf{U}\ \mathsf{Standby}_{\pi_0}$$

4.3 Fragments of Interest

As the design of our logic has been inspired by HyperCTL* [26], we can derive several sublogics analogous to its classical fragments.

Definition 4.3. Hyper*tidy* LTL contains the formulae in *prenex form*, i.e., $Q_1\pi_1 \ldots Q_n\pi_n.\varphi$, $Q_i \in \{\forall, \exists\}$, φ without path quantifiers.

It notably encompasses indistinguishability properties, but not liveness. Another one is the non-relational fragment, *tidy* CTL*:

Definition 4.4. tidy CTL* contains formulae whose subformulae α_π and $\xi \vdash_\pi u$ always refer to the last quantified path variable π.

This fragment is for example sufficient to express all properties of our motivating example, but not indistinguishability, making it incomparable to Hyper*tidy* LTL. However, both subsume safety properties, also targeted by our automated proofs (Section 7):

Definition 4.5. tidy LTL contains the formulae of the form $\forall \pi.\varphi$ where φ does not contain path quantifications.

In addition of the examples of safety properties presented all across this paper, *tidy* LTL can also naturally express *resilience* assumptions on a channel c, as used in some analyses of fair non-repudiation protocols [7]. This intuitively means that, during a trace π, network manoeuvres are made to ensure that any message sent on c is received eventually.

5 THE MODEL AT WORK

We now formalise several protocols, featuring various flavours of reachability, liveness and indistinguishability properties, expressed uniformly in our logic. An additional blockchain atomic swap protocol, more technical, is provided in Appendix A.

5.1 Yet Another Protocol for String Sampling

Similarly to the motivating example, we model here a sampling protocol between two mutually suspicious parties [48], based this time on *Verifiable Delay Functions* (VDF). A VDF is simply a pseudo-random function whose output cannot be computed faster than a chosen time d, *but* that can be verified efficiently (i.e., without spending time d to recompute it). In our model, it would simply be a function symbol with a non-null cost.

$$\mathcal{F} = \mathsf{VDF}[x,d], \mathsf{verify}[x,y], \mathsf{h}[x,y] \qquad cost(\mathsf{VDF}) = d$$
$$\mathcal{R} = \mathsf{verify}(\mathsf{VDF}(x,d),x) \to \mathsf{ok}$$

The symbol h models a binary hash function, with no associated rewrite rules, thus expressing a random-oracle assumption. Concretely, the protocol proceeds as follows. Two parties A and B send random strings r_A, r_B to each other, in plaintext. Given a time parameter d, each party only accepts the other's string for a duration of d after sending their own, and the result of the protocol is then $\mathsf{VDF}(\mathsf{h}(r_A,r_B),d)$. This way, provided A or B is honest, the result cannot be predicted before expiration of the timeout. We formalise unpredictability through the following process, encoding a security game where the adversary plays the role of B and guesses the result.

$$\begin{aligned} A(d) = {} & \mathsf{new}\ r_A;\ \mathsf{out}(r_A)@t;\ \mathsf{in}(r_B);\\ & \mathsf{in}(guess)@t' \ \mathsf{when}\ t' < t+d;\\ & \mathsf{event}\ \mathsf{Challenge}(\mathsf{verify}(guess, \mathsf{h}(r_A,r_B)));0 \end{aligned}$$

Unpredictability is then simply modelled by the following formula, to be satisfied by the process $\mathsf{in}(d); A(d)$, with $d \in \mathcal{TX}$:

$$\forall \pi. \mathsf{G}\ \neg \mathsf{Challenge}(\mathsf{ok})_\pi$$

5.2 Randomness Beacon

To sample strings publicly, one may apply an extraction function to entropy sources like stock markets [25] or blockchains [18]. These sources are believed to produce high entropy but are also manipulable to some extent. To make such manipulations virtually useless, one may use a VDF (see Section 5.1) as an extractor [16]. If its computation takes longer than the time necessary to influence the entropy source, an active attacker might indeed bias the source, but without actual insight on the impact of their action.

Concretely, consider, given an initial seed s, a *randomness beacon* [16] that publishes a new random string at regular time intervals (say $d \in \mathbb{R}^+$). It is required that the successive published strings remain unpredictable across the successive rounds, meaning:

> *n-round unpredictability: given the transcript of the first n sampled strings, one cannot distinguish (in time less than d) the last string from a random one.*

Weaker variants may also be considered, based on computability instead of indistinguishability from random ("the adversary should not be able to guess the actual value of the n^{th} string"), similarly to how we formalise unpredictability in the string-sampling protocol of Section 5.1. The actual protocol simply proceeds iteratively by taking the last emitted string s (initially, the seed), and computes a new string r as the output of the VDF on input s with time parameter d. It then reveals r (and the corresponding proof), sets $s = r$, and repeats the process. We model unpredictability using the following two processes A_i, $i \in \{0,1\}$, given a private channel $e \in \mathcal{N}$ to carry out the state of the beacon across multiple rounds, and $d \in \mathcal{TX}$.

$$\begin{aligned} A_i = {} & \mathsf{in}(d); (Init \mid Chal_i(d) \mid !\, R(d))\\ Init = {} & \mathsf{new}\ s;\ \mathsf{out}(e,s);\ \mathsf{event}\ \mathsf{Reveal};\ \mathsf{out}(s);0\\ R(d) = {} & \mathsf{in}(e,state);\ \mathsf{let}\ r = \mathsf{VDF}(state,d)\ \mathsf{in}\\ & \mathsf{out}(e,r);\ \mathsf{event}\ \mathsf{Reveal};\mathsf{out}(r);0\\ Chal_i(d) = {} & \mathsf{in}(e,state);\\ & \mathsf{new}\ r_0;\ \mathsf{let}\ r_1 = \mathsf{VDF}(state,d)\ \mathsf{in}\\ & \mathsf{event}\ \mathsf{Challenge}@t;\mathsf{out}(r_i);\\ & \mathsf{event}\ \mathsf{Stop}@t' \ \mathsf{when}\ t' < t+d;0 \end{aligned}$$

The process *Init* initialises the seed, R carries out one round of the generation, and $Chal_i$ reveals a string r_i and challenges to the adversary to guess, before the event Stop, whether it is the last pseudo-random string r_1, or a real random string r_0. The process however allow the successive random strings r to be revealed gradually, whereas the security game requires them to be released in one go, after the challenge is issued. We enforce this property through what can be seen as *trace restriction* H, similarly to the feature of verification tools such as ProVerif or Tamarin [11, 15]. Concretely, for all $i \in \{0,1\}$, the following formula should be satisfied:

$$\forall \pi : A_i.\, H_\pi \Rightarrow \exists \pi' : A_{1-i}.\, H_{\pi'} \wedge (\pi \sim \pi'\ \mathsf{U}\ \mathsf{Stop}_\pi)$$

where $H_\pi = \mathsf{F}\ \mathsf{Stop}_\pi \wedge (\neg \mathsf{Reveal}_\pi\ \mathsf{U}\ \mathsf{Challenge}_\pi)$.

5.3 Sealed-Bid Auctions

We sketch a protocol for sealed-bid auctions [17], where multiple parties engage in a Vickrey auction without the aid of a trusted auctioneer. This protocol uses the same theory for timed commitment that we used for our motivating example. We assume that the bidding phase has a (conservatively set) maximum duration of d.

(1) All participants send a d-timed commitment on their bid.
(2) After time d, honest participants open their commitment.
(3) Then, they force the commitments of parties that did not provide an opening, and compute the winner.

We give here a general model of the protocol for an unbounded number of participants. If a process P models the behaviour of an honest participant of an auction parametrised by an ending time t_f, the overall process can be modelled as follows, given $d \in \mathcal{TX}$:

$$\begin{aligned} A[P] = {} & \mathsf{in}(d);\ \mathsf{event}\ \mathsf{Start}@t_0;\\ & (\mathsf{event}\ \mathsf{Stop}@t_1 \ \mathsf{when}\ t_1 < t_0 + d; 0) \mid !\, P(t_0+d) \end{aligned}$$

The auction lasts at most a duration d (event Stop), involves an arbitrary number of honest participants (process $!\, P(t_0+d)$) and an implicit coalition of an arbitrary number of dishonest participants (impersonated by the adversary). The process defining one participant, with bid x for an auction ending at time t_f, is:

$$\begin{aligned} Q(x,t_f) = {} & \mathsf{new}\ r;\ \mathsf{out}(\mathsf{commit}(x,r,d));\\ & (\mathsf{out}(x)@t \ \mathsf{when}\ t > t_f; \mathsf{out}(r);0) \ \mid\ !\, R(t_f)\\ R(t_f) = {} & \mathsf{in}(y)@t \ \mathsf{when}\ t < t_f;\ \mathsf{event}\ \mathsf{Standby}(y);\\ & (\mathsf{in}(z); (Open + F)) + F\\ Open = {} & \mathsf{in}(s); \mathsf{if}\ \mathsf{open}(y,z,s) = \mathsf{ok}\ \mathsf{then}\ \mathsf{event}\ \mathsf{Get}(z);0\ \mathsf{else}\ F\\ F = {} & \mathsf{event}\ \mathsf{Get}(\mathsf{force}(y));0 \end{aligned}$$

The process R receives a single bid and opens it (event Get); the process is replicated to model an arbitrary number of receptions. The desired security properties are then that:

(1) no one can be prevented from computing the winner;
(2) the bids remain secret until the end of the auction.

To express the liveness property (1), we refer to process $A[P]$, with

$$P(t_f) = \mathsf{new}\ bid; Q(bid, t_f)$$

that should verify the formula $\forall \pi.\ \mathsf{Progress} \Rightarrow \varphi$, with:

$$\varphi = \mathsf{G}\ (\forall x, y, d.\ \mathsf{Standby}(\mathsf{commit}(x, y, d))_\pi \Rightarrow \mathsf{F}\ \mathsf{Get}(x)_\pi)$$

Regarding Property (2), similarly as before, we model time limited strong secrecy through a security game where the adversary may spawn a number of (honest) auction participants of their choice, crafts, for each, a pair of bids x_0, x_1, and then attempts to distinguish two hypothetical auctions where the first and second bids are used, respectively. We therefore consider the following processes P_i:

$$P_i(t_f) = \mathsf{in}(x_0);\ \mathsf{in}(x_1);\ Q(x_i, t_f) \qquad i \in \{0, 1\}$$

and model the property by the following formula:

$$\forall \pi_0 : A[P_0].\ \exists \pi_1 : A[P_1].\ \mathsf{F}\ \mathsf{Stop}_{\pi_0} \Rightarrow \pi_0 \sim \pi_1\ \mathsf{U}\ \mathsf{Stop}_{\pi_0}$$

5.4 Fair Contract Signing

To conclude, we present a contract signing protocol [17], where two parties A and B want to sign a common contract Γ. Neither of the parties wants to sign the contract if it is not ensured that the other party will also sign. For that we use a *verifiable-timed signature* (VTS) [59]: this notion is similar to timed commitments, except that whatever is inside the commitment is guaranteed to be a valid signature on a chosen string. The protocol fixes a security time parameter $d = 2^\eta$ (typically $\eta = 128$), and proceeds as follows:

(1) A computes a VTS with time parameter d on Γ and sends it to B, and so does B, sending it to A;
(2) if $d = 1$, or if either party failed to send a valid VTS, force the one obtained at the previous round. Otherwise, repeat the steps with time parameter $d' = \frac{d}{2}$.

One round of the protocol is unfair: whoever signs first can be worse off, as the other may simply force the VTS and not send their own signature. This is compensated by making a force opening prohibitively hard in the first round ($d = 2^\eta$), and then incrementally easing it until reaching $d = 1$. If one party interrupts the protocol at step i, they may obtain a VTS with parameter $d = 2^{\eta - i}$, but the other party obtained a VTS with parameter $2d = 2^{\eta - i + 1}$ during the previous round. In short: one may worse off a party only by a factor of 2 in terms of computation effort. To model this, we use:

$$\mathcal{F} = \mathsf{sign}[x, y, z], \mathsf{pk}[x], \mathsf{VTS}[x, d], \mathsf{verify}[x, y, z], \mathsf{force}[x]$$

$$\mathcal{R} = \mathsf{force}(\mathsf{VTS}(\mathsf{sign}(x, y, z), d)) \xrightarrow{+d} \mathsf{sign}(x, y, z)$$
$$\mathsf{verify}(\mathsf{VTS}(\mathsf{sign}(x, y, z), d), x, \mathsf{pk}(z)) \to \mathsf{ok}$$

The second rewrite rule checks that a given a VTS contains a signature on x by the owner of the public key $\mathsf{pk}(z)$. We use three arguments for the sign function, the second one being a placeholder for randomness. We then model fairness through a security game:

(1) the adversary B engages in protocol rounds with an honest agent A (and completes at least one);
(2) when B computes a signature of A on Γ, say, at time T, they can challenge A: the latter wins the game *iff* it manages to compute B's signature on Γ in time $2T$ starting from the challenge time.

To model this practically, we let $sk_A \in \mathcal{N}$ be a name modelling the signing key of A, and Γ, $sk_B \in \mathcal{F}_0$ be two constant modelling the contract and the signing key of the adversary B. We use internal communications on private channels $e_1, e_2 \in \mathcal{N}$ to model state passing from one round to the other, which enforces in addition that rounds are executed sequentially. The protocol can then be modelled as follows, writing $S_{I,d} = \mathsf{VTS}(\mathsf{sign}(\Gamma, r_I, sk_I), d)$ a term modelling a signature of the agent I on Γ:

$$\begin{aligned}
A &= \mathsf{out}(\mathsf{pk}(sk_A)); (\mathit{Init} \mid \mathit{Chal} \mid !\, R) \\
\mathit{Init} &= \mathsf{in}(d);\ \mathsf{out}(e_1, \tfrac{d}{2}); \\
&\quad \mathsf{new}\ r_A;\ \mathsf{new}\ r_B;\ \mathsf{out}(S_{A,d});\ \mathsf{out}(e_2, S_{B,d}); 0 \\
R &= \mathsf{in}(e_1, d);\ \mathsf{new}\ r_A;\ \mathsf{out}(S_{A,d}); \\
&\quad \mathsf{in}(\mathit{vts});\ \mathsf{if}\ \mathsf{verify}(\mathit{vts}, \Gamma, \mathsf{pk}(\mathsf{sk}_B)) = \mathsf{ok}\ \mathsf{then} \\
&\qquad \mathsf{out}(e_1, \tfrac{d}{2});\ \mathsf{in}(e_2, \mathit{state});\ \mathsf{out}(e_2, \mathit{sig}); 0 \\
\mathit{Chal} &= \mathsf{in}(e_2, \mathit{state}); \mathsf{in}(\mathit{sig}); \\
&\quad \mathsf{event}\ \mathsf{Challenge}(\mathit{check})@T; \\
&\qquad \textit{with check} = \mathsf{verify}(\mathsf{VTS}(\mathit{sig}, 0), \Gamma, \mathsf{pk}(sk_A)) \\
&\quad \mathsf{event}\ \mathsf{Lose}(\mathsf{force}(\mathit{state}))@t\ \mathsf{when}\ t < 3T; 0
\end{aligned}$$

The initial output of $\mathsf{pk}(sk_A)$ reveals the verification key to the adversary, and *Init* encodes a first, mandatory round of the protocol. The process $!\, R$ then models optional rounds: the agent retrieves the current value of the parameter on e_1, sends their VTS and verifies the adversary's, and then updates the internal state on e_2. At any point, the adversary may start to execute *Chal*: they provide at time T the signature *sig* of A, who is then tasked to retrieve the adversary's signature within a $2T$ window, i.e., by time $t = 3T$. Fairness is thus stated as a liveness property: "*whenever the adversary B issues a challenge, A can meet the challenge in time*".

$$\varphi = \mathsf{G}\ (\mathsf{Challenge}_\pi(\mathsf{ok}) \Rightarrow \mathsf{F}\ \exists x.\ \mathsf{Lose}_{\pi'}(x))$$

To avoid the situation of A losing by simply waiting $2T$ units of time idly, the actual security formula includes an assumption of what we call here *active-time progress*:

$$\forall \pi.\ (\mathsf{Progress} \wedge \mathsf{Active}_\pi) \Rightarrow \varphi$$

Here $\mathsf{Active}_\pi = \mathsf{G}\ (\neg \mathsf{stuck} \Rightarrow \mathsf{F}\ \neg \tau_\pi)$ (*time activeness*), namely, a non-stuck process always perform at least one action in the future.

6 DECIDABILITY AND COMPLEXITY

In this section, we study the decidability of properties expressed in our framework, i.e., the following decision problem VERIF:

Input: A signature $\mathcal{F}$, a rewriting system $\mathcal{R}$ and a process P built from them, a Hyper*tidy* CTL* formula φ.
Question: $P \models \varphi$?

This can be seen as the analogue of the model checking problem of a Kripke structure against a standard HyperCTL* formula [26, 35]. This latter problem is known to be decidable in general [26], and even **PSPACE** complete in the non-relational fragments LTL and CTL*, using techniques mostly from automata theory. One may wonder to which extent these results carry out to the more involved *tidy* logics, in particular in regards of results for extensions of temporal logics with first-order quantifiers [41]. However:

PROPOSITION 6.1. *The VERIF problem is undecidable for tidy LTL (and thus tidy CTL*), even for processes without replication.*

Indeed, the undecidability of *deducibility* for convergent rewriting systems is a standard result in (untimed) protocol analysis [2]; said differently, VERIF is undecidable even for inputs of the form:

$$P = \mathsf{out}(u_1); \ldots; \mathsf{out}(u_n); 0 \qquad \varphi = \exists \pi.\, \mathsf{F}\ (\exists X.\, X \vdash_\pi u)$$

Note that this does not rely on time constraints, hence our comparison with untimed logics such as LTL and CTL*. We propose in this section further results that draw a preliminary picture of the possibilities and limits of the problem in terms of decidability. Basics of complexity theory can be found in Appendix B but, as a minimal reminder, we give here common relations between complexity classes, including **PH** (polynomial hierarchy) and **EXPH**(poly) (polynomially-bounded exponential hierarchy):

$$\mathbf{P} \subseteq \mathbf{NP} \subseteq \mathbf{PH} \subseteq \mathbf{PSPACE} \subseteq \mathbf{EXP}$$
$$\mathbf{EXP} \subseteq \mathbf{NEXP} \subseteq \mathbf{EXPH} \subseteq \mathbf{EXPH}(\text{poly}) \subseteq \mathbf{EXPSPACE}$$

6.1 Reduction to Constraint Solving

Despite the overall undecidability, powerful techniques have been developed in protocol analysis to tackle more specific classes of security properties. A popular one for untimed bounded processes is to characterise security as a finite set of adversarial constraints [12, 22, 23]. Examples include the reduction of trace equivalence (expressible in Hyper*tidy* LTL) to the *equivalence of vectors of constraint systems* [22], a subsequent work establishing in a similar way its co**NEXP** completeness for a class of rewriting systems [23]. Another similar result establishes the co**NP** completeness of some safety properties (expressible in *tidy* LTL), by reducing them to the solvability of simpler constraints [12]. In this section, we generalise this set of techniques by exhibiting a natural notion of constraint crisply characterising the decision of arbitrary hyperproperties expressed in Hyper*tidy* CTL*. This provides in particular for the first time such reductions for, e.g., liveness properties.

Our constraints are, intuitively, first order formulae expressing time conditions and computability requirements. They are thus similar to Hyper*tidy* CTL* formulae, but evacuate all considerations related to traces (path variables, "until", actions), and keep track of relations between different attacker computations.

Definition 6.2 (Constraint). The grammar of *constraints* is:

$$\begin{array}{lllll} \Gamma ::= & \forall X.\, \Gamma & \forall x.\, \Gamma & & \textit{term quantifiers} \\ & \Gamma \wedge \Gamma & \neg \Gamma & & \textit{logical operators} \\ & u \vdash_{\Phi@t} v & X \vdash_{\Phi@t} v & b \quad \xi = \zeta & \textit{atomic constraints} \end{array}$$

with u, v terms, $x \in \mathcal{X}, X \in \mathcal{X}^2, t \in T\mathcal{X}$, b time condition, ξ, ζ recipes that may contain second order variables, and Φ dated frame whose domain contains elements of the form $\mathsf{ax}@t$, where $t \in T\mathcal{X}$ instead of $\mathbb{R}^+$. We say that Γ is *valid* when it is satisfied by interpreting $\vdash_{\Phi@t}$ as dated computability, and $=$ as syntactic equality.

The classical approach to reduce security to the validity of constraints is to define a *symbolic semantics* abstracting all sources of unboundedness of the baseline semantics—here, Rules (TIC) and (IN)—by constraints characterising them. We define one in Appendix C for our timed calculus, and formalise its soundness and completeness w.r.t. the regular semantics for bounded processes. Building on it, we obtain the following result (proof in Appendix C):

THEOREM 6.3. *The VERIF problem is decidable for bounded processes with an oracle for testing the validity of constraints.*

Solving the underlying constraints is in particular undecidable in general by Proposition 6.1. A promising direction for future work is therefore to study their solvability for practical, more restricted fragments to obtain decidability results similar to the aforementioned ones, for protocols built on top of timed cryptography.

6.2 Negative Results

As a complement, we now establish complexity lower bounds for VERIF, in order to give a theoretical insight on the difficulty of various instances of the problem. We focus here on notable fragments:

(1) The *pure π-calculus* corresponds to having an empty signature (but we still allow arbitrary event symbols for convenience). Although its interest is limited in terms of security modelling, the resulting framework is closer to standard logics such as HyperCTL*, making their comparative study more insightful.
(2) *Subterm convergence* is a syntactic restriction on rewrite rules $\ell \to r$, stating that r should either be a strict subterm of ℓ, or a term without variables in normal form. All rules presented in examples in this paper are subterm convergent, and several decidability results are known in the untimed case under this assumption [2, 22, 23], making the class relevant to consider.
(3) *Bounded processes* are processes without replication [22, 23]. Replication makes even the pure π-calculus Turing complete, yielding many undecidability results. Yet, the bounded fragment is not trivially decidable (recall Proposition 6.1), and corresponds to protocols with a fixed number of participants.
(4) *Guarded formulae* is a standard syntactic criterion of formulae, required in practical tools such as Tamarin [11]. This intuitively restricts term quantifiers to cases where only a finite number of instances need to be considered to (dis)prove the formula.

As formulae may contain arbitrary term quantifiers $\forall x$ and negation $\neg$, one may easily encode the satisfiability of *Quantified Boolean Formulae*, the prototypical **PSPACE** complete problem, into the verification of even trivial processes. To get more insightful complexity lower bounds, we will hence focus on guarded formulae as they typically prevent this issue. We formalise them as follows.

Definition 6.4 (Guarded quantifier). A Hyper*tidy* CTL* formula

$$\forall \omega_1 \ldots \forall \omega_n.\, \varphi_0 \Rightarrow \varphi$$

is called a *guarded quantifier* if $\vec{\omega} = \omega_1, \ldots, \omega_n$ are regular or second-order variables appearing in φ_0, called a *guard*. We also require that φ_0 does not contain quantifiers over regular or second-order variables, and that all of its atomic formulae occur positively (i.e., are under an even number of negations in φ_0) and are of the form:

- either $x \vdash_\pi z$, with x regular or second-order variable not appearing in $\vec{\omega}$, and z regular variable;
- either $\mathsf{in}(X, Y)_\pi$ or $\mathsf{out}(X, Y)_\pi$, X, Y second-order variables;
- or $\mathsf{Ev}(\vec{x})_\pi$ for some event Ev and (regular) variables $\vec{x}$.

The advantage of guarded quantifiers in terms of decidability is that they ensure that only a finite number of cases need to be considered regarding the instantiation of $\vec{\omega}$ when verifying $\forall \vec{\omega}.\, \varphi_0 \Rightarrow \varphi$, due to the constraints imposed by the guard φ_0. We thus consider:

Definition 6.5 (Guarded formula). A Hyper*tidy* CTL* formula is said to be *guarded* if all of its quantifiers over regular and second-order variables are either guarded, or of the form $\forall X.\, \neg X \vdash_\pi u$.

We stress in particular that $\exists\vec{\omega}.\,\varphi_0 \wedge \varphi = \neg(\forall\vec{\omega}.\,\varphi_0 \implies \neg\varphi)$ is also guarded. As such, all *tidy* CTL* formulae presented throughout this paper can be interpreted as guarded, showcasing that it is a natural specification syntax. Guarded formulae are however unable to express static equivalence (cf Section 4.2, the formula φ_E involves a quantifier alternation). We obtain the following results, proved in Appendices D, E (also valid in the untimed setting). First of all:

Proposition 6.6. *For bounded processes of the pure π-calculus and guarded formulae, the* Verif *problem is co***NP** *hard in tidy LTL, and* ***PSPACE*** *hard in tidy CTL* and Hypertidy LTL.*

Note that, in absence of function symbols, deciding properties of bounded processes mostly amounts to establishing the validity of time conditions. This puts into perspective the following results:

Proposition 6.7. *For bounded processes built from a term algebra with a subterm convergent rewriting system, and for guarded formulae, the* Verif *problem is co***NP** *hard in tidy LTL, and* ***EXPH****(poly) hard in tidy CTL* and Hypertidy LTL.*

Existing results for trace equivalence already imply the hardness of Hyper*tidy* LTL w.r.t. some levels of the exponential or polynomial hierarchies, depending on whether subterm convergent rewriting, or none, is used [24]. We therefore non-trivially extend these lower bounds. Our negative results are summarised by the following table:

rewriting	*tidy* LTL	*tidy* CTL*	Hyper*tidy* LTL/CTL*
any		undecidable	
subterm	co**NP** hard	**EXPH**(poly) hard	**EXPH**(poly) hard
none	co**NP** hard	**PSPACE** hard	**PSPACE** hard

7 AUTOMATED VERIFICATION

We conducted a preliminary evaluation of our timed model with Tamarin, a popular verification tool for untimed protocols [11]. Our approach is based on successive counterexample-guided refinements of an untimed model. Tamarin supports the syntax of the applied π-calculus, but internally uses *multiset rewrite rules* (MSR):

$$[\ \vec{u}\ ] -[\ \vec{v}\ ]\rightarrow [\ \vec{w}\ ]$$

An execution state in Tamarin can be seen as a multiset of terms, and applying the above rule removes the terms $\vec{u}$ from it, adds in addition the terms $\vec{w}$, and labels the operation with the *facts* $\vec{v}$ (similarly to events in our model). Cryptographic primitives can be specified in a term algebra similar to the one we use. Regarding security properties, Tamarin supports among others a first-order logic with explicit timestamps (with a restriction similar to our notion of guarded formulae in Section 6.2). This allows in particular to express most properties of interest built from the "until" operator. Given a *tidy* LTL formula $\forall\pi.\,\varphi$ and a process P (not necessarily bounded), we used the following approach to (dis)prove $P \models \forall\pi.\,\varphi$.

(1) When a symbol $\mathsf{f}[\vec{x}]$ has a non-null cost, we declare it as a *private symbol* in Tamarin, meaning that the adversary cannot use it to build recipes. We then add a MSR:

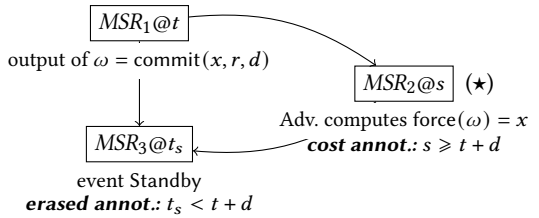
$$[\ \mathsf{in}(\vec{x})\ ] -[\ \mathsf{App\_f}(\vec{x})\ ]\rightarrow [\ \mathsf{out}(\mathsf{f}(\vec{x}))\ ] \qquad (\star)$$

This allows the adversary to apply f arbitrarily by using this rule as a form of oracle that outputs $\mathsf{f}(\vec{x})$ upon reception of $\vec{x}$. This way, all applications of f are materialised in traces by a fact App_f. For simplicity, we modelled all timed cryptography this way, i.e., by making without costs on rewrite rules.

(2) We then erase time annotations from P (timestamps, time conditions). We record this erasure, syntactically, by adding fresh labels on the corresponding MSR (called Timestamp facts in the following). This results in an untimed process $\bar{P}$ whose set of traces *supersedes* that of P. We then verify $\bar{P} \models \forall\pi.\,\varphi$ using Tamarin. Assuming the analysis terminates and conclusive:
   (a) either we get a proof;
   (b) or Tamarin returns a trace violating φ, displayed as a DAG (V, E). Its vertices are MSR instances timestamped by temporal variables, and an edge $(r_1@t_1 \rightarrow r_2@t_2) \in E$ can be seen here as a temporal dependency $t_1 < t_2$.

In case (2a), we conclude that $P \models \forall\pi.\,\varphi$. This is sound for *tidy* LTL formulae, but not in general in presence of arbitrary path quantifiers as in liveness or indistinguishability properties. This was however sufficient to cover several protocols among those presented in Section 5, provided we replace indistinguishability properties by weaker variants based on computability (K formula).

In case (2b), we verify that the attack graph is consistent with the erased time annotations (marked by Timestamp facts) and the computation costs (marked by App_f facts when performed by the adversary). This amounts to a simple SMT solving of these time constraints and of the inequalities induced by the graph edges. For example, when analysing the motivating example, Tamarin finds a potential violation of fairness towards A; that is, a way to obtain A's commitment x before the Standby event. A simplified attack graph is informally pictured below, with time annotations added:



If the overall induced time constraint is satisfiable by an instantiation of the timestamps as real numbers, we conclude that $P \not\models \forall\pi.\,\varphi$. However, in the case of the above attack graph, the constraint is:

$$(t < s < t_s) \wedge (s \geqslant t + d) \wedge (t_s < t + d)$$

which cannot be satisfied by any instantiation of t, t_s, s. This means that the attack graph does not correspond to a valid *timed* trace, and we generate a formula ψ that excludes this inconsistent trace T exactly. It is a straightforward Tamarin formula stating that, if all of the facts in T occur, then at least one inequality induced by the edges of the graph of T should be false. We then add ψ as a *trace restriction*, which intuitively means repeating the verification to prove $\bar{P} \models \forall\pi.\,(\psi \implies \varphi)$. The results we obtained with this method when analysing protocols of Section 5 are collected in Figure 4. They indicate how many times Tamarin was additionally run in our iterative approach to eliminate invalid attacks at Step 2b of the procedure, and how many auxiliary lemmas were added manually to guide Tamarin's solver. All modelling files can be found online:

https://anonymous.4open.science/r/submission_SP2022-E8C2/

	Protocol	Result	# add. rounds	# lemmas
fairness	string sampling	✓	1	1
	sealed-bid auction	✓	2	1
	fair contract signing	✗	–	–
unpredict.	VDF string sampling	✓	1	0
	randomness beacon			
	1-round	✓	1	0
	unbounded	?	1	–

✓ property verified ? timeout ✗ out of scope

Figure 4: Verification of *tidy* LTL properties using Tamarin

Discussion. We currently conducted the steps of our approach manually (model writing, test and exclusion of invalid attacks), although automating these steps would however only require a mostly non-technical engineering effort. The limitation to *tidy* LTL is more significant, excluding for example the contract signing protocol and its only liveness security property. An important direction for future work is hence to study the decidability of constraint solving to verify examples outside the scope of our embedding in Tamarin, by relying on our results in Section 6.1. This notably still remains a challenge even in the untimed case, although decision procedures for specific hyperproperties could be relevant starting points [23]. One could also rely on the support of Tamarin for liveness [7] and indistinguishability; this may however require to discard our counterexample-guided approach in favour of a *static* generation of trace restrictions characterising timed behaviours.

8 RELATED WORK

Timed Cryptographic Primitives. Time-lock puzzles are the timed equivalent of an encryption scheme and have been introduced in the work of Rivest et al. [56]. This primitive has recently received a lot of attention, exploring constructions from new assumptions [14] or with new structural properties, such as homomorphism [52] or identity-based key derivation [19]. The security of this primitive has been recently extended to CCA-security [42], non-malleability [36], and UC-security [13]. Timed commitment is a related primitive [17, 37], recently extended to the setting of verifiable timed signatures [59]. Another related notion is the recently introduced verifiable delay functions [16], that cannot encrypt messages but allow for efficient verification of the computation.

Timed Symbolic Models. There exist several timed models to reason about protocols [5, 10, 30]. However, these models aim to show the physical proximity between participants (*distance bounding*), by reasoning about network delays. These models do not consider timed cryptographic primitives, and are not supported by hyperlogics. One may still mention [54], that proves properties for distance-bounding protocols in Tamarin, recalling our implementation in Section 7. However, in their (simpler) setting not involving timed cryptography, they can characterise time conditions statically, without the need to go over a refinement loop as us. An example closer to ours may be [49], proposing a modelling of timed protocols, based on MSR like Tamarin. The security properties that it can express are however too limited (a finite set of trace properties).

Fairness in Symbolic Models. Fairness has been intensively studied in the context of security protocols [21, 38, 46, 47, 58]. These works often characterise it similarly as us, i.e., as a formula in a temporal logics similar to CTL*. They however build on finite models, which can mean considering only one or two protocol sessions, and/or approximating the power of the adversary by bounding the size of messages, thus often missing attacks. Some more recent work [6, 7] lifts these restrictions by formalising fairness in a variant of the applied π-calculus, which is closer to our contributions. They are however limited to properties written $\forall\pi$, Progress $\Rightarrow \varphi$ in our logic, and to protocols involving a TTP (i.e., no timed cryptography). Such applications can naturally be expressed in our framework.

Hyperproperties. Hyperproperties [27] is a unified framework encompassing classic trace properties, such as safety and liveness, relational properties, including equivalence relations commonly used to model security, and k-properties, which relate k traces of a system. Reasoning about hyperproperties therefore requires rich logics that are able to reason about multiple executions. Such *hyperlogics* include HyperCTL* [26] or its real-time analogue HyperMITL [40], but also many others [29]. Although our logic is inspired from them, there are major differences. First, we have an explicit model of cryptographic primitives, that can be referenced (through terms) in formulae. Second, we express properties about processes executed in an adversarial environment, and formulas can quantify over arbitrary adversarial computations. In particular, deciding whether an atomic formula holds is undecidable in our context without additional assumption—whereas it is a simple boolean test in usual hyperlogics—highlighting a gap in complexity. These logics are supported by a strong theoretical understanding of the limits of the problem in terms of decidability [4, 34, 35, 40, 53]. This includes among others results of decidability for relational properties, including fragments of HyperLTL, despite not carrying in general to *tidy* variants as shown in Section 6. There also exist formalisations of indistinguishability in concurrent process algebras with time, a few examples including [8, 55, 57]. They however only support a fixed set of cryptographic primitives, or not at all (pure π-calculus), and cannot express more refined variants of indistinguishability or hyperproperties in general.

9 CONCLUDING REMARKS

We proposed the first symbolic approach to reason about multi-party computations based on timed cryptographic primitives. We have illustrated its benefits by mechanising several protocols of interest, and by reducing decidability to a notion of constraint solving. As discussed in the corresponding sections, interesting directions for future work include investigating a static characterisation of time conditions as untimed formulas, and studying the notions of constraint solving for deciding hyperproperties.

Another exciting direction is to establish computational soundness, i.e., showing that protocols that can be proved secure in our model are also secure in the computational model. Such results have been popularised in the untimed setting [3, 45], and would be relevant to study w.r.t. standard notions of timed security [43]. A different line of work is to investigate to which extent our model may capture primitives enforcing time/memory tradeoffs [32], or to express consensus based on Nakamoto-style proofs of work.

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A BLOCKCHAIN-BASED ATOMIC SWAP

We model in this section an additional protocol to illustrate the scope of our model, thus complementing Section 5. Although not involving timed cryptography explicitly, its timing behaviour is crisply characterisable by the constrained version of the temporal operator F_I (see Section 4.2), due to assumptions about the reactivity of protocol participants. The protocol is a folklore approach to perform atomic swaps of coins in blockchain-based cryptocurrencies [39, 51]. For this overview, it is sufficient to think of a blockchain as an immutable append-only ledger where a user is associated with an account and can transfer coins to others. In addition, it users A, B to establish contracts of the form:

> $\mathsf{HTLC}(\mathsf{co}, y, d, B, A)$*: if* B *posts a valid* x *such that* $y = \mathsf{h}(x)$ *before time* d*, then transfer the coin* co *to* B*. Otherwise transfer the coin to* A*.*

Here, h is a cryptographic hash function and HTLC stands for *Hash Time-Lock Contract* (the term of "time lock" is however unrelated to the notion of time puzzle studied in this paper). With this functionality in mind, we describe how A and B can swap coins fairly.

(1) A samples a random string r, computes $s = \mathsf{h}(r)$, posts on the blockchain a contract $\mathsf{HTLC}(\mathsf{co}_A, s, 2d, B, A)$, where d is some conservatively chosen time bound (e.g., a day).
(2) Once B verifies that A has posted a contract on the blockchain, they post a contract $\mathsf{HTLC}(\mathsf{co}_B, s, d, A, B)$ for the same s and half the duration of A's time, i.e., d.
(3) Since A knows a valid pre-image r, it can redeem B's coin by posting r on the blockchain before time d. If A does not act, the coin will return to B after time d.
(4) Once r is publicly available on the blockchain, then B can redeem A's coin as well. Note that even if A waits until the very end of the duration of B's contract to act, B still has time $2d - d = d$ to redeem A's coin.

To model the blockchain in our framework, we abstract it by a set of events without apparent structure. We will then consider a security property of the form $\forall \pi, H \Rightarrow \varphi$ where the formula H axiomatises the expected properties of a public ledger. This approach recalls symbolic models of similar protocols [20], although one could argue that our framework, by using explicit time, makes some security implications more natural to express. Typically, the above protocol is unfair to B if B uses a contract of duration $2d$ instead of d, and more complex time-relevant relations arise for increasingly many parties [39]. Concretely, we consider the theory:

$$\mathcal{F}_{\mathsf{c}} = \mathsf{HTLC}/5, \mathsf{h}/1 \qquad \mathcal{F}_{\mathsf{d}} = \pi_i/1, i \in \{2, 5\}$$
$$\pi_i(\mathsf{HTLC}(x_1, x_2, x_3, x_4, x_5)) \longrightarrow x_i$$

The actual processes are then defined below, given $id_A, id_B \in \mathcal{F}_0$ modelling identities. For succinctness, we abstract a time parameter d from the process A, but it could be encoded by an input of a temporal variable as in the multiple examples of Section 5.

$$\begin{aligned}
A = {} & \mathsf{new}\ r;\ \mathsf{new}\ \mathsf{co}_A; \\
& \mathsf{let}\ x = \mathsf{HTLC}(\mathsf{co}_A, \mathsf{h}(r), 2d, id_A, id_B)\ \mathsf{in} \\
& \mathsf{out}(x);\ \mathsf{event}\ \mathsf{Post}(x, id_A); \\
& \mathsf{in}(\gamma);\ \mathsf{event}\ \mathsf{Read}(\gamma, id_A); \\
& \mathsf{if}\ \mathsf{h}(r) = \pi_2(\gamma)\ \mathsf{then} \\
& \mathsf{if}\ id_A = \pi_5(\gamma)\ \mathsf{then} \\
& \mathsf{event}\ \mathsf{Post}(r, id_A); 0
\end{aligned}$$

$$\begin{aligned}
B = {} & \mathsf{new}\ \mathsf{co}_B;\ \mathsf{in}(\gamma); \mathsf{event}\ \mathsf{Read}(\gamma); \\
& \mathsf{let}\ d = \pi_3(\gamma)\ \mathsf{in} \\
& \mathsf{if}\ id_B = \pi_5(\gamma)\ \mathsf{then} \\
& \mathsf{let}\ x = \mathsf{HTLC}(\mathsf{co}_B, \pi_2(\gamma), \tfrac{d}{2}, id_B, id_A)\ \mathsf{in} \\
& \mathsf{out}(x);\ \mathsf{event}\ \mathsf{Post}(x, id_B); \\
& \mathsf{in}(r);\ \mathsf{event}\ \mathsf{Read}(r, id_B);\ \mathsf{event}\ \mathsf{Post}(r, id_B); 0
\end{aligned}$$

$$\begin{aligned}
Trans = {} & !\,\mathsf{in}(x);\ \mathsf{in}(id_1);\ \mathsf{in}(id_2); \\
& \mathsf{event}\ \mathsf{Give}(x, id_1, id_2); 0
\end{aligned}$$

$$Post(id) = !\,\mathsf{in}(x);\ \mathsf{event}\ \mathsf{Post}(x, id); 0$$

The events $\mathsf{Post}(x, id)$ indicate that x is posted by the participant of identity id on the blockchain. When it is posted by the processes A or B (modelling honest agents), a public output is performed as well, modelling the attacker getting access to the ledger. The adversary, on the contrary, has access to the process $Post(id)$ as a form of posting oracle. The event $\mathsf{Read}(x, id)$ then indicates that an arbitrary value on the blockchain is read by agent of identity id (and we will restrict the analysis to traces such that Read events recover the desired posts). Finally, the process *Trans* simply emits events expressing that some coin x is given from one agent to another and, again, the security property will restrain its behaviours. Formally, fairness for A (the formalisation for B is symmetric) can be modelled by the following formula, to be satisfied by the processes $A \mid Trans \mid Post(id_B)$:

$$\forall \pi.\, H_A \Rightarrow \mathsf{F}\ \exists x, id, \mathsf{Give}(x, id, id_A)_\pi$$

where H_A is the conjunction of the formulas below, modelling structural hypotheses on the ledger when A is honest. It typically expresses the properties of time-lock contracts, and that when B appends something on the blockchain, A will eventually read it, and reply, reasonably fast. To this end, we will use this time the interval-constrained temporal operator F_I, with the interval $I = (0, \frac{d}{4})$ (we refer to Section 4.2 for details).

$$\mathsf{G}\ (\forall x.\, \mathsf{Post}(x, id_B)_\pi \Rightarrow \mathsf{F}_I\ \mathsf{Read}(x, id_A)_\pi) \tag{1}$$

$$\mathsf{G}\ (\forall x.\, \mathsf{Read}(x, id_A)_\pi \Rightarrow \mathsf{F}_I\ (\mathsf{stuck} \vee \exists y, \mathsf{Post}(y, id_A)_\pi)) \tag{2}$$

$$\begin{aligned}
& \mathsf{G}\ (\forall x, r, d, id_1, id_2. \\
& \quad \mathsf{Post}(\mathsf{HTLC}(x, \mathsf{h}(r), d, id_1, id_2))_\pi \Rightarrow \varphi \wedge \psi)
\end{aligned} \tag{3}$$

$$\text{with } \varphi = \mathsf{G}_{(0,d)}\ (\mathsf{Post}(r, id_2)_\pi \Rightarrow \mathsf{F}\ \mathsf{Give}(x, id_1, id_2)_\pi)$$
$$\text{and } \psi = (\mathsf{G}_{(0,d)}\ \neg \mathsf{Post}(r, id_2)_\pi) \Rightarrow \mathsf{F}\ \mathsf{Give}(x, id_1, id_1)_\pi$$

B REMINDERS OF COMPLEXITY THEORY

In this section, we recall standard definitions and results of computational complexity theory that we use in several of our contributions.

B.1 Basic Notions

Time and Space. Given $f : \mathbb{N} \to \mathbb{N}$, we define $\mathsf{TIME}(f(n))$ (resp. $\mathsf{SPACE}(f(n))$) to be the class of problems decidable by a deterministic Turing machine running in time (resp. in space) at most $f(n)$ for input tapes of length n. In particular:

- $\mathbf{P} = \bigcup_{p\in\mathbb{N}} \mathsf{TIME}(n^p)$ is the class of problems decidable in polynomial time;
- $\mathbf{PSPACE} = \bigcup_{p\in\mathbb{N}} \mathsf{SPACE}(n^p)$ is the class of problems decidable in polynomial space;
- $\mathbf{EXP} = \bigcup_{p\in\mathbb{N}} \mathsf{TIME}(2^{n^p})$, $\mathbf{EXPSPACE} = \bigcup_{p\in\mathbb{N}} \mathsf{SPACE}(2^{n^p})$ are their exponential analogues.

When considering non-deterministic Turing machines instead of deterministic ones as above, we add a "N" prefix to the name of the class, leading, e.g., to **NP** (non deterministic polynomial time) and **NEXP** (non deterministic exponential time). Given a (non-deterministic) class C, we call co$C$ the class of problems whose negation is in C.

Alternation. We also recall that *alternating Turing machines* are non-deterministic Turing machines whose states are partitioned between *universal states* (or $\forall$-states) and *existential states* (or $\exists$-states). An execution from a universal (resp. existential) state is then accepting *iff* any (resp. at least one) execution from this state accepts. In particular, non-deterministic Turing machines are alternating Turing machines with only existential states, whereas arbitrary alternation of types of states is allowed in general. We add a "A" prefix to express that we consider alternating Turing machines, e.g., **AP** is the class of problems decidable in alternating polynomial time. The following relations between classes are known:

$$\mathbf{P} \subseteq \mathbf{NP}, \text{co}\mathbf{NP} \subseteq \mathbf{PSPACE} = \mathbf{NPSPACE} = \mathbf{AP} \subseteq \mathbf{EXP}$$
$$\mathbf{EXP} \subseteq \mathbf{NEXP}, \text{co}\mathbf{NEXP} \subseteq \mathbf{EXPSPACE} = \mathbf{NEXPSPACE} = \mathbf{AEXP}$$

B.2 Polynomial and Exponential Hierarchies

The difference between **NP** and **PSPACE** = **AP** is the capacity to express alternation, as **NP** is restricted to purely existential states. This is particular visible when studying complete problems for these complexity class. A classical **NP** complete problem is *SAT*: given a boolean formula φ of variables $x_1, \dots, x_n$, does

$$\exists x_1 \dots \exists x_n.\, \varphi$$

hold? On the contrary, the usual complete problem for **PSPACE** is *QBF*: given a boolean formula φ of variables $x_1, y_1, \dots, x_n, y_n$, does

$$\forall x_1.\exists y_1 \dots \forall x_n.\exists y_n.\, \varphi$$

hold? A gradual hierarchy of classes between **NP** and **PSPACE**, the *polynomial hierarchy* **PH**, captures problems whose decision require only a bounded number of $\forall$-$\exists$ state alternations. We consider here in the analogue hierarchy between **NEXP** and **EXPSPACE**, with a distinction between constant and polynomial alternations. For simplicity, we omit the technical definitions of the classes and only provide here their usual characterisation:

Proposition B.1. ***EXPH*** *(resp.* ***EXPH****(poly)), called the exponential hierarchy (resp. polynomially-bounded exponential hierarchy), is the class of problems decidable in exponential time by alternating Turing machines with a constant (resp. polynomial) number of alternations. We have* $\mathbf{EXP} \subseteq \mathbf{EXPH} \subseteq \mathbf{EXPH}(\text{poly}) \subseteq \mathbf{EXPSPACE}$.

B.3 Complete Problems

We use *many-to-one* polynomial-time reductions to characterise complete problems for given complexity classes. We list here those that we will use to obtain our lower bounds in Appendices D and E. They are various problems of propositional logic, that can be all presented in the following general formalism. A *quantified boolean second-order formula* (qbsf) is defined by the following grammar:

$$\begin{array}{rcll} \varphi, \psi & ::= & \varphi \wedge \psi \quad \neg\varphi & \textit{logical operators} \\ & & f^n(\varphi_1, \dots, \varphi_n) & \textit{application} \\ & & \exists f^n.\, \varphi & \textit{quantifier} \end{array}$$

where f^n is called a *boolean function symbol*. Given a mapping $\mathcal{I}$ from the free boolean function symbols of a qbsf φ to actual boolean functions $F : \{0,1\}^n \to \{0,1\}$ of same arity, the *interpretation* of φ is the boolean $[\![\varphi]\!]_{\mathcal{I}} \in \{0,1\}$ inductively defined as:

$$\begin{aligned} [\![\varphi \wedge \psi]\!]_{\mathcal{I}} &= [\![\varphi]\!]_{\mathcal{I}} \cdot [\![\psi]\!]_{\mathcal{I}} \\ [\![\neg\varphi]\!]_{\mathcal{I}} &= 1 - [\![\varphi]\!]_{\mathcal{I}} \\ [\![f^n(\varphi_1, \dots, \varphi_n)]\!]_{\mathcal{I}} &= F([\![\varphi_1]\!]_{\mathcal{I}}, \dots, [\![\varphi_n]\!]_{\mathcal{I}}) \qquad \text{with } F = \mathcal{I}(f^n) \\ [\![\exists f^n.\, \varphi]\!]_{\mathcal{I}} &= \max\left\{ [\![\varphi]\!]_{\mathcal{I} \cup \{f^n \mapsto F\}} \mid F : \{0,1\}^n \to n \right\} \end{aligned}$$

In general, we only consider qbsf whose symbols are all quantified, and therefore write $[\![\varphi]\!]$ instead of $[\![\varphi]\!]_{\emptyset}$. Note that usual constructions of propositional logic can be encoded in this formalism.

- *Boolean variables* are simply boolean function symbols of arity 0. We will write variables $x, y, z, \dots$ for simplicity.
- *Constants* 0 and 1 can be encoded as $x \wedge \neg x$ and $x \vee \neg x$ for some fresh variable x.
- *Logical operators* such as $\varphi \vee \psi = \neg(\neg\varphi \wedge \neg\psi)$, $\varphi \Rightarrow \psi = \neg\varphi \vee \psi$, and $\forall f^n.\, \varphi = \neg(\exists f^n.\, \neg\varphi)$. We assume in the following that qbsf have been put in *negation normal form*, i.e., that negations $\neg$ have been pushed inwards using the above identities.

The classical decision problem *SAT* can be stated as follows:

> Input: A qbsf φ whose quantifiers are all of the form $\exists x.\, \psi$, with x a variable, when put in negation normal form.
> Question: $[\![\varphi]\!] = 1$?

The variation considering not only existentially quantified variables but arbitrary (alternation of) variable quantifiers is called *QBF*:

> Input: A qbsf φ whose quantifiers are all of the form $\exists x.\, \psi$ or $\forall x.\, \psi$, with x a variable, when put in negation normal form.
> Question: $[\![\varphi]\!] = 1$?

Finally, the most general instance is the *QBSF* problem:

> Input: A qbsf φ.
> Question: $[\![\varphi]\!] = 1$?

We summarise the complexity of these problems below. The cases of *SAT* and *QBF* are textbook material, while *QBSF* is taken from [50].

Proposition B.2. *SAT, QBF and QBSF are respectively* ***NP*** *complete,* ***PSPACE*** *complete, and* ***EXPH****(poly) complete.*

$$(\{\!\{\mathsf{in}(u,x)@t \text{ when } b; P\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma \xrightarrow{\mathsf{in}(X,Y)} (\{\!\{P\rho\}\!\} \cup \mathcal{P}, \Phi \cup \{x'@t_0 \mapsto y\}, t_0),\ \Gamma' \quad \text{(s-In)}$$
$$\text{with } x_0, x', y \in \mathcal{X}, X, Y \in \mathcal{X}^2, \rho = \{t \mapsto t_0, x \mapsto x'\},$$
$$\text{and } \Gamma' = \Gamma \wedge b\rho \wedge X \vdash_{\Phi@t_0} x_0 \wedge u \vdash_{\Phi@t_0} x_0 \wedge Y \vdash_{\Phi@t_0} y$$

$$(\{\!\{\mathsf{out}(u,v)@t \text{ when } b; P\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma \xrightarrow{\mathsf{out}(X,\mathsf{ax})} (\{\!\{P\rho\}\!\} \cup \mathcal{P}, \Phi \cup \{\mathsf{ax}@t_0 \mapsto y\}, t_0),\ \Gamma' \quad \text{(s-Out)}$$
$$\text{with } x_0, x', y \in \mathcal{X}, X \in \mathcal{X}^2, \mathsf{ax} \in \mathcal{AX} \setminus dom(\Phi), \rho = \{t \mapsto t_0\},$$
$$\text{and } \Gamma' = \Gamma \wedge b\rho \wedge X \vdash_{\Phi@t_0} x_0 \wedge u \vdash_{\Phi@t_0} x_0 \wedge v \vdash_{\Phi@t_0} y$$

$$(\{\!\{\mathsf{in}(u,x)@t \text{ when } b; P, \mathsf{out}(w,v)@t' \text{ when } b'; Q\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma \xrightarrow{\tau} (\{\!\{P\rho, Q\rho'\}\!\} \cup \mathcal{P}, \Phi \cup \{x'@t_0 \mapsto y\}, t_0),\ \Gamma' \quad \text{(s-Comm)}$$
$$\text{with } x_0, x', y \in \mathcal{X}, \rho = \{t \mapsto t_0, x \mapsto x'\}, \rho' = \{t' \mapsto t_0\},$$
$$\text{and } \Gamma' = \Gamma \wedge b\rho \wedge b\rho' \wedge v \vdash_{\Phi@t_0} y \wedge u \vdash_{\Phi@t_0} x_0 \wedge w \vdash_{\Phi@t_0} x_0$$

$$(\{\!\{\mathsf{event}\ \mathsf{Ev}(u_1, \ldots, u_n)@t \text{ when } b; P\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma \xrightarrow{\mathsf{Ev}(x_1,\ldots,x_n)} (\{\!\{P\rho\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma' \quad \text{(s-Event)}$$
$$\text{with } x_1, \ldots, x_n \in \mathcal{X}, \rho = \{t \mapsto t_0\} \text{ and } \Gamma' = \Gamma \wedge b\rho \wedge \textstyle\bigwedge_{i=1}^{n} u_i \vdash_{\Phi@t_0} x_i$$

$$(\{\!\{\mathsf{new}\ k; P\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma \xrightarrow{\tau} (\{\!\{P\{k \mapsto x\}\}\!\} \cup \mathcal{P}, \Phi \cup \{x@t_0 \mapsto k'\}, t_0),\ \Gamma \quad \text{(s-New)}$$
$$\text{with } x \in \mathcal{X}, k' \in \mathcal{N} \text{ fresh}$$

$$(\mathcal{P}, \Phi, t_0),\ \Gamma \xrightarrow{\tau} (\mathcal{P}, \Phi, t_1), \Gamma \wedge t_0 < t_1 \qquad \text{with } t_1 \in T\mathcal{X} \quad \text{(s-Tic)}$$

$$(\{\!\{P_0 + P_1\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma \xrightarrow{\alpha} (\mathcal{P}', \Phi', t_0),\ \Gamma' \qquad \text{if } \exists i \in \{0,1\}, (\{\!\{P_i\}\!\} \cup \mathcal{P}, \Phi, t_0) \xrightarrow{\alpha} (\mathcal{P}', \Phi', t_0) \text{ and } P_i \notin \mathcal{P}' \quad \text{(s-Choice)}$$

$$(\{\!\{P \mid Q\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma \xrightarrow{\tau} (\{\!\{P, Q\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma \quad \text{(s-Par)}$$

$$(\{\!\{!\,P\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma \xrightarrow{\tau} (\{\!\{!\,P, P\}\!\} \cup \mathcal{P}, \Phi, t_0),\ \Gamma \quad \text{(s-Repl)}$$

Figure 5: Symbolic semantics of the calculus

C REDUCTION TO CONSTRAINT SOLVING

We give in this appendix the proof of Theorem 6.3, that establishes the general decidability of the *Verif* problem provided constraint solving is decidable. The outline of the proof is the following:

(1) we introduce a *symbolic semantics* for our calculus (strongly inspired by analogue semantics in the untimed case [22, 23]). It has the property of being finitely branching, i.e., bounded processes have a finite number of symbolic traces;
(2) we formalise its *soundness* ("all symbolic traces yield a regular trace when the constraints they contain are satisfied") and *completeness* ("all traces can be abstracted by a symbolic trace");
(3) we finally give a *decision procedure* based on all of these properties. Intuitively, the symbolic semantics allows to reduce the path quantifications $\forall\pi$ to the enumeration of a finite number of symbolic traces, while other quantifiers are translated directly inside the constraint.

C.1 A Symbolic Semantics for Finite Traces

An *abstract extended process* A is an extended process that may contain contain free variables (i.e., variables not bound by a prior input instruction) and free temporal variables where a real number is expected (typically, in the global time t in $A = (\mathcal{P}, \Phi, t)$, in time conditions b, or in the domain of frames $x@t$). We write $vars(A)$ the set of free variables of an abstract extended process A. In particular, given a substitution σ with $vars(A) \subseteq dom(\sigma)$, $A\sigma$ is a regular extended process. The symbolic semantics itself then operates on *symbolic processes* A, Γ, A abstract extended process and Γ a conjunction of of atomic constraints. In particular, Γ does not contain quantifiers but may contain free variables, that are implicitly existentially quantified. The semantics itself then takes the form of a labelled transition relation $\xrightarrow{\alpha}_{\mathrm{s}}$ where α is a *symbolic action*, that may be either:

- a symbolic input or output actions $\mathsf{in}(X, Y), \mathsf{out}(X, \mathsf{ax})$ where $X, Y \in \mathcal{X}^2$ are second order variables,
- a silent action τ, or an event $\mathsf{Ev}(\vec{x})$, where $\vec{x}$ are regular variables.

The relation $\xrightarrow{\alpha}_{\mathrm{s}}$ is defined in Figure 5, and essentially follows the same intuition as the regular semantics, except that the condition of rule applications are replaced by constraints stored in Γ. We also use a notation $\overset{w}{\Longrightarrow}_{\mathrm{s}}$ analogue to the regular semantics.

Definition C.1 (Symbolic trace). A *symbolic trace* is a finite sequence of transitions $A_0 \xrightarrow{\alpha_1}_{\mathrm{s}} \cdots \xrightarrow{\alpha_n}_{\mathrm{s}} A_n$, such that:

- either all even transitions (and exactly these) $A_{2i-1} \xrightarrow{\alpha_{2i}}_{\mathrm{s}} A_{2i}$ are (s-Tic) transitions;
- or all odd transitions (and exactly these) $A_{2i} \xrightarrow{\alpha_{2i+1}}_{\mathrm{s}} A_{2i+1}$ are (s-Tic) transitions.

Note that a bounded process P has a finite number of symbolic traces, more precisely a number that is exponential in the size of P. The finiteness is in particular due to the determinism of Rule (s-Tic), and the impossibility to chain multiple instances of this rule without interleaving another rule making the size of the process decrease. We now state the soundness and completeness of the symbolic semantics. It is however only valid for *almost finite* traces, i.e., traces of the regular semantics $A_0 \xrightarrow{\alpha_1} A_1 \xrightarrow{\alpha_2} \cdots$ such that there exists $i \in \mathbb{N}$ such that all transitions $A_j \xrightarrow{\alpha_{j+1}} A_{j+1}$, $j \geqslant i$ are (Tic) transitions. Note that all traces of bounded processes are almost finite in the above sense.

Definition C.2 (Solution). Let Γ be a constraint with free variables, and Σ, σ be two substitutions where Σ maps the free second order variables of Γ to recipes, and σ maps its regular and temporal variables to terms and non-negative real numbers, respectively. We say that (Σ, σ) is a *solution* of Γ if $\Gamma\Sigma\sigma$ is valid. We write $Sol(\Gamma)$ the set of solutions of Γ.

Soundness itself then states that all symbolic traces yield concrete traces when instantiating free variables w.r.t. substitution satisfying the collected constraints.

Proposition C.3 (Soundness). *Let $A, \Gamma \overset{w}{\Longrightarrow}_{\mathsf{s}} A', \Gamma'$ be a symbolic trace and $(\Sigma, \sigma) \in Sol(\Gamma')$. Then for all infinite, temporally structured sequence of (Tic) transitions T_∞, $(A\sigma \overset{w\Sigma\sigma}{\Longrightarrow} A'\sigma) \cdot T_\infty$ is a timed trace.*

Second, completeness states that all concrete traces can be seen as the instance of a symbolic trace.

Proposition C.4 (Completeness). *Let A, Γ be a symbolic process and $(\Sigma, \sigma) \in Sol(\Gamma)$. If $(A\sigma \overset{w}{\Longrightarrow} A') \cdot T_\infty$ is a timed trace, then there exists a symbolic trace $A, \Gamma \overset{w_s}{\Longrightarrow}_{\mathsf{s}} A'_s, \Gamma'$ and $(\Sigma', \sigma') \in Sol(\Gamma')$ such that $\Sigma \subseteq \Sigma'$ (meaning that Σ' extends Σ), $A'_s\sigma = A'$ and $w_s\Sigma = w$.*

They are proved by a quick induction on the length of traces.

C.2 Reduction To Constraint Validity

We now prove Theorem 6.3 that reduces *Verif* to the constraint validity problem. For that we define a function compCstr that takes as arguments a mapping Π from path variables to symbolic traces (of bounded processes), $\pi_0 \in dom(\Pi)$ and a Hyper*tidy* CTL* formula φ, and computes a constraint Γ that is valid, intuitively, when instances of Π satisfy φ. We first define a symbolic analogue of trace suffixes.

Definition C.5 (symbolic suffix). Let $T = A_0 \xrightarrow{\alpha_1}_{\mathsf{s}} \cdots \xrightarrow{\alpha_n}_{\mathsf{s}} A_n$ be a symbolic trace and $t \in \mathcal{TX}$. We write $A_i = (\mathcal{P}_i, \Phi_i, t_i), \Gamma_i$, and $\bar{A}_i = (\mathcal{P}_i, \Phi_i, t), \Gamma_i$. A *symbolic suffix of T timestamped at t* is a pair T', Γ', with T' a (possibly empty) symbolic trace of the form

$$T' : \bar{A}_i \xrightarrow{\alpha_{i+1}}_{\mathsf{s}} A_{i+1} \xrightarrow{\alpha_{i+2}}_{\mathsf{s}} \cdots \xrightarrow{\alpha_n}_{\mathsf{s}} A_n \qquad i \in [\![0, n]\!]$$

and Γ' is the following constraint:

- if $i = n$ then $\Gamma' = t_i < t$;
- if $i \in [\![0, n-1]\!]$ and $A_i \xrightarrow{\alpha_{i+1}}_{\mathsf{s}} A_{i+1}$ is a (s-Tic) transition, then $\Gamma' = t_i < t < t_{i+1}$;
- if $i \in [\![1, n-1]\!]$ and $A_i \xrightarrow{\alpha_{i+1}}_{\mathsf{s}} A_{i+1}$ is not a (s-Tic) transition, then $\Gamma' = (t = t_i)$.

Note that there are finitely (polynomially) many symbolic suffixes. The definition of compCstr is then given in Algorithm 1.

The desired property of the algorithm can be formalised by the proposition below. Let Π be a mapping from path variables to symbolic traces, with $dom(\Pi) = \{\pi_0, \pi_1, \ldots, \pi_n\}$ for some $n \geqslant 0$, and assuming without loss of generality that all $\Pi(\pi_i), \Pi(\pi_j)$ do not share free variables if $i \neq j$. We then call (Σ, σ) a *solution* of Π when $\Sigma = \Sigma_0 \uplus \cdots \uplus \Sigma_n$ and $\sigma = \sigma_1 \uplus \cdots \sigma_n$ with $(\Sigma_i, \sigma_i) \in Sol(\Gamma_i)$ and Γ_i the final constraint of the symbolic trace $\Pi(\pi_i)$. We write $Sol(\Pi)$ the set of solutions of Π. Below, we treat indistinctly almost finite traces and those that may become so when extended with an arbitrary temporally structured sequence of (Tic) transitions.

Proposition C.6. *Let Π be a mapping as above, $\pi_0 \in dom(\Pi)$ and φ be a Hypertidy CTL* formula, potentially with free variables $vars(\varphi)$, but path variables among them are in $dom(\Pi)$, and other variables do not appear in $img(\Pi)$. We also let $(\Sigma, \sigma) \in Sol(\Pi)$, and Σ' (resp. σ') a substitution mapping all second order (resp. regular) free variables of φ to recipes (resp. terms). Then $\Pi(\pi_0)\Sigma\sigma, \Pi\Sigma\sigma \models \varphi\Sigma'\sigma'$ iff $\mathsf{compCstr}(\pi_0, \Pi, \varphi)\Sigma\Sigma'\sigma\sigma'$ is valid.*

Note that, in the statement of the proposition, we implicitly use the soundness of the symbolic semantics (Proposition C.3) when we consider $\Pi(\pi_0)\Sigma\sigma$ as a valid trace. Besides, the assumption that non-path variables of φ do not appear in the symbolic traces is without loss of generality, and is here to ensure that the consecutive applications of substitutions $\Sigma\Sigma'\sigma\sigma'$ are commutative. The proof of the property can then be found below.

Proof. We prove the property by induction on φ. We thus tet Π be a mapping of non-empty domain $dom(\Pi)$ and whose image are symbolic traces of bounded processes. Let $\pi_0 \in dom(\Pi)$, and φ be a Hyper*tidy* CTL* formula such that $vars(\varphi) \cap vars(img(\Pi)) = \emptyset$. We also let $(\Sigma, \sigma) \in Sol(\Pi)$ and (Σ', σ') arbitrary extensions with

$$vars(\varphi) \setminus dom(\Pi) = dom(\Sigma') \cup dom(\sigma') \,.$$

▹ *Case φ atomic formula.* We simply treat one case to give the proof structure, e.g., $\varphi = \mathsf{in}(\xi, \zeta)_\pi$, and all other cases are either straightforward or following a similar approach. We still mention that the case $\varphi = \mathsf{Ev}(\vec{u})$ relies on the convergence assumption on the rewriting system $\mathcal{R}$, i.e., that normal forms are unique.

By hypothesis, $\pi \in dom(\Pi)$, and we thus write $\Pi(\pi) : A \xrightarrow{\alpha}_{\mathsf{s}} B \xrightarrow{w}_{\mathsf{s}} C$. If α is not an input action, the result holds as neither $\mathsf{compCstr}(\pi_0, \Pi, \varphi) = \bot$ is valid, nor $\Pi(\pi_0)\Sigma\sigma, \Pi\Sigma\sigma \models \varphi\Sigma'\sigma'$. Otherwise let us write $\alpha = \mathsf{in}(X, Y)$, and therefore

$$\alpha\Sigma = \mathsf{in}(X\Sigma, Y\Sigma)$$
$$\mathsf{compCstr}(\pi_0, \Pi, \varphi)\Sigma\Sigma'\sigma\sigma' = (X\Sigma = \xi\Sigma' \wedge Y\Sigma = \zeta\Sigma')$$

We thus obtain the expected result.

▹ *Case $\varphi = \forall\omega.\, \psi$, with ω non path variable, or $\varphi = \psi_0 \wedge \psi_1$ or $\varphi = \neg\psi$.* Straightforward reductions to the induction hypothesis(es).

▹ *Case $\varphi = \forall\pi.\, \psi$, with π path variable.* We have to prove that $\Pi(\pi_0)\Sigma\sigma, \Pi\Sigma\sigma \models \forall\pi.\, \psi\Sigma'\sigma'$ *iff* the following constraint is valid:

$$\bigwedge_{i=1}^{p} \forall\vec{\omega}_i.\, \Gamma_i \Rightarrow \mathsf{compCstr}(\pi, \Pi \cup \{\pi \mapsto T_i\}, \psi)$$

where $\{T_1, \ldots, T_p\}$ is the set of symbolic traces of the first symbolic process of $\Pi(\pi_0)$, $\vec{\omega}_i$ is the set of fresh variables introduced by T_i, and Γ_i is the final constraint of T_i. Using the induction hypothesis applied to ψ, the forward direction of this equivalence then simply follows from the soundness of the symbolic semantics, while the converse direction follows from completeness.

▹ *Case $\varphi = \psi_0 \,\mathsf{U}\, \psi_1$.* We refer to the notations of the corresponding case of Algorithm 1 due to their heavy number. Let us assume $\Pi(\pi_0)\Sigma\sigma, \Pi\Sigma\sigma \models \psi_0\Sigma'\sigma' \,\mathsf{U}\, \psi_1\Sigma'\sigma'$, and prove that

$$\mathsf{compCstr}(\pi, \Pi, \psi_0 \,\mathsf{U}\, \psi_1)\Sigma\Sigma'\sigma\sigma'$$

is valid. This is easily obtained by instantiating the variable t_0 by the corresponding $t + \delta \in \mathbb{R}^+$ given by the hypothesis on the satisfaction of $\psi_0 \,\mathsf{U}\, \psi_1$. The converse implication is obtained in the same way, observing that the constraints generated by symbolic suffixes timestamped at t_0 imply all constraints $t_0 > t$, with $(\mathcal{P}, \Phi, t), \Gamma$ initial symbolic process of T_i, $i \in [\![1, n]\!]$. □

Corollary C.7 (Correctness of the reduction). *Let P be a bounded process and φ be a Hypertidy CTL* formula. We consider $\Pi = \{\pi \mapsto \varepsilon_P\}$ where ε_P is the empty symbolic trace starting from $(\{\!|P|\!\}, \emptyset, 0), \top$. Then $P \models \varphi$ iff $\mathsf{compCstr}(\pi, \Pi, \varphi)$ is valid.*

Algorithm 1: Reducing Verification to Constraint Solving

Assumption: all free path variables appearing in φ are in $dom(\Pi)$.

procedure compCstr(π_0, Π, φ)

case $\varphi = \tau_\pi$**:**

if $\Pi(\pi)$ *is empty or of the form* $A \xrightarrow{\tau}_{\mathsf{s}} B \overset{w}{\Longrightarrow}_{\mathsf{s}} C$ **then return** $\top$

else return $\bot$

case $\varphi = \mathsf{in}(\xi, \zeta)_\pi$**:**

if $\Pi(\pi)$ *is of the form* $A \xrightarrow{\mathsf{in}(X,Y)}_{\mathsf{s}} B \overset{w}{\Longrightarrow}_{\mathsf{s}} C$ **then return** $X = \xi \wedge Y = \zeta$

else return $\bot$

case $\varphi = \mathsf{out}(\xi, \zeta)_\pi$**:**

if $\Pi(\pi)$ *is of the form* $A \xrightarrow{\mathsf{out}(X,\mathsf{ax})}_{\mathsf{s}} B \overset{w}{\Longrightarrow}_{\mathsf{s}} C$ **then return** $X = \xi \wedge \mathsf{ax} = \zeta$

else return $\bot$

case $\varphi = \mathsf{Ev}(u_1, \ldots, u_n)_\pi$**:**

if $\Pi(\pi)$ *is of the form* $(\mathcal{P}, \Phi, t), \Gamma \xrightarrow{\mathsf{Ev}(x_1,\ldots,x_n)}_{\mathsf{s}} B \overset{w}{\Longrightarrow}_{\mathsf{s}} C$ **then return** $\bigwedge_{i=1}^{n} u_i \vdash_{\Phi@t} x_i$

else return $\bot$

case $\varphi = \xi \vdash_\pi u$**:**

Let us write $\Pi(\pi) : (\mathcal{P}, \Phi, t), \Gamma \overset{w}{\Longrightarrow}_{\mathsf{s}} A_s$

return $\xi \vdash_{\Phi@t} u$

case $\forall \omega. \psi$ *with* ω *regular or second order variable*:

return $\forall \omega.$ compCstr(π_0, Π, ψ)

case $\varphi = \psi_0 \wedge \psi_1$**:**

return compCstr$(\pi_0, \Pi, \psi_0) \wedge$ compCstr(π_0, Π, ψ_1)

case $\varphi = \neg\psi$**:**

return $\neg$compCstr(π_0, Π, ψ)

case $\varphi = \forall \pi. \psi$**:**

Let us write $\Pi(\pi_0) : A, \Gamma \overset{w}{\Longrightarrow}_{\mathsf{s}} A', \Gamma'$

Let $E = \{T_1, \ldots, T_p\}$ be the set of symbolic traces of A

If $i \in [\![1, p]\!]$, we write $\vec{\omega}_i$ the set of fresh free variables introduced by of T_i, and Γ_i the constraint such that $T_i : A, \Gamma \overset{w_i}{\Longrightarrow}_{\mathsf{s}} A_i, \Gamma_i$

return $\bigwedge_{i=1}^{p} \forall \vec{\omega}_i. \Gamma_i \Rightarrow$ compCstr$(\pi, \Pi \cup \{\pi \mapsto T_i\}, \psi)$

case $\varphi = \psi_0 \mathsf{U} \psi_1$**:**

Let $dom(\Pi) = \{\pi_1, \ldots, \pi_n\}$, with $T_i = \Pi(\pi_i)$

Let $t_0, t_1 \in \mathcal{TX}$ fresh

For each $i \in [\![1, n]\!]$, let E_i^b be the set of symbolic suffixes of T_i timestamped at t_b

return $\exists t_1. \bigvee_{(T_1^1,\Gamma_1^1) \in E_1^1, \ldots, (T_n^1,\Gamma_n^1) \in E_n^1} \left(\bigwedge_{i=1}^{n} \Gamma_i^1 \right) \wedge \Delta_1 \wedge \forall t_0. \bigwedge_{(T_1^0,\Gamma_1^0) \in E_1^0, \ldots, (T_n^0,\Gamma_n^0) \in E_n^0} \left(t_0 < t_1 \wedge \bigwedge_{i=1}^{n} \Gamma_i^0 \right) \Rightarrow \Delta_0$

with $\Delta_b = \text{compCstr}\left(\pi_0, \Pi\left[\pi_i \mapsto T_i^b, i \in [\![1, n]\!]\right], \psi_b\right)$

end

D LOWER BOUNDS IN THE PURE CALCULUS

We prove in this section the complexity lower bounds for bounded processes of the pure π calculus, i.e., when the signature is empty and terms are therefore only constants, names or variables. We recall that the results stated in the body of the paper regarding this fragment are summarised by the proposition:

Proposition D.1. *For bounded processes of the pure π-calculus and guarded formulae, the Verif problem is co**NP** hard in tidy LTL, and **PSPACE** hard in tidy CTL* and Hypertidy LTL.*

D.1 Lower Bound for Non-Relational Logics

We first prove the lower bounds of Proposition 6.6 in the cases of *tidy* LTL and *tidy* CTL*. We first prove the **PSPACE** hardness of *tidy* CTL* by reduction from *QBF*, and the co**NP** hardness of *tidy* LTL will be an easy consequence of the construction. Let thus Ψ be a *QBF* input, and let us construct a bounded process $[\Psi]_P$ (built from an empty signature and rewriting system) and a guarded *tidy* CTL* formula $[\Psi]_\varphi$ such that $[\Psi]_P \models [\Psi]_\varphi$ *iff* $[\![\Psi]\!] = 1$. Without loss of generality, we can assume up to a polynomial preprocessing that Ψ is of the form $\Psi = Q_1 x_1, \dots, Q_n x_n, \Phi$, where $Q_i \in \{\forall, \exists\}$. First of all, we give an encoding of boolean formulae as bounded processes of the pure π calculus. We let $\mathbf{0}, \mathbf{1} \in \mathcal{F}_0$ modelling booleans, and we see a formula Φ as a boolean circuit; intuitively, we model the circuit flow by private communications of $\mathbf{0}, \mathbf{1}$ constants, performed on fresh private channels modelling circuit edges. A gate G of Φ with two input edges e_1, e_2, one output edge e_3, and computing a boolean function $f : \{0,1\}^2 \to \{0,1\}$, is encoded as the process:

$$P_G = \mathsf{in}(e_1, x_1); \mathsf{in}(e_2, x_2); \prod_{b,b' \in \{0,1\}} Q_G^{b,b'}$$

with $Q_G^{b,b'} = \mathsf{event}\ \mathsf{Pos}(x_1, b); \mathsf{event}\ \mathsf{Pos}(x_2, b'); \mathsf{out}(e_3, f(b, b')); 0$

and where e_1, e_2, e_3 are fresh names used here as private channels, and the $\prod$ indexed operator refers to the parallel composition of processes. The Pos event is used for the encoding of conditionals, glossed over in Section 4.2, but detailed here for the sake of rigour. If Φ is a boolean formula (taking m arguments), we thus write

$$x \leftarrow \Phi(u_1, \dots, u_m); P \qquad\qquad u_i \text{ terms}$$

instead of the following process, seeing Φ as a boolean circuit with input edges $e_1, \dots, e_m$, output edge e, and gates $G_1, \dots, G_p$:

$$\prod_{i=1}^{m} \mathsf{out}(e_i, u_i); 0 \mid \prod_{i=1}^{p} P_{G_i} \mid \mathsf{in}(e, x); P$$

We can then define the process of the reduction as:

$$\begin{aligned}[\Psi]_P = \ & \mathsf{in}(x_1); \mathsf{event}\ \mathsf{Qtf}_1; \\ & \vdots \\ & \mathsf{in}(x_n); \mathsf{event}\ \mathsf{Qtf}_n; \\ & x \leftarrow \Phi(x_1, \dots, y_n); \mathsf{event}\ \mathsf{Res}(x); 0\end{aligned}$$

To define the formula $[\Psi]_\varphi$, we introduce an intermediary formula H_π intuitively modelling that the structural assumptions of the process are satisfied in the trace π, i.e., that the event $\mathsf{Pos}(u, v)$ represents an equality test between u and v, and that the input terms x_i need to be booleans. We define it as $H_\pi = H_\pi^1 \wedge H_\pi^2$, where

$$H_\pi^1 = \mathsf{G}\ (\forall x, y.\, \mathsf{Pos}(x, y) \Rightarrow \exists z.\, x \vdash_\pi z \wedge y \vdash_\pi z)$$
$$H_\pi^2 = \mathsf{G}\ (\forall X.\, \mathsf{in}(X)_\pi \Rightarrow X \vdash_\pi \mathbf{0} \vee X \vdash_\pi \mathbf{1})$$

The formula of the reduction can then be defined inductively as $[\Psi]_\varphi = [\Psi]_\varphi^n$, where:

$$\begin{aligned}[\Psi]_\varphi^0 &= \exists \pi.\, \mathsf{F}\ \mathsf{Res}(1)_\pi \\ [\Psi]_\varphi^{i+1} &= \forall \pi.\, H_\pi \Rightarrow \mathsf{G}\ (\mathsf{Qtf}_{i+1} \Rightarrow [\Psi]_\varphi^i) && \text{if } Q_{i+1} = \forall \\ &\quad\ \exists \pi.\, H_\pi \wedge \mathsf{F}\ (\mathsf{Qtf}_{i+1} \wedge [\Psi]_\varphi^i) && \text{if } Q_{i+1} = \exists\end{aligned}$$

A straightforward induction on n then gives the expected correctness property, for the **PSPACE** and the co**NP** reduction:

Proposition D.2 (Correctness of the reduction). *$[\Psi]_\varphi$ is a guarded tidy CTL* formula, and $[\Psi]_P \models [\Psi]_\varphi$ iff $[\![\Psi]\!] = 1$. Besides, if Ψ is a SAT formula, $[\Psi]_P \models \forall \pi.\, H_\pi \Rightarrow \mathsf{G}\ \neg 1_\pi$ iff $[\![\Psi]\!] = 0$.*

D.2 Lower Bound for Relational Logics

To complete the proof of Proposition 6.6, it suffices to prove that *Verif* is also **PSPACE** hard for Hyper*tidy* LTL formulae in our context. We thus let as in Section D.1 a *QBF* formula of the form $\Psi = Q_1 x_1, \dots, Q_n x_n, \Phi$, with $Q_i \in \{\forall, \exists\}$. It then suffices to construct a guarded Hyper*tidy* LTL formula $\langle\Psi\rangle_\varphi$ and a bounded process of the pure π calculus $\langle\Psi\rangle_P$ such that $\langle\Psi\rangle_P \models \langle\Psi\rangle_\varphi$ *iff* $[\![\Psi]\!] = 1$. We define the process similarly as in Section D.1, more precisely:

$$\langle\Psi\rangle_P = \mathsf{event}\ \mathsf{Qtf}_0; [\Psi]_P$$

The formula $\langle\Psi\rangle_\varphi$, we will intuitively define a substantially equivalent formula to $[\Psi]_\varphi$, replacing nested path quantifications by toplevel path quantifications and relations between them. Formally, we consider the following formula, given path variables π and π':

$$\pi \equiv \pi' \ \triangleq\ \forall X.\, \mathsf{in}(X)_\pi \Leftrightarrow \mathsf{in}(X)_{\pi'}$$

It intuitively formalises that π and π' perform the same input (if any) at the current time. We also refer to the formula H_π defined in Section D.1. We define $\langle\Psi\rangle_\varphi = Q_1 \pi_1 \dots Q_n \pi_n . \exists \pi.\, \langle\Psi\rangle_\varphi^1$, where:

$$\begin{aligned}\langle\Psi\rangle_\varphi^{n+1} &= H_\pi \wedge (\pi \equiv \pi_n \mathsf{U}\ \mathsf{Res}(1)_\pi) \\ \langle\Psi\rangle_\varphi^i &= (H_{\pi_i} \wedge (\pi_i \equiv \pi_{i-1} \mathsf{U}\ (\mathsf{Qtf}_{i-1})_{\pi_i})) \Rightarrow \langle\Psi\rangle_\varphi^{i+1} && \text{if } Q_i = \forall \\ \langle\Psi\rangle_\varphi^i &= H_{\pi_i} \wedge (\pi_i \equiv \pi_{i-1} \mathsf{U}\ (\mathsf{Qtf}_{i-1})_{\pi_i}) \wedge \langle\Psi\rangle_\varphi^{i+1} && \text{if } Q_i = \exists\end{aligned}$$

with the convention $\pi_{-1} = \pi_0$. A quick induction on n then gives:

Proposition D.3 (Correctness of the reduction). *$\langle\Psi\rangle_\varphi$ is a guarded Hypertidy LTL formula, and $\langle\Psi\rangle_P \models \langle\Psi\rangle_\varphi$ iff $[\![\Psi]\!] = 1$.*

E LOWER BOUNDS WITH A SUBTERM CONVERGENT REWRITING SYSTEM

We prove in this section the complexity lower bounds for bounded processes of the applied π calculus when the rewriting system is subterm convergent, and the formula is guarded as in our other results. Our reduction only relies on a rudimentary, fixed rewriting system that makes the reduction likely to be applicable to most classes of rewriting systems of interest. We recall that the results stated in the body of the paper regarding this fragment are summarised by the proposition:

Proposition E.1. *For bounded processes built from a term algebra with a subterm convergent rewriting system, and for guarded formulae, the* Verif *problem is co***NP** *hard in tidy LTL, and* ***EXPH****(poly) hard in tidy CTL* and Hypertidy LTL.*

The co**NP** hardness follows from the stronger result in the pure π calculus, proved in Appendix D.1. We therefore only prove the **EXPH**(poly) hardness of *Verif* for *tidy* CTL* and Hyper*tidy* LTL.

E.1 Lower Bound for Non-Relational Logics

We prove here the lower bound for *tidy* CTL* guarded formulae, by reduction from *QBSF*. Let thus Ψ be a qbsf and let us construct (in polynomial time) a subterm convergent rewriting system $\mathcal{R}$, a bounded process $[\Psi]_P$ and a guarded formula $[\Psi]_\varphi$ such that $[\![\Psi]\!] = 1$ *iff* $[\Psi]_P \models [\Psi]_\varphi$. Up to a linear preprocessing on Ψ, we can assume without loss of generality that it is of the form:

$$Q_1 f_1^{n_1}, \ldots, Q_k f_k^{n_k}, \psi$$

where $Q_i \in \{\forall, \exists\}$, $n_i \in \mathcal{N}$, and ψ does not contain quantifiers. Intuitively, the process $[\Psi]_P$ will receive a term u_i from the attacker for each $f_i^{n_i}$, verify that it is a valid encoding of a truth table for a boolean function of arity n_i, and finally compute the corresponding outcome of the boolean formula ψ. Formally, we will encode a truth table for a n_i-ary function f as a binary tree of depth n_i, whose leaf at position $b_0 \ldots b_{n_i}$ is the boolean $f_i(b_0, \ldots, b_{n_i})$. To model binary trees and boolean computations, we consider the following signature $\mathcal{F}$ and rewriting system $\mathcal{R}$, assuming $\mathbf{0}, \mathbf{1}, \mathsf{ok} \in \mathcal{F}_0$:

$$\begin{aligned}
\mathcal{F}:\ & \mathsf{node}/2,\ \mathsf{child}/2,\ \mathsf{test}_{\mathsf{N}}/1,\ \mathsf{test}_{\mathbb{B}}/1, \mathsf{and}/2, \mathsf{neg}/1\\
\mathcal{R}:\ & \mathsf{child}(\mathsf{node}(x,y), \mathbf{0}) \to x\\
& \mathsf{child}(\mathsf{node}(x,y), \mathbf{1}) \to y\\
& \mathsf{test}_{\mathsf{N}}(\mathsf{node}(x,y)) \to \mathsf{ok}\\
& \mathsf{test}_{\mathbb{B}}(b) \to \mathsf{ok} \quad \text{for } b \in \{\mathbf{0}, \mathbf{1}\}\\
& \mathsf{and}(b_1, b_2) \to b_1 \cdot b_2 \quad \text{for } b_1, b_2 \in \{\mathbf{0}, \mathbf{1}\} \text{ interpreted as } 0, 1\\
& \mathsf{neg}(b) \to 1 - b \quad \text{for } b \in \{\mathbf{0}, \mathbf{1}\} \text{ interpreted as } 0, 1
\end{aligned}$$

The test functions are used to verify that a term has a given tree structure. Note also that the rewrite rules for and and neg are effectively subterm convergent, as $b_1 \cdot b_2$ and $1 - b$ refer to the term obtained by interpreting $b_1, b_2, b \in \{\mathbf{0}, \mathbf{1}\}$ as the corresponding integer (0 or 1), resulting in a total of 6 rewrite rules with a constant right-hand side. We define in addition a syntactic sugar for the iterative application of the extraction symbol child. If u is a term and $w \in \{\mathbf{0}, \mathbf{1}\}^*$ is a finite sequence of $\mathbf{0}$'s and $\mathbf{1}$'s, we define $\mathsf{child}^*(u, w)$ to be the term inductively defined as

$$\begin{aligned}
\mathsf{child}^*(u, \varepsilon) &= u\\
\mathsf{child}^*(u, b \cdot w) &= \mathsf{child}^*(\mathsf{child}(u, b), w) \qquad \text{if } b \in \{\mathbf{0}, \mathbf{1}\}
\end{aligned}$$

In particular, we interpret the boolean formula ψ as a term $[\psi]$ of the applied π calculus, using the following inductive translation; it assumes a collection of terms $\vec{u}$ associating a term u_i to each function symbol f_i (intuitively encoding its truth table):

$$\begin{aligned}
[f_i(\psi_1, \ldots, \psi_{n_i})] &= \mathsf{child}^*(u_i, [\psi_1] \cdots [\psi_{n_i}])\\
[\psi_1 \wedge \psi_2] &= \mathsf{and}([\psi_1], [\psi_2])\\
[\neg\psi] &= \mathsf{neg}([\psi])
\end{aligned}$$

The overall structure of the process $[\Psi]_P$ is then outlined in Figure 6 and detailed below. Intuitively, the generation of each truth table u_i of f_i is indicated by an event $\mathsf{Get}_i(u_i)$, the process $\mathit{Verif}_i(u_i)$ verifies that it is well-formed, and the final event $\mathsf{Res}([\psi])$ outputs the result of ψ according to the above translation $[\psi]$.

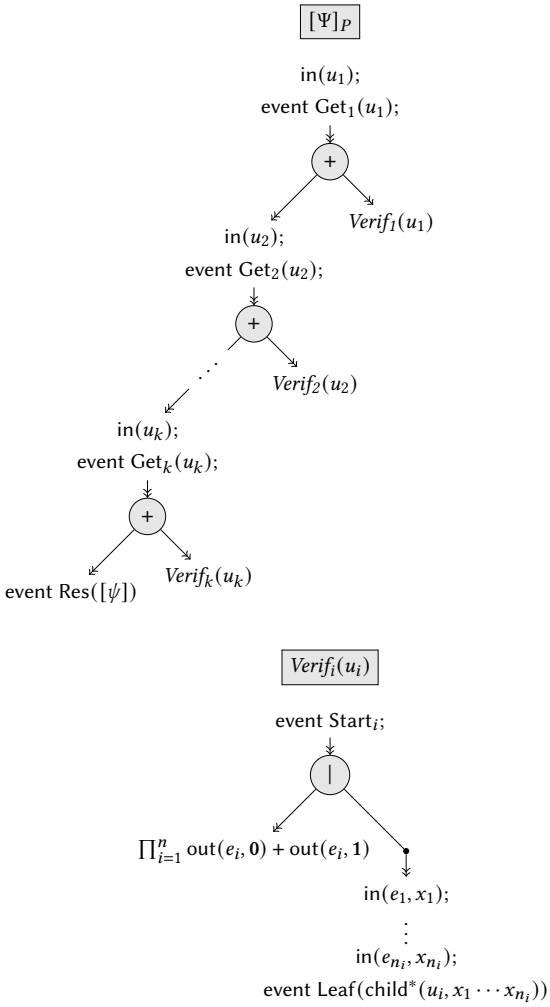

Figure 6: Informal structure of the process $[\Psi]_P$

Formally, we define $[\Psi]_P = [\Psi]_P^1$, where $[\Psi]_P^i$ is defined inductively as follows:

$$\text{if } i \leqslant k,\ [\Psi]_P^i = \mathsf{in}(u_i); \mathsf{event}\ \mathsf{Get}_i(u_i); \left([\Psi]_P^{i+1} + \mathit{Verif}_i(u_i)\right)$$

$$\text{with } \mathit{Verif}_i(u_i) = \mathsf{event}\ \mathsf{Start}_i; (O \mid I)$$

$$\text{with } O = \prod_{i=1}^{n_i} (\mathsf{out}(e_i, \mathbf{0}); 0) + (\mathsf{out}(e_i, \mathbf{1}); 0)$$

$$\text{and } I = \mathsf{in}(e_1, x_1); \ldots; \mathsf{in}(e_{n_i}, x_{n_i}); \mathsf{event}\ \mathsf{Leaf}(\mathsf{child}^*(u_i, x_1 \cdots x_{n_i})); 0$$

with e_i fresh names and, as before, $\prod$ is an indexed operator for parallel composition

$$\text{and finally } [\Psi]_P^{k+1} = \mathsf{event}\ \mathsf{Res}([\psi]); 0$$

The formula $[\Psi]_\varphi$ is then defined as $[\Psi]_\varphi = [\Psi]^1_\varphi$, with:

$$\text{if } i \leqslant k,\ [\Psi]^i_\varphi = Q_i\pi, (\mathsf{G}\ \neg \mathsf{Start}_{i\pi})\ \mathbf{OP}$$
$$\mathsf{G}\ (\forall x.\, \mathsf{Get}_i(x)_\pi \Rightarrow (\varphi^i_{Verif}(x) \Rightarrow [\Psi]^{i+1}_\varphi))$$
$$\textit{with } \mathbf{OP} \textit{ being } \Rightarrow \textit{ if } Q_i = \forall, \textit{ and } \wedge \textit{ if } Q_i = \exists$$

$$\text{with } \varphi^i_{Verif} = \forall\pi.\, (\mathsf{F}\ \mathsf{Start}_{i\pi}) \Rightarrow$$
$$\forall x.\, \mathsf{F}\ \mathsf{Leaf}(x)_\pi \Rightarrow x \vdash_\pi \mathbf{0} \vee x \vdash_\pi \mathbf{1}$$

$$\text{and } [\Psi]^{k+1}_\varphi = \exists\pi.\, \mathsf{F}\ \mathsf{Res}(\mathbf{1})_\pi$$

We then aim at establishing the following result:

Proposition E.2 (Correctness of the reduction). *If Ψ is qbsf, $[\Psi]_\varphi$ is a guarded tidy CTL* formula, and $[\Psi]_P \models [\Psi]_\varphi$ iff $[\![\Psi]\!] = 1$.*

This can be obtained by a straightforward induction on k provided the following two auxiliary lemmas (E.3 and E.4) have been priorly established. The first one formalises the desired correctness property of the $Verif_i$ processes.

Lemma E.3 (Correctness of structure verification). *Let $i \in [\![1, k]\!]$ and consider, for some term u, the process $P(u) = [\Psi]^{i+1}_P + Verif_i(u)$. Then we have $P(u) \models \varphi^i_{Verif}$ iff u is a complete binary tree of depth n_i with boolean leaves, that is, a term such that for all $b_0, \dots, b_{n_i-1} \in \{\mathbf{0}, \mathbf{1}\}$,*

$$\mathsf{child}^*(u, b_0 \cdots b_{n_i-1}) \to^*_{\mathcal{R}} b \in \{\mathbf{0}, \mathbf{1}\}$$

Proof. We recall that the process $Verif_i(u)$ is built only from private communications on fresh private channels $e_1, \dots, e_{n_i}$. For all frames Φ and $t \in \mathbb{R}^+$, all maximal traces of $A = (\{\!|Verif_i(u)|\!\}, \Phi, t_0)$ are therefore of the form:

$$A \xrightarrow{\mathsf{Start}_i \cdot \mathsf{Leaf}(b)} (\mathcal{P}, \Phi, t_1)$$

where $\mathsf{child}^*(u, b_0 \cdots b_{n_i-1}) \vdash_{\Phi@t} b$ for some $b_0, \dots, b_{n-1} \in \{\mathbf{0}, \mathbf{1}\}$ and $t \in]t_0, t_1]$. Conversely, for all $b_0, \dots, b_{n-1} \in \{\mathbf{0}, \mathbf{1}\}$ and $t_1 \geqslant t > t_0$, if $\mathsf{child}^*(u, b_0 \cdots b_{n_i-1}) \to^*_{\mathcal{R}} b$, b in normal form, then there exists a trace of the above form. Let us then prove the two directions of the proposition separately.

($\Rightarrow$) Let u be a term such $P(u) \models \varphi^i_{Verif}$ let also $b_0, \dots, b_{n_i-1} \in \{\mathbf{0}, \mathbf{1}\}$ and let us show that b, the normal form of $\mathsf{child}^*(u, b_0 \cdots b_{n_i-1})$, is either $\mathbf{0}$ or $\mathbf{1}$. For that, it suffices to consider, by the preliminary discussion, a trace π of $P(u)$ of the form $\pi : P(u) \xrightarrow{\mathsf{Start}_i \cdots \mathsf{Leaf}(b)} Q$ and to use the assumption that $P(u) \models \varphi^i_{Verif}$.

($\Leftarrow$) Conversely, let us assume that u is a complete binary tree of depth n_i with boolean leaves, and let us prove that $P(u) \models \varphi^i_{Verif}$. First of all, since the Start_i event does not appear in the process $[\Psi]^{i+1}_P$, we observe that it suffices to prove that

$$Verif_i(u) \models \forall\pi.\forall x.\, \mathsf{F}\ \mathsf{Leaf}(x)_\pi \Rightarrow x \vdash_\pi \mathbf{0} \vee x \vdash_\pi \mathbf{1}$$

Let thus π be a trace of $Verif_i(u)$ verifying the *tidy LTL* formula $\mathsf{F}\ \mathsf{Leaf}(b)$ for some term b in normal form. Since the event Leaf can be executed at most once in $Verif_i(u)$, and since the process $Verif_i(u)$ contains no replication, we can assume without loss of generality that the trace π is maximal. From the preliminary discussion, we therefore get $b_0, \dots, b_{n_i-1} \in \{\mathbf{0}, \mathbf{1}\}$ such that b is the normal form of $\mathsf{child}^*(u, b_0 \cdots b_{n_i-1})$, hence $b \in \{\mathbf{0}, \mathbf{1}\}$ by hypothesis. □

The second intermediary lemma formalises the correctness of the interpretation of quantifier-free qbsf ψ as terms $[\psi]$. For that, we introduce the following notation. Given a boolean function $f : \{0,1\}^n \to \{0,1\}$, we write $[f]$ its encoding as a binary tree of depth n; that is, $[f]$ is the term such that for all $b_0, \dots, b_{n-1} \in \{\mathbf{0}, \mathbf{1}\}$,

(1) $\mathsf{child}^*([f], b_0 \cdots b_{n-1}) \to^*_{\mathcal{R}} \mathbf{0}$ *iff* $f(b_0, \dots, b_{n-1}) = 0$; and
(2) $\mathsf{child}^*([f], b_0 \cdots b_{n-1}) \to^*_{\mathcal{R}} \mathbf{1}$ *iff* $f(b_0, \dots, b_{n-1}) = 1$.

Note that this encoding is a bijection from boolean functions of arity n to complete binary trees of height n with $\mathbf{0}, \mathbf{1}$ leaves. Building on this, we obtain by a quick induction on the structure of ψ:

Lemma E.4 (Correctness of formula interpretation). *Let ψ be a qbsf without quantifier (but potentially with free function symbols), and an interpretation $\mathcal{I}$ mapping each of its function symbols f^n to a boolean function $\mathcal{I}(f) : \{0,1\}^n \to \{0,1\}$. We then consider the translation $[\psi]$ of ψ as a term, parametrised by the collection of terms $\vec{u} = [\mathcal{I}(f_1)], \dots, [\mathcal{I}(f_k)]$. Then we have:*
(1) $[\![\psi]\!]_{\mathcal{I}} = 1$ iff $[\psi] = \mathbf{1}$; and
(2) $[\![\psi]\!]_{\mathcal{I}} = 0$ iff $[\psi] = \mathbf{0}$.

E.2 Lower Bound for Relational Logics

We now present a similar reduction for the Hyper*tidy* LTL fragment. As before, we thus consider a qbsf of the form

$$\Psi = Q_1 f_1^{n_1}, \dots, Q_k f_k^{n_k}, \psi \qquad \psi \text{ quantifier free}$$

and we construct a bounded process $\langle\Psi\rangle_P$ and a Hyper*tidy* LTL formula $\langle\Psi\rangle_\varphi$ such that $\langle\Psi\rangle_P \models \langle\Psi\rangle_\varphi$ *iff* $[\![\Psi]\!] = 1$. We use in particular the same rewriting system $\mathcal{R}$ as for the *tidy* CTL* reduction in Appendix E.1, and refer to previous constructions and notations:

- the processes $Verif_i(u)$ defined in Appendix E.1;
- the interpretation $[\psi]$ of qbsf (without quantifiers but free function symbols) as terms, assuming a term u_i implicitly associated to each function symbol appearing in ψ.

Building on these notations, we define $\langle\Psi\rangle_P = \sum_{i=0}^k P_i$ where:

$$\begin{aligned} P_0 = {} & \mathsf{event}\ \mathsf{Start}_0; \\ & \mathsf{in}(u_1); \mathsf{event}\ \mathsf{Get}_1(u_1); \\ & \vdots \\ & \mathsf{in}(u_k); \mathsf{event}\ \mathsf{Get}_k(u_k); \\ & \mathsf{event}\ \mathsf{Res}([\psi]); 0 \end{aligned}$$

$$P_i = \mathsf{event}\ \mathsf{Start}_i; \mathsf{in}(u_i); \mathsf{event}\ \mathsf{Get}_i(u_i); Verif_i(u_i) \quad \text{if } i \in [\![1, k]\!]$$

We then define the formula $\langle\Psi\rangle_\varphi$. First of all, we write $\pi \equiv \pi'$ to express that the two traces π, π' perform the same Get actions at a given time, i.e., it is a shortcut for the formula:

$$\pi \equiv \pi' \ \triangleq\ \textstyle\bigwedge_{i=1}^k \forall x.\, \mathsf{Get}_i(x)_\pi \Leftrightarrow \mathsf{Get}_i(x)_{\pi'}$$

Then similarly as the notation introduced in Section 4.2, we use extended path quantifiers $\forall\pi : P_i.\, \varphi$ and $\exists\pi : P_i.\, \varphi$ instead of

$$\forall\pi.\, (\mathsf{F}\ \mathsf{Start}_{i\pi}) \Rightarrow \varphi \qquad \text{and} \qquad \exists\pi.\, (\mathsf{F}\ \mathsf{Start}_{i\pi}) \wedge \varphi$$

Although this encoding does not lead a Hyper*tidy* LTL formula in general, it can easily be converted back into one using basic identities of first-order logic. Writing $\overline{\forall} = \exists$ and $\overline{\exists} = \forall$, we define:

$$\langle\Psi\rangle_\varphi = Q_1\pi_1 : P_0, \overline{Q_1}\pi'_1 : P_1 \dots Q_k\pi_k : P_0.\overline{Q_k}\pi'_k : P_k.\exists\pi_{k+1} : P_0.\, \varphi_1$$

where the path-quantifier free formulae φ_i, $i \in [\![1, 2k+1]\!]$ are inductively defined as follows, with the convention $\pi_0 = \pi_1$:

$$\varphi_i = \forall x_i.\, \mathsf{F}\ \mathsf{Get}_i(x_i)_{\pi_i} \Rightarrow (\pi_i \equiv \pi_{i-1} \cup \mathsf{Get}_i(x_i)_{\pi_i}) \Rightarrow \varphi_{i+1} \quad \text{if } i \in [\![1,k]\!], Q_i = \forall$$

$$\varphi_i = \exists x_i.\, \mathsf{F}\ \mathsf{Get}_i(x_i)_{\pi_i} \wedge (\pi_i \equiv \pi_{i-1} \cup \mathsf{Get}_i(x_i)_{\pi_i}) \wedge \varphi_{i+1} \quad \text{if } i \in [\![1,k]\!], Q_i = \exists$$

$$\varphi_{k+i} = (\mathsf{F}\ \mathsf{Get}_i(x_i)_{\pi'_i} \wedge \mathsf{F}\ (\exists b.\, \mathsf{Leaf}(b)_{\pi'_i} \wedge b \not\vdash_{\pi'_i} \mathbf{0} \wedge b \not\vdash_{\pi'_i} \mathbf{1})) \vee \varphi_{k+i+1} \quad \text{if } i \in [\![1,k]\!], Q_i = \forall$$

$$\varphi_{k+i} = (\mathsf{F}\ \mathsf{Get}_i(x_i)_{\pi'_i} \Rightarrow \forall b.\, \mathsf{F}\ \mathsf{Leaf}(b)_{\pi'_i} \Rightarrow (b \vdash_{\pi'_i} \mathbf{0} \vee b \vdash_{\pi'_i} \mathbf{1})) \wedge \varphi_{k+i+1} \quad \text{if } i \in [\![1,k]\!], Q_i = \exists$$

$$\varphi_{2k+1} = \pi_{k+1} \equiv \pi_k \cup \mathsf{Res}(\mathbf{1})_{\pi_{k+1}}$$

It then remains to prove:

Proposition E.5 (Correctness of the reduction). *$\langle\Psi\rangle_\varphi$ is a guarded Hypertidy LTL formula, and $\langle\Psi\rangle_P \models \langle\Psi\rangle_\varphi$ iff $[\![\Psi]\!] = 1$.*

To show this, we first prove the following intermediary result. We say in its statement that a trace is *well-formed* when all of the $\mathsf{Get}_i(u_i)$ events it contains verify that u_i is a complete binary tree of depth n_i with boolean leaves (in the same sense as in Section E.1). By extension, a mapping Π from path variables to traces is said to be well-formed when for all $i \in [\![1,k]\!]$,

- if $\pi_i \in dom(\Pi)$ then $\Pi(\pi_i)$ is a well-formed trace executing an event $\mathsf{Get}_i(u_i)$ for some term u_i;
- if $\pi_i, \pi_{i+1} \in dom(\Pi)$, then $\Pi(\pi_i)$ and $\Pi(\pi_{i+1})$ coincide until the event $\mathsf{Get}_i(u_i)$ included.

We finally consider the suffix of $\langle\Psi\rangle_\varphi$ starting at index $i \in [\![1, k+1]\!]$, written using the following notations:

$$\langle\Psi\rangle^i_\varphi = Q_i\pi_i : P_0.\overline{Q_i}\pi'_i : P_i \ldots Q_k\pi_k : P_0.\overline{Q_k}\pi'_k : P_k.\exists\pi_{k+1} : P_0.\, \varphi_1$$

The auxiliary lemma itself is then stated and proved below, followed by the conclusion of the proof of Proposition E.5:

Lemma E.6. *Let $i \in [\![1,k]\!]$ and Π be a well-formed mapping with $dom(\Pi) = \{\pi_1, \pi'_1, \ldots, \pi_{i-1}, \pi'_{i-1}\}$. Then assuming $Q_i = \forall$ (resp. $Q_i = \exists$), $\Pi \models \langle\Psi\rangle^i_\varphi$ iff for all well-formed traces T_i (resp. there exists a well-formed trace T_i), $\Pi \cup \{\pi_i \mapsto T_i, \pi'_i \mapsto T'_i\} \models \langle\Psi\rangle^{i+1}_\varphi$, where T'_i is an arbitrary trace of P_i.*

Proof. Let us first handle the case $Q_i = \forall$. By definition, $\Pi \models \langle\Psi\rangle^i_\varphi$ *iff* for all traces T_i (well-formed or not),

$$\Pi \cup \{\pi_i \mapsto T_i\} \models \exists \pi'_i : P_i.\, \langle\Psi\rangle^{i+1}_\varphi$$

We rely now on the correctness of the $Verif_i$ process, proved in Appendix E.1, Proposition E.3. In particular, if T_i is well-formed, the left-hand term of the disjunction φ_{k+i} cannot be satisfied, and we can thus indeed let T'_i an arbitrary trace of P_i to obtain the expected result. On the contrary if T_i is not well-formed, then the correctness of $Verif_i$ yields a trace T'_i of P_i satisfying the left-hand term of the disjunction φ_{k+1}, thus resulting again in $\Pi \cup \{\pi_i \mapsto T_i, \pi'_i \mapsto T'_i\} \models \langle\Psi\rangle^{i+1}_\varphi$. We thus obtain the desired conclusion. Suppose now that $Q_i = \exists$. By definition, $\Pi \models \langle\Psi\rangle^i_\varphi$ *iff* there exists a trace T_i such that

$$\Pi \cup \{\pi_i \mapsto T_i\} \models \forall \pi'_i : P_i.\, \langle\Psi\rangle^{i+1}_\varphi$$

However this implies that for all traces T'_i of P_i, the left-hand term of the conjunction φ_{k+i} is satisfied. In particular, again by the correctness of the $Verif_i$ process, i.e., Proposition E.3, we deduce that T_i is well-formed, hence the result. □

To obtain a proof for Proposition E.5, we rely again on the interpretation of boolean formulae f as binary trees $[f]$, as defined in Appendix E.1 and whose correctness w.r.t. the quantifier-free-qbsf interpretation $[\psi]$ is formalised in Lemma E.4. Using this, we can indeed associate to each well-formed mapping Π an interpretation $\mathcal{I}$ of the function symbols of Ψ to boolean functions of adequate arity. Thus Proposition E.5 can be obtained by a quick (increasing) induction on $i \in [\![1, k+1]\!]$, relying on the above discussion for the case $i = k+1$, and Lemma E.6 for inductive steps $i \leqslant k$.