

Distributed Systems

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توصیف رسمی

نمایش ریاضی سیستم

$+, -, *, /$

جبر ☐

$\wedge \vee \neg \Rightarrow \equiv$

منطق ☐

\cap intersection \cup union \subseteq subset \setminus set difference

مجموعه ها ☐

Propositional Logic

\wedge conjunction (and)

\vee disjunction (or)

\neg negation (not)

\Rightarrow implication (implies)

\equiv equivalence (is equivalent to)

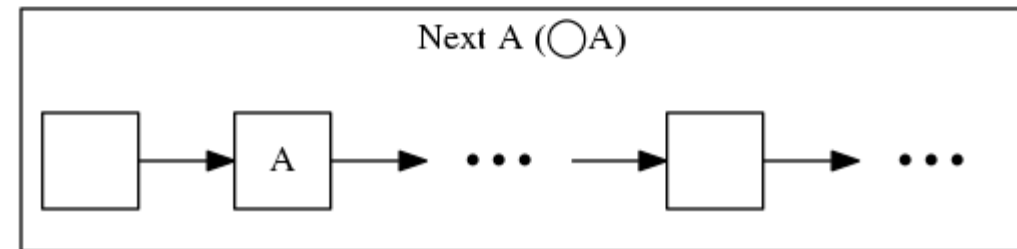
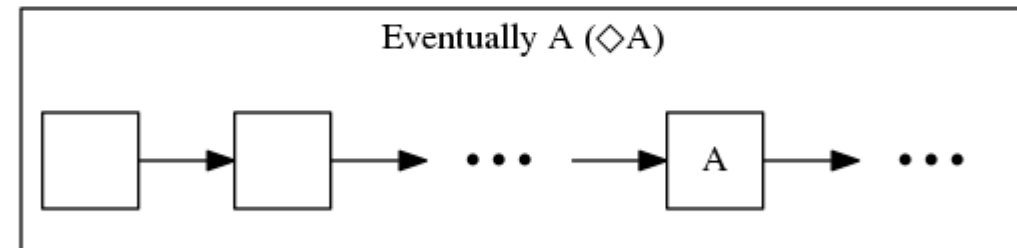
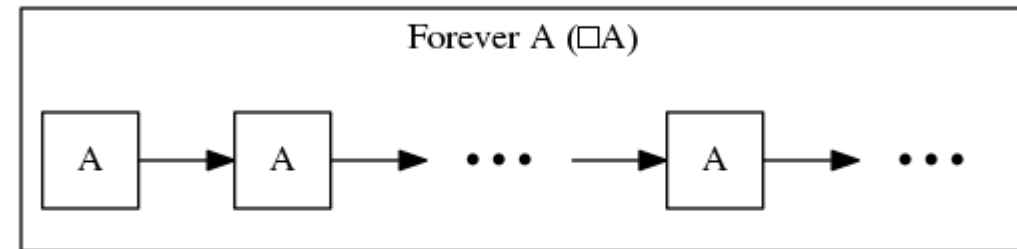
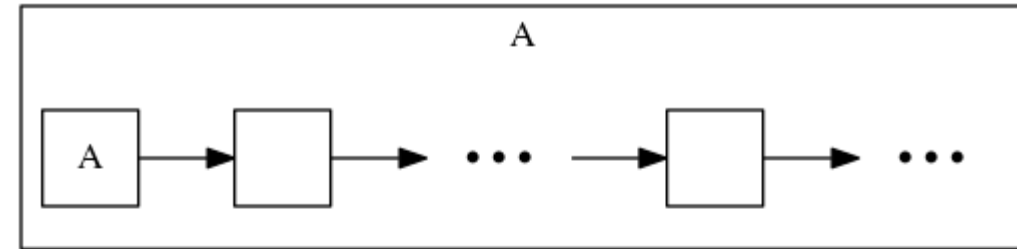
Predicate Logic

Predicate logic extends propositional logic with the two quantifiers:

\forall universal quantification (for all)

\exists existential quantification (there exists)

Temporal Logic



Specifying a simple clock

An hour clock (HC)

A typical behavior of the clock is the **sequence of states**:

$$[hr = 11] \rightarrow [hr = 12] \rightarrow [hr = 1] \rightarrow [hr = 2] \rightarrow \dots$$

$[hr = 11]$ is a state in which the variable hr has the value 11

A pair of successive states, such as $[hr = 1] \rightarrow [hr = 2]$, is called a step.

To specify the hour clock, we describe **all its possible behaviors**.

We write an **initial predicate** that species the possible initial values of hr , and a **next-state** relation that species how the value of hr can change in any step.

... is informal.

$$HCini \triangleq hr \in \{1, \dots, 12\}$$

$$HCnext \triangleq hr' = \text{IF } hr \neq 12 \text{ THEN } hr + 1 \text{ ELSE } 1$$

The temporal formula $\Box F$ asserts that formula F is always true.

In particular, $\Box HCnext$ is the assertion that $HCnext$ is true for every step in the behavior.

Weather station:

$$\begin{aligned} \begin{bmatrix} hr & = & 11 \\ tmp & = & 23.5 \end{bmatrix} &\rightarrow \begin{bmatrix} hr & = & 12 \\ tmp & = & 23.5 \end{bmatrix} \rightarrow \begin{bmatrix} hr & = & 12 \\ tmp & = & 23.4 \end{bmatrix} \rightarrow \\ \begin{bmatrix} hr & = & 12 \\ tmp & = & 23.3 \end{bmatrix} &\rightarrow \begin{bmatrix} hr & = & 1 \\ tmp & = & 23.3 \end{bmatrix} \rightarrow \dots \end{aligned}$$

$$HCini \wedge \Box HCnext$$

$$HCini \wedge \Box (HCnext \vee (hr' = hr))$$

$$HCini \wedge \Box [HCnext]_{hr}$$

TLA+

- Reserved words that appear in small upper-case letters (like EXTENDS) are written in ASCII with ordinary upper-case letters.
- When possible, symbols are represented pictorially in ASCII—for example, \square is typed as [] and \neq as #. (You can also type \neq as /=.)
- When there is no good ASCII representation, T_EX notation is used—for example, \in is typed as \in. The major exception is \triangleq , which is typed as ==.

MODULE *HourClock*

EXTENDS *Naturals*

VARIABLE *hr*

$HCini \triangleq hr \in (1 \dots 12)$

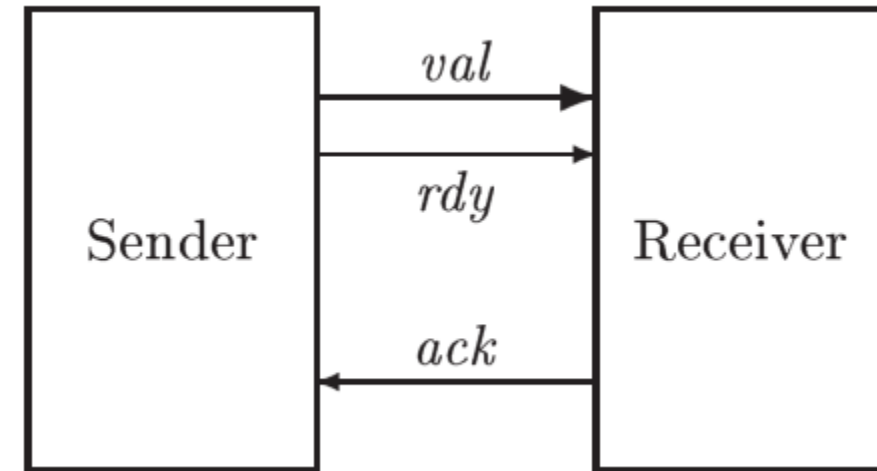
$HCnxt \triangleq hr' = \text{IF } hr \neq 12 \text{ THEN } hr + 1 \text{ ELSE } 1$

$HC \triangleq HCini \wedge \Box[HCnxt]_{hr}$

THEOREM $HC \Rightarrow \Box HCini$

```
----- MODULE HourClock -----  
EXTENDS Naturals  
VARIABLE hr  
HCini == hr \in (1 .. 12)  
HCnxt == hr' = IF hr # 12 THEN hr + 1 ELSE 1  
HC == HCini /\ [] [HCnxt]_hr  
-----  
THEOREM HC => [] HCini  
=====
```

An Asynchronous Interface



$$\begin{array}{l} \left[\begin{array}{l} val = 26 \\ rdy = 0 \\ ack = 0 \end{array} \right] \xrightarrow{\text{Send } 37} \left[\begin{array}{l} val = 37 \\ rdy = 1 \\ ack = 0 \end{array} \right] \xrightarrow{\text{Ack}} \left[\begin{array}{l} val = 37 \\ rdy = 1 \\ ack = 1 \end{array} \right] \xrightarrow{\text{Send } 4} \\ \left[\begin{array}{l} val = 4 \\ rdy = 0 \\ ack = 1 \end{array} \right] \xrightarrow{\text{Ack}} \left[\begin{array}{l} val = 4 \\ rdy = 0 \\ ack = 0 \end{array} \right] \xrightarrow{\text{Send } 19} \left[\begin{array}{l} val = 19 \\ rdy = 1 \\ ack = 0 \end{array} \right] \xrightarrow{\text{Ack}} \dots \end{array}$$

EXTENDS *Naturals*

CONSTANT *Data*

VARIABLES *val, rdy, ack*

$$\begin{aligned} \textit{TypeInvariant} \triangleq & \quad \wedge \textit{val} \in \textit{Data} \\ & \quad \wedge \textit{rdy} \in \{0, 1\} \\ & \quad \wedge \textit{ack} \in \{0, 1\} \end{aligned}$$

$$\begin{aligned} \textit{Init} \triangleq & \quad \wedge \textit{val} \in \textit{Data} \\ & \quad \wedge \textit{rdy} \in \{0, 1\} \\ & \quad \wedge \textit{ack} = \textit{rdy} \end{aligned}$$

$$\begin{aligned} \textit{Send} \triangleq & \quad \wedge \textit{rdy} = \textit{ack} \\ & \quad \wedge \textit{val}' \in \textit{Data} \\ & \quad \wedge \textit{rdy}' = 1 - \textit{rdy} \\ & \quad \wedge \text{UNCHANGED } \textit{ack} \end{aligned}$$

$$\begin{aligned} \textit{Rcv} \triangleq & \quad \wedge \textit{rdy} \neq \textit{ack} \\ & \quad \wedge \textit{ack}' = 1 - \textit{ack} \\ & \quad \wedge \text{UNCHANGED } \langle \textit{val}, \textit{rdy} \rangle \end{aligned}$$

$$\textit{Next} \triangleq \textit{Send} \vee \textit{Rcv}$$

$$\textit{Spec} \triangleq \textit{Init} \wedge \Box[\textit{Next}]_{\langle \textit{val}, \textit{rdy}, \textit{ack} \rangle}$$

THEOREM $\textit{Spec} \Rightarrow \Box \textit{TypeInvariant}$

$$\begin{bmatrix} big & = 0 \\ small & = 0 \end{bmatrix}$$

The big jug is filled from the faucet.

↓

$$\begin{bmatrix} big & = 5 \\ small & = 0 \end{bmatrix}$$

The small jug is filled from the big one.

↓

$$\begin{bmatrix} big & = 2 \\ small & = 3 \end{bmatrix}$$

The small jug is emptied (onto the ground).

↓

$$\begin{bmatrix} big & = 2 \\ small & = 0 \end{bmatrix}$$

A little thought reveals that there are three kinds of steps in a behavior:

- Filling a jug.
- Emptying a jug.
- Pouring from one jug to the other. There are two cases:
 - This empties the first jug.
 - This fills the second jug, possibly leaving water in the first jug.

EXTENDS *Integers*

VARIABLES *big, small*

$Init \triangleq \bigwedge big = 0$
 $\bigwedge small = 0$

$Next \triangleq \bigvee FillSmall$
 $\bigvee FillBig$
 $\bigvee EmptySmall$
 $\bigvee EmptyBig$
 $\bigvee SmallToBig$
 $\bigvee BigToSmall$

$$FillSmall \triangleq small' = 3$$

$$\left[\begin{array}{l} big = 2 \\ small = 1 \end{array} \right] \rightarrow \left[\begin{array}{l} big = 2 \\ small = 3 \end{array} \right]$$

$$\left[\begin{array}{l} big = 2 \\ small = 1 \end{array} \right] \rightarrow \left[\begin{array}{l} big = \sqrt{42} \\ small = 3 \end{array} \right]$$

$$FillSmall \triangleq \begin{array}{l} \wedge small' = 3 \\ \wedge big' = big \end{array}$$

FillBig

$$\begin{aligned} \textit{FillBig} &\triangleq \bigwedge big' = 5 \\ &\quad \bigwedge small' = small \end{aligned}$$

$$\begin{aligned} \textit{EmptySmall} &\triangleq \bigwedge small' = 0 \\ &\quad \bigwedge big' = big \end{aligned}$$

$$\begin{aligned} \textit{EmptyBig} &\triangleq \bigwedge big' = 0 \\ &\quad \bigwedge small' = small \end{aligned}$$

SmallToBig

$$\begin{aligned} \textit{SmallToBig} \triangleq & \quad \vee \wedge \textit{big} + \textit{small} > 5 \\ & \quad \wedge \textit{big}' = 5 \\ & \quad \wedge \textit{small}' = \textit{small} - (5 - \textit{big}) \\ & \vee \wedge \textit{big} + \textit{small} \leq 5 \\ & \quad \wedge \textit{big}' = \textit{big} + \textit{small} \\ & \quad \wedge \textit{small}' = 0 \end{aligned}$$

$$\textit{Min}(m, n) \triangleq \text{IF } m < n \text{ THEN } m \text{ ELSE } n$$

$$\begin{aligned} \textit{SmallToBig} &\triangleq \wedge \textit{big}' = \textit{Min}(\textit{big} + \textit{small}, 5) \\ &\quad \wedge \textit{small}' = \textit{small} - (\textit{Min}(\textit{big} + \textit{small}, 5) - \textit{big}) \end{aligned}$$

$$\begin{aligned} \textit{SmallToBig} &\triangleq \\ \text{LET } \textit{poured} &\triangleq \textit{Min}(\textit{big} + \textit{small}, 5) - \textit{big} \\ \text{IN } \wedge \textit{big}' &= \textit{big} + \textit{poured} \\ &\quad \wedge \textit{small}' = \textit{small} - \textit{poured} \end{aligned}$$

BigToSmall

$$\begin{aligned} \textit{BigToSmall} &\triangleq \\ \text{LET } \textit{poured} &\triangleq \text{Min}(\textit{big} + \textit{small}, 3) - \textit{small} \\ \text{IN } \quad \wedge \textit{big}' &= \textit{big} - \textit{poured} \\ &\quad \wedge \textit{small}' = \textit{small} + \textit{poured} \end{aligned}$$

Invariant:

$$\begin{aligned} TypeOK \triangleq & \bigwedge big \in 0 .. 5 \\ & \bigwedge small \in 0 .. 3 \end{aligned}$$

Leslie Lamport, Specifying Systems, The TLA+ Language and Tools for Hardware and Software Engineers, Addison-Wesley, 2002.